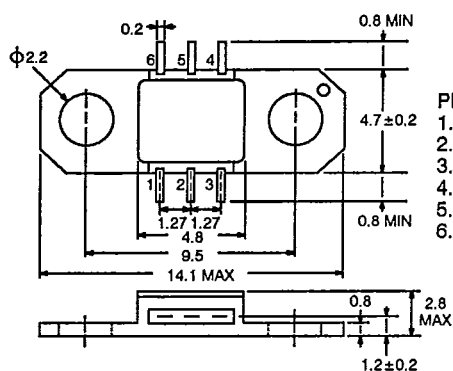


NEC[®]**ULTRA WIDE-BAND AMPLIFIER****UPG102
SERIES****FEATURES**

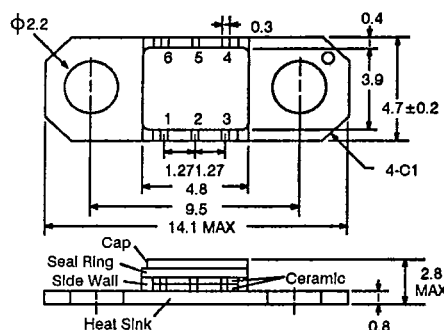
- **ULTRA WIDE-BAND AMPLIFIER**
f = 1 to 20 GHz, G_L = 7 dB TYP
- **CASCADABLE**
- **INPUT/OUTPUT MATCHED TO 50 Ω**
- **HERMETICALLY SEALED PACKAGE ASSURES HIGH RELIABILITY**

DESCRIPTION AND APPLICATIONS

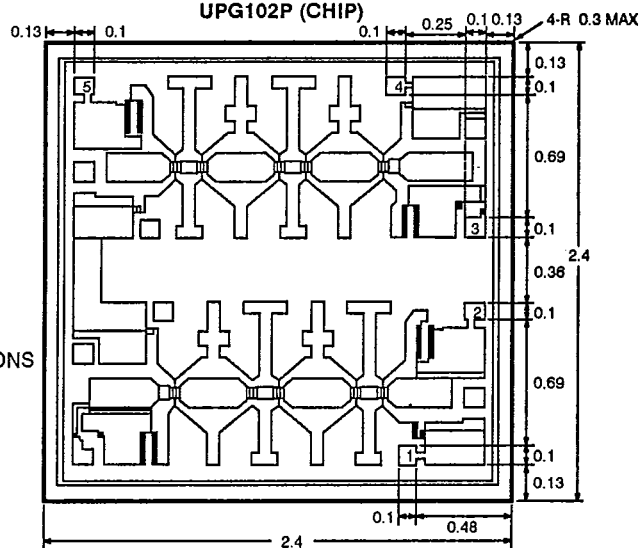
The UPG102 is an ultra wide-band amplifier for applications requiring the widest operating frequency band presently available. Designed for high-rel and commercial applications, it is available in a hermetically sealed ceramic package, leadless carrier and chip form.

OUTLINE DIMENSIONS (Units in mm)**OUTLINE B06****PIN CONNECTIONS**

1. NC
2. OUT
3. VDD1
4. NC
5. IN
6. VDD2

**OUTLINE L06
(Leadless)****PIN CONNECTIONS**

1. NC
2. OUT
3. VDD1
4. NC
5. IN
6. VDD2

UPG102P (CHIP)**PAD CONNECTIONS**

1. IN
2. VDD1
3. GND
4. OUT
5. VDD2

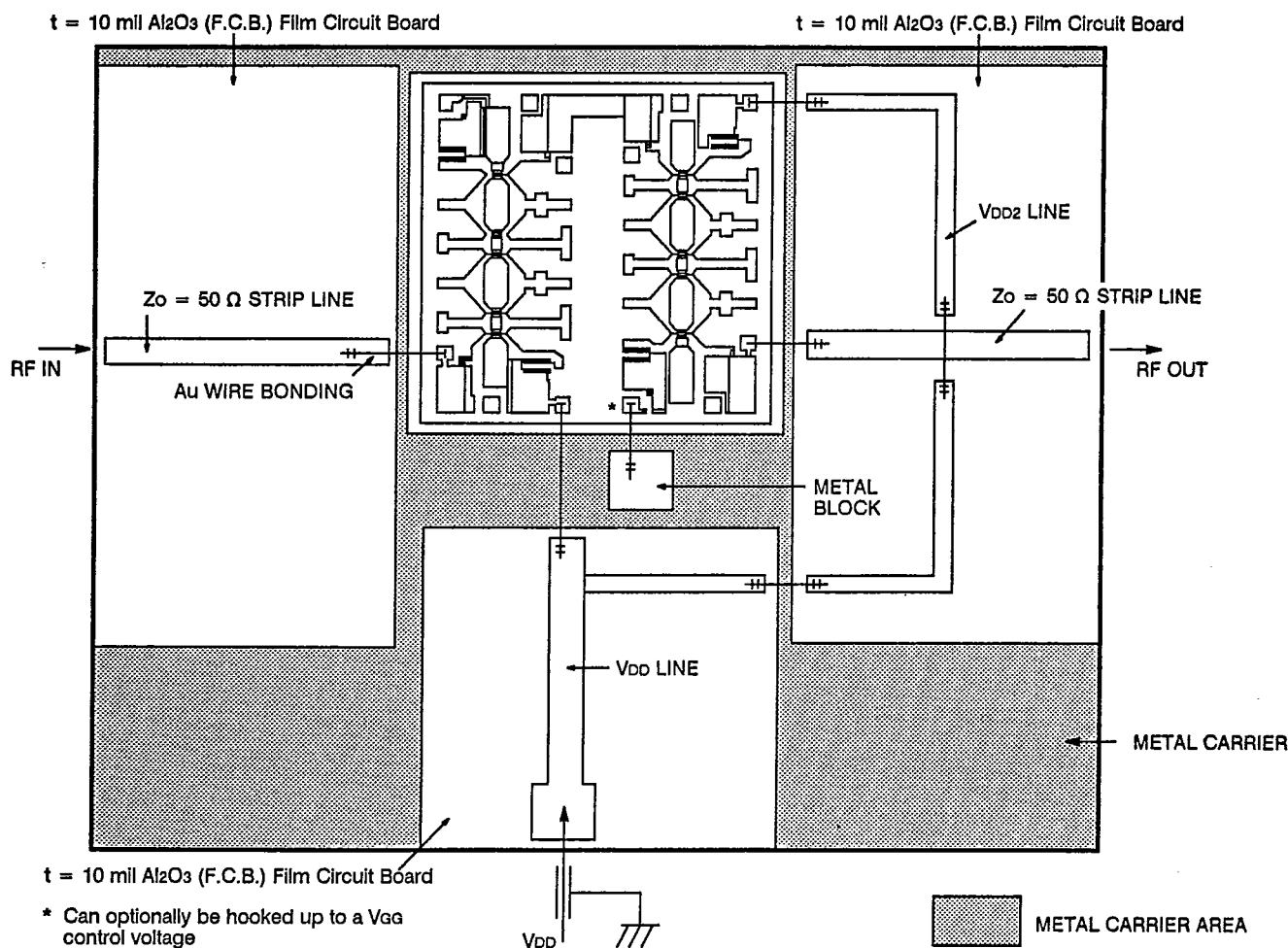
ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VDD1, 2	Drain Voltage	V	+10
P _{IN}	Input Power Level	dBm	+15
P _T *	Power Dissipation	W	1
T _{STG}	Storage Temperature	°C	-65 to +175
T _C (OP)	Case Operating Temperature	°C	-65 to +125

*T_C = 125°C

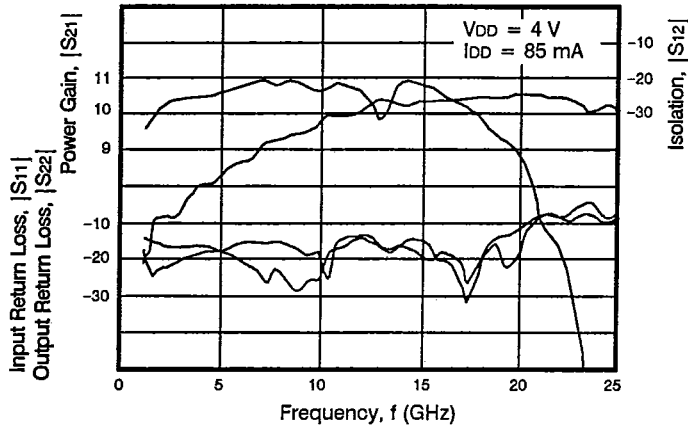
ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

PART NUMBER			UPG102B-1 UPG102K-1 UPG102P-1			UPG102B-2 UPG102K-2 UPG102P-2		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX
I_{DD}	Drain Current, $V_{DD1,2} = 4\text{ V}$	mA	50	70	100	50	70	100
GL	Linear Gain, $V_{DD1,2} = 4\text{ V}$ $f = 18\text{ GHz}$ $f = 20\text{ GHz}$	dB dB	7 6			5 4		
ΔGL	Gain Variation, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }18\text{ GHz}$ $f = 12\text{ to }20\text{ GHz}$ $f = 1\text{ to }20\text{ GHz}$	dB dB dB			± 1.5 ± 1.5 ± 2.0			± 1.5 ± 1.5 ± 2.0
NF	Noise Figure, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }20\text{ GHz}$	dB		8	9		8	9
RL_{IN}	Input Return Loss, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }20\text{ GHz}$	dB	7	10		7	10	
RL_{OUT}	Output Return Loss, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }20\text{ GHz}$	dB	7	10		7	10	
Liso	Input/Output Isolation, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }20\text{ GHz}$	dB	20			20		
P_{1dB}	Output Power at 1 dB Compression, $V_{DD1,2} = 4\text{ V}$, $f = 1\text{ to }20\text{ GHz}$	dBm	7	10		7	10	

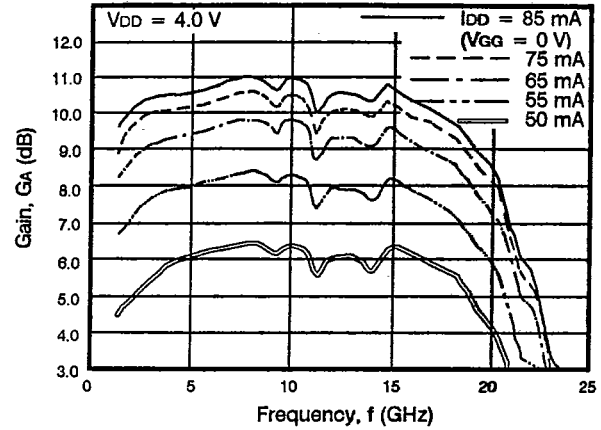
ASSEMBLY EXAMPLE FOR UPG102P

TYPICAL PERFORMANCE CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

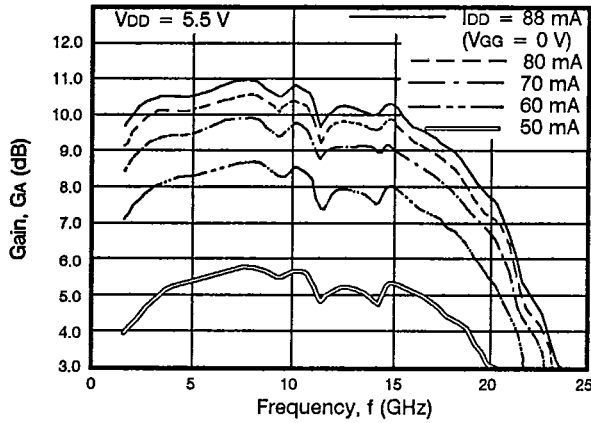
S-PARAMETERS vs. FREQUENCY



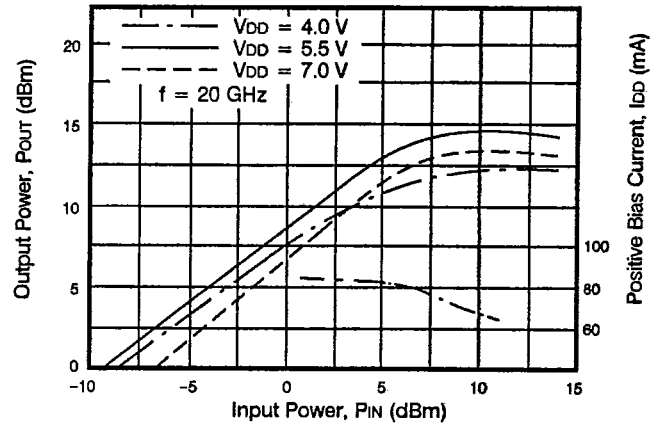
GAIN vs. FREQUENCY FOR CHIP



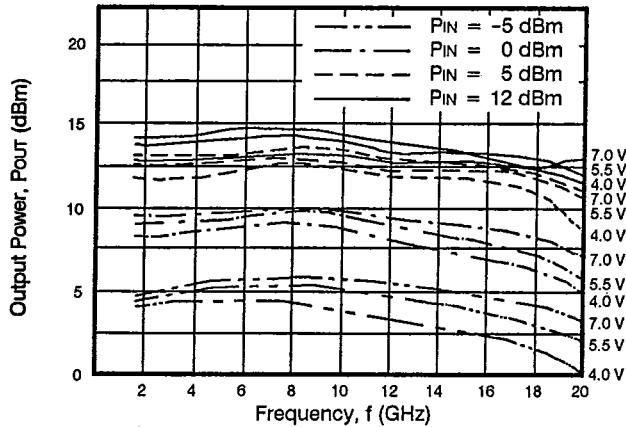
GAIN vs. FREQUENCY FOR CHIP



OUTPUT POWER vs. INPUT POWER



OUTPUT POWER vs. FREQUENCY



GAIN AND INPUT RETURN LOSS vs. FREQUENCY AND TEMPERATURE

