

N-channel TrenchMOS standard level FET Rev. 3 — 21 April 2011

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

AEC Q101 compliant

Low conduction losses due to low on-state resistance

1.3 Applications

Automotive and general purpose power switching

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	100	V
I _D	drain current	T _{mb} = 25 °C	-	-	75	А
P _{tot}	total power dissipation		-	-	300	W
Static char	racteristics					
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C	-	12	15	mΩ
Avalance I	ruggedness					
$E_{DS(AL)S}$	non-repetitive drain-source avalanche energy	$ \begin{split} I_D &= 35 \text{ A}; \ V_{sup} \leq 25 \text{ V}; \\ R_{GS} &= 50 \ \Omega; \ V_{GS} = 10 \text{ V}; \\ T_{j(\text{init})} &= 25 \ ^\circ\text{C}; \ \text{unclamped} \end{split} $	-	-	120	mJ



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2. Pinning information

Table 2.	Pinning	j information				
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	G	gate		-		
2	D	drain	mb			
3	S	source				
mb	D	mounting base; connected to drain		G the mbbo76 S		
			SOT78A (TO-220AB)			

3. Ordering information

Table 3.Ordering information

Type number	Package					
	Name	Description	Version			
BUK7515-100A	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A			

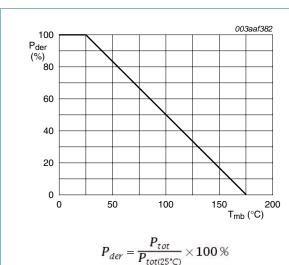
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Limiting values 4.

Limiting values Table 4.

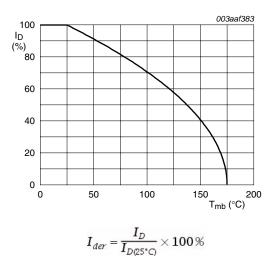
In accordance with the Absolute Maximum Rating System (IEC 60134).

		J			
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	100	V
V _{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	100	V
V _{GS}	gate-source voltage		-20	20	V
I _D	drain current	T _{mb} = 25 °C	-	75	А
		T _{mb} = 100 °C	-	60.8	А
I _{DM}	peak drain current	T _{mb} = 25 °C; pulsed	-	240	А
P _{tot}	total power dissipation	T _{mb} = 25 °C	-	300	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
Source-drai	n diode				
ls	source current	T _{mb} = 25 °C	-	75	А
I _{SM}	peak source current	pulsed; T _{mb} = 25 °C	-	240	А
Avalance ru	Iggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$I_D = 35 \text{ A}; V_{sup} \le 25 \text{ V}; R_{GS} = 50 \Omega;$ $V_{GS} = 10 \text{ V}; T_{j(init)} = 25 \text{ °C}; unclamped$	-	120	mJ





Normalized total power dissipation as a Fig 1. function of mounting base temperature

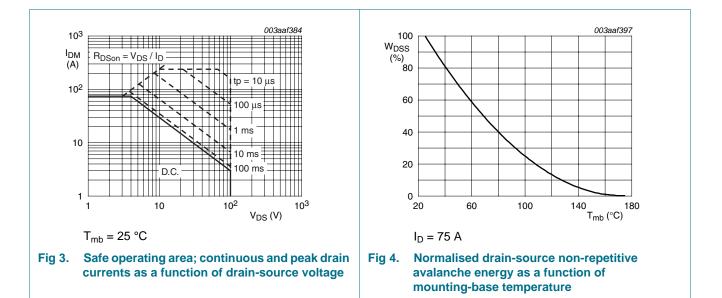




Normalized continuous drain current as a Fig 2. function of mounting base temperature

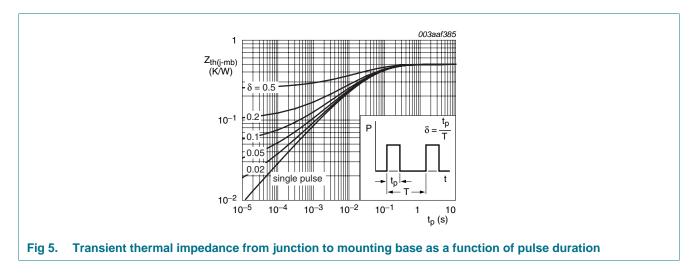
BUK7515-100A

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5. Thermal characteristics

Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base		-	-	0.5	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



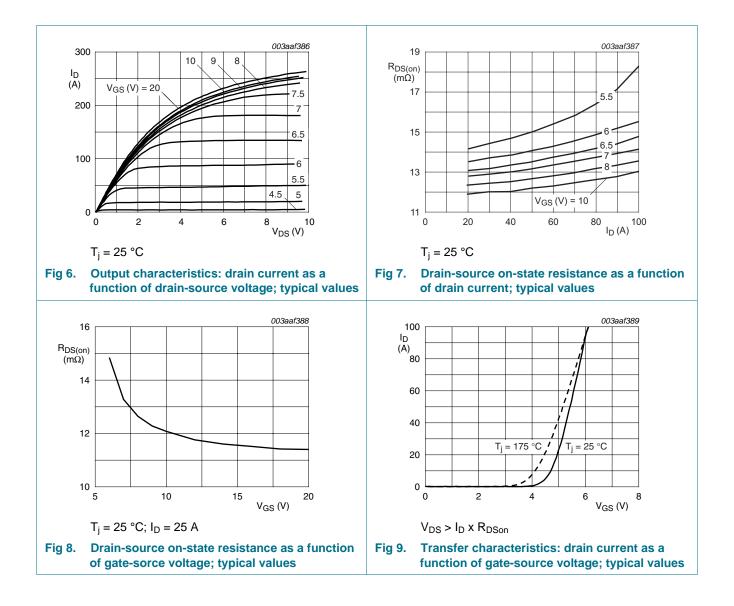
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6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS} drain-source		I _D = 0.25 mA; V _{GS} = 0 V; T _j = 25 °C	100	-	-	V
. ,	breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	89	-	-	V
V _{GS(th)}	gate-source threshold	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 25 °C	2	3	4	V
	voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 175 °C	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C}$	-	-	4.4	V
I _{DSS}	drain leakage current	V _{DS} = 100 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μA
		V_{DS} = 100 V; V_{GS} = 0 V; T_j = 25 °C	-	0.05	10	μA
I _{GSS} gate leakage current		$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon} drain-source on-state		V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C	-	-	40.5	mΩ
	resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C	-	12	15	mΩ
Dynamic cl	naracteristics					
C _{iss}	input capacitance	$V_{GS} = 0 V; V_{DS} = 25 V; f = 1 MHz;$	-	4500	6000	pF
C _{oss}	output capacitance	$T_j = 25 \ ^{\circ}C$	-	550	660	pF
C _{rss}	reverse transfer capacitance		-	305	400	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; R_L = 1.2 Ω ; V_{GS} = 10 V;	-	35	55	ns
t _r	rise time	$R_{G(ext)} = 10 \ \Omega; T_j = 25 \ ^{\circ}C$	-	85	125	ns
t _{d(off)}	turn-off delay time		-	150	225	ns
t _f	fall time		-	70	100	ns
L _D	internal drain inductance	from contact screw on tab to centre of die; $T_j = 25 \ ^{\circ}C$	-	3.5	-	nH
		from drain lead 6 mm from package to centre of die; T _j = 25 °C	-	4.5	-	nH
L _S	internal source inductance	from source lead 6 mm from package to source bond pad; $T_j = 25 \text{ °C}$	-	7.5	-	nH
Source-dra	in diode					
V _{SD}	source-drain voltage	$I_{S} = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_{j} = 25 \text{ °C}$	-	0.85	1.2	V
		$I_{S} = 75 \text{ A}; V_{GS} = 0 \text{ V}; T_{j} = 25 \text{ °C}$	-	1.1	-	V
t _{rr}	reverse recovery time	$I_{S} = 75 \text{ A}; \text{ d}I_{S}/\text{d}t = -100 \text{ A}/\mu\text{s};$	-	80	-	ns
Q _r	recovered charge	V _{GS} = -10 V; V _{DS} = 30 V; T _j = 25 °C	-	0.35	-	μC

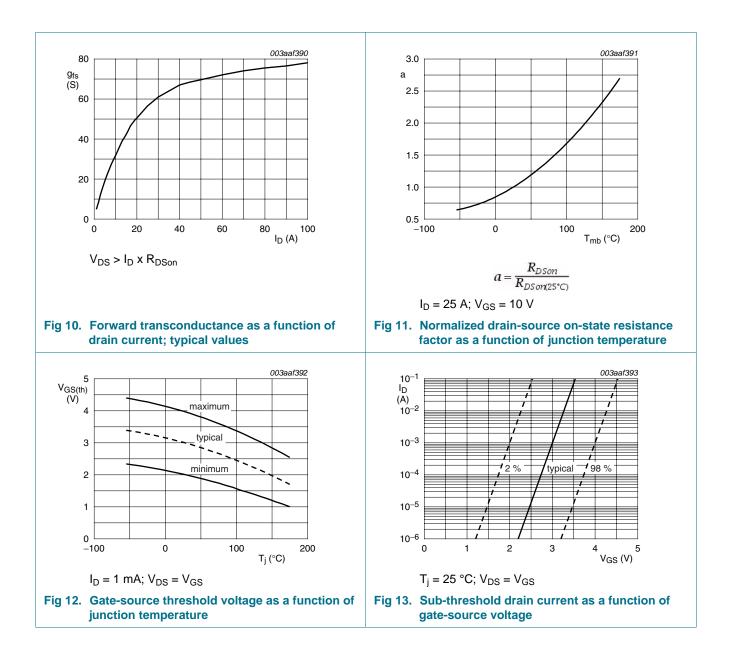
BUK7515-100A Product data sheet

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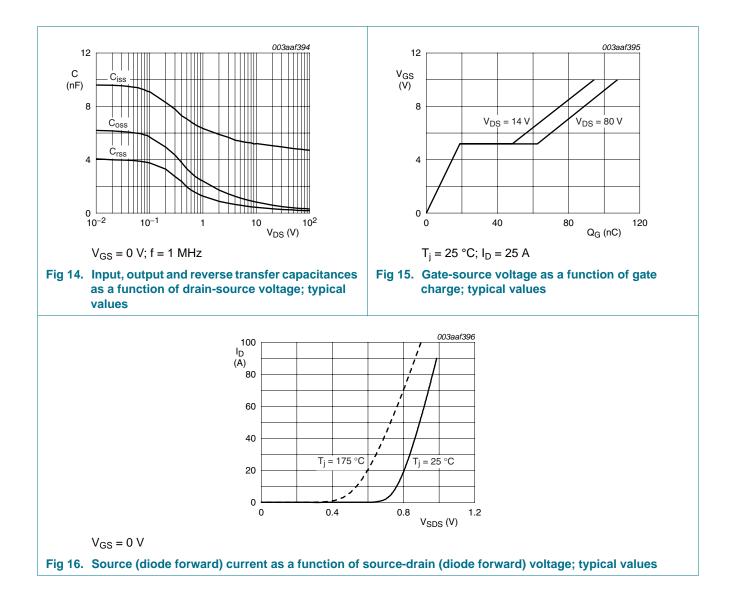
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7. Package outline

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	4.5	1.39	0.9	1.3	0.7	15.8	6.4	10.3		15.0	3.30	max. 3.0	р 3.8	ч 3.0	2.6	-
mm	4.1	1.27	0.6	1.0	0.4	15.2	5.9	9.7	2.54	13.5	2.79	3.0	3.6	2.7	2.2	
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Fig 17. Package outline SOT78A (TO-220AB)

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8. Revision history

Table 7.Revision	history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK7515-100A v.3	20110421	Product data sheet	-	BUK7515-100A_2
Modifications:	of NXP Semic			
	 Legal texts hat 	we been adapted to the new	company name where	appropriate.
BUK7515-100A_2	20030601	Product specification	-	BUK7515-100A_1

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9.1 Data sheet status

Document status [1] [2]	Product status 3	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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