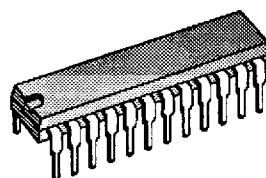


**MULTISTANDARD VIDEO AND SOUND IF SYSTEM
WITH AUDIO AND VIDEO SWITCHES**

- VIDEO PLL DEMODULATION
- SOUND PLL DEMODULATION
- NEGATIVE MODULATION
- AGC FOR ALL STANDARDS
- AUDIO SWITCH
- DC VOLUME CONTROL
- VIDEO SWITCH



SHRINK24
(Plastic Package)

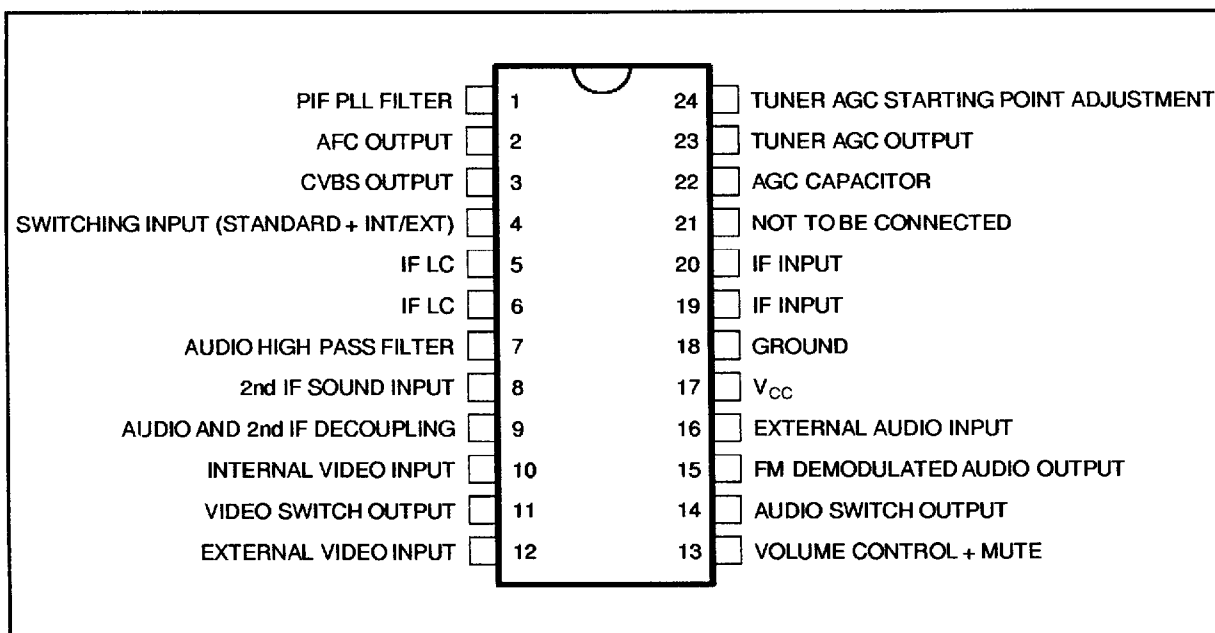
ORDER CODE : STV8223A1

DESCRIPTION

The STV8223A1 is a picture and sound IF processor for negative modulation application with very few external components and adjustments.

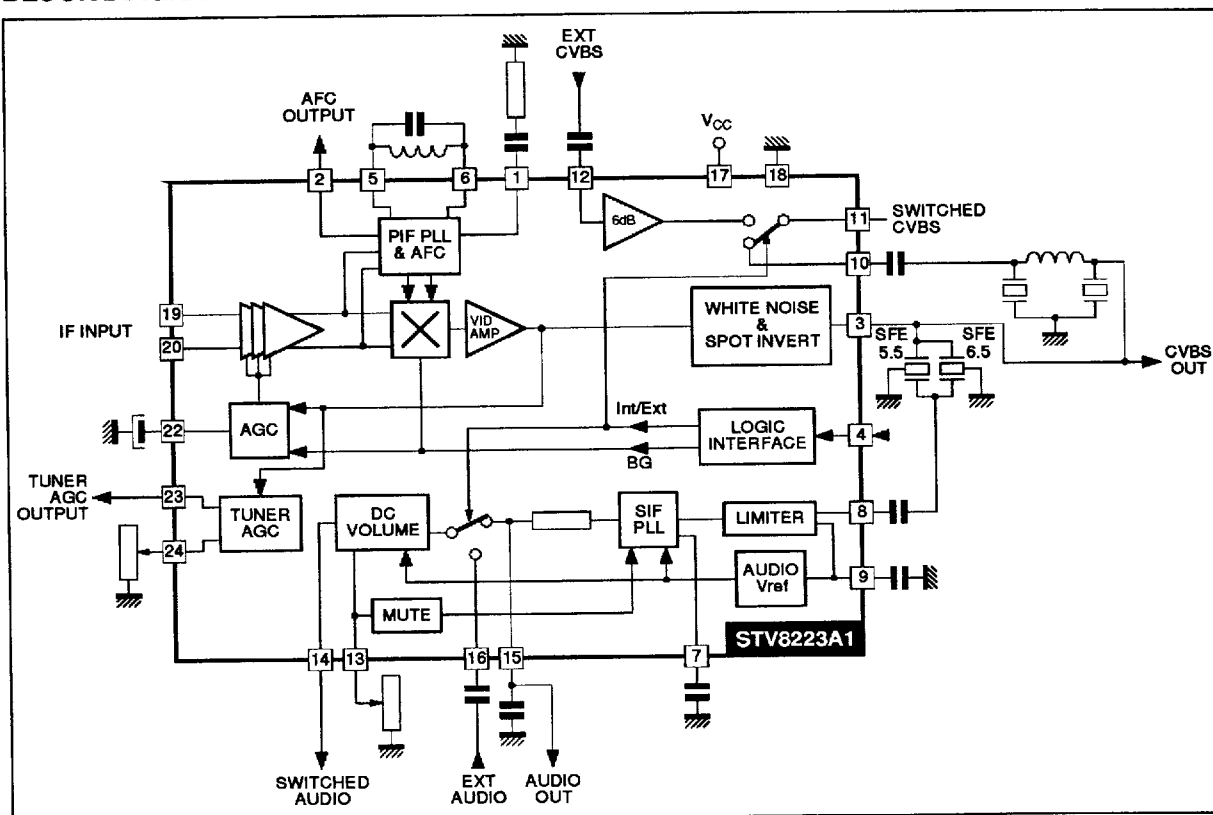
It provides the audio and video switches for one SCART plug application.

PIN CONNECTIONS



8231-01/EP5

BLOCK DIAGRAM



82231-02.EPS

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_s	Supply Voltage	13.5	V
V_x	Tuner AGC Voltage	V_{cc}	V
T_{stg}	Storage Temperature	-40, +150	°C
T_{oper}	Operating Temperature	0, +70	°C

82231-01.TBL

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th(j-a)}$	Junction-ambient Thermal Resistance	Max. 75	°C/W

82231-02.TBL

ELECTRICAL CHARACTERISTICS(T_{amb} = 25°C, V_{CC} = 9V, IF input = 10mV_{RMS} sync level at B/G, Video modulation DSB,D = 90% at B/G, f_{PC} = 38.9MHz, f_{SC} = 33.4MHz, Video BW = 5MHz,Sound carrier input : 5.5MHz, 10mV_{RMS}, f_M = 1kHz, Audio BW = 20kHz, Δf = ±50kHz,

Volume attenuation = 0dB, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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SUPPLY

V _{CC}	Supply Voltage		8	9	12.6	V
I _{CC}	Supply Current	I ₁₇ , V _{CC} = 9V		70	95	mA

IF AMPLIFIER

V ₁₉₋₂₀	Input Sensitivity (RMS)	-3dB Video at Output		70		μV _{RMS}
R ₁₉₋₂₀	Differential Input Resistance			1.5		kΩ
C ₁₉₋₂₀	Differential Input Capacitance			2		pF
Gr	Gain Control Range			64		dB
	Max Input Signal	+1dB Video at Output		110		mV _{RMS}

SYNCHRONOUS VIDEO DEMODULATOR

DF _{PC}	Vision Carrier Capture		-1.4		1.6	MHz
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AFC

S2	AFC Slope	See Figure 19		0.2		μA/kHz
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DEMODULATED VIDEO OUTPUT (Pin 3)

V _{A3}	Amplitude	Top Sync to White	2	2.3	2.6	V _{PP}
V _{S3}	Top Sync Level	B/G	1.6	1.9	2.2	V
	Zero Carrier Level	B/G		4.4		V
BW	Bandwidth	-3dB Video Signal	6	7		MHz
Dg	Differential Gain			6	8	%
Dp	Differential Phase			6	8	Degree
Vr3c	Residual Carrier Signal (RMS Value)			1	10	mV
Vr3h	Residual 2nd Harmonic (RMS Value)			1	10	mV
I ₃	Internal Bias of Emitter Follower		3	5		mA
S/N	Signal to Noise Ratio	Note 1 - Weighted CCIR-567	53	58		dB
	Intermodulation 1.07MHz	Note 2		52		dB
V _{WTH}	White Noise Threshold Voltage			4.85		V
V _{WIL}	White Noise Insertion Level			3.6		V
V _{BTH}	Black Noise Threshold Voltage			1.3		V
V _{BIL}	Black Noise Insertion Level			2.5		V

AGC CIRCUIT (BG MODE)

I _{22CBG}	Charging Current		550	950	1300	μA
I _{22DBG}	Discharge Current		12	20	28	μA
C/D	Charging/Discharging Ratio			45		

TUNER AGC

I ₂₃	Maximum Sinked Current		1.5	2	2.5	mA
S23	Current Slope	R ₂₄ = 5kΩ	100	170	230	μA/dB
I ₂₃₊	Maximum Tuner Plus Sinked Current	Note 3		40		mA

Notes : 1. $\frac{S}{N} = 20 \log 10 \frac{V_{out \text{ black white}}}{V_N (mV_{RMS})}$ at BW = 5MHz

2. Video carrier relative level = 0dB, Chroma subcarrier level = -3.2dB, Sound carrier relative level = -20dB. AGC voltage (Pin 22) is adjusted to get 1V_{PP} signal on output (Pin 3).

3. Additional sinked current for large increasing steps of input signal when :
 - Voltage Pin 22 > starting point defined Pin 24.
 - Output signal (Pin 3) saturated (V₃ < V_{BTH} in BG mode).

ELECTRICAL CHARACTERISTICS (continued)

($T_{amb} = 25^{\circ}\text{C}$, $V_{CC} = 9\text{V}$, IF input = 10mV_{RMS} sync level at B/G, Video modulation DSB,
 $D = 90\%$ at B/G, $f_{PC} = 38.9\text{MHz}$, $f_{SC} = 33.4\text{MHz}$, Video BW = 5MHz ,
 Sound carrier input : 5.5MHz , 10mV_{RMS} , $f_M = 1\text{kHz}$, Audio BW = 20kHz , $\Delta f = \pm 50\text{kHz}$,
 Volume attenuation = 0dB , unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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FM SOUND DEMODULATION

V_{8S}	Input Sensitivity	-3dB FM detected signal		35		μV
R_8	Limiter Input Resistance			600		Ω
	DC Voltage (Pin 8)			4.2		V
AMR	Amplitude Modulation Rejection	Note 4	50	61		dB
SVR	Supply Voltage Rejection Ratio	Ripple signal : 100Hz , 0.5V_{PP}	12	17		dB
V_{15}	Detected Audio Output Signal		0.85	1.1	1.4	V_{RMS}
THD	Total Harmonic Distortion			0.2	1	%
R_{15}	Internal Deemphasis Resistor		600	750	900	Ω
S/N	Signal to Noise Ratio	Note 5, Weighted CCIR 468-4 (quasi peak level)	50	55		dB

VOLUME CONTROL

V_C Range	Control Range	See Figure 21	72	77		dB
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AUDIO SWITCH

R_{16}	Input Resistance		45	60	75	$\text{k}\Omega$
CR_{1k}	Crosstalk		70	80		dB
E_n	Output Noise Level (Pin 14)	Weighted CCIR 468-4, $V_{13} = 0.5\text{V}$ (quasi peak level)		120		μV
EXTHD	THD on External Signal (Pin 14)	$V_{IN} = 2\text{V}_{RMS}$, Attenuation = 0dB		0.1	0.3	%
	Audio Reference Voltage (Pin 9)			5		V

VIDEO SWITCH

V_{DC12}	DC Input Level	No signal	1.6	1.9	2.2	V
V_{S12}	Top Sync. Clamp Level			1.8		V
V_{11}	DC Output Level	No signal	1.7	2	2.3	V
V_{S11}	Top Sync. Clamp Level			1.5		V
	Crosstalk			55		dB
GEX	Gain from Ext. Input to Output		5.5	6	6.5	dB
	Output Swing		4	5		V
I_{12}	Input Current	$V_{12} = V_{DC12} = 1.5\text{V}$		1	5	μA
VBW	Bandwidth	$V_{IN} = 1\text{V}_{PP}$		15		MHz
G_{IN}	Gain from Int. Input to Output		-0.5	0	+0.5	dB

MUTE (Pin 13)

V_{TH13}	Threshold Voltage Pin 13		0.2	0.3	0.4	V
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CONTROL INPUT

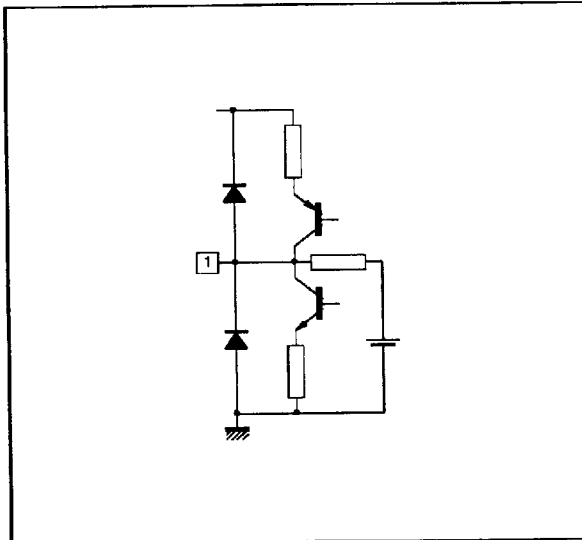
	Negative Modulation	Video : External - Audio : External	7.2			V
	Negative Modulation	Video : Internal - Audio : Internal			1.8	V

Notes : 4. $AMR = 20 \log \frac{V_{15} (\text{mV}_{RMS})}{V_{AM}}$ (dB) where V_{AM} = output amplitude in AM for $f_M = 1\text{kHz}$ and $m = 30\%$

$$5. \frac{S}{N} = 20 \log \frac{V_{15} (\text{mV}_{RMS})}{V_N (\text{mV}_{RMS})} \text{ (dB)}$$

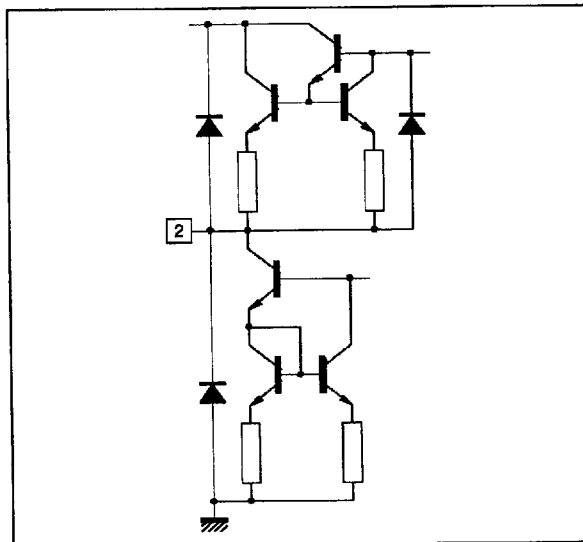
INPUT/OUTPUT PIN CONFIGURATION

Figure 1 : PIF PLL Filter



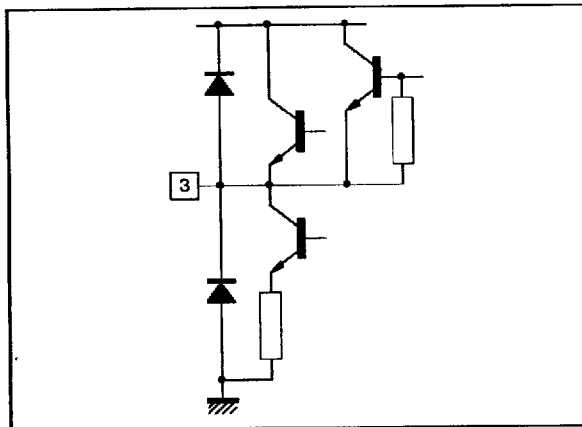
82231-03.EPS

Figure 2 : AFC Output



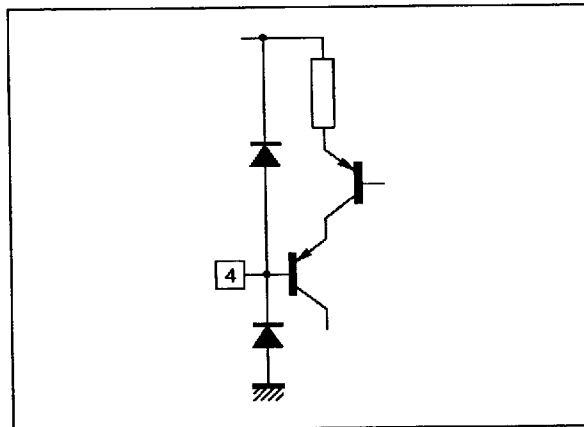
82231-04.EPS

Figure 3 : CVBS Output



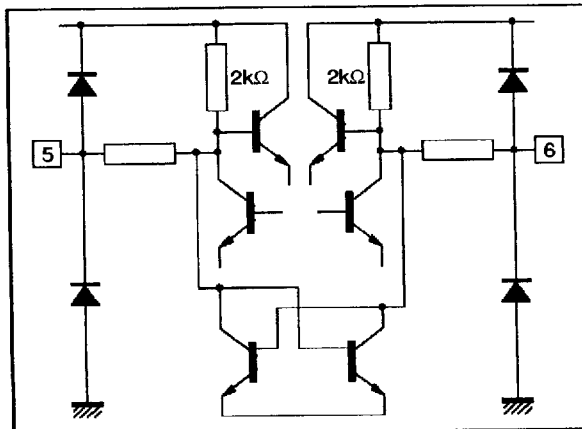
82231-05.EPS

Figure 4 : Switching Input Standard + INT/EXT



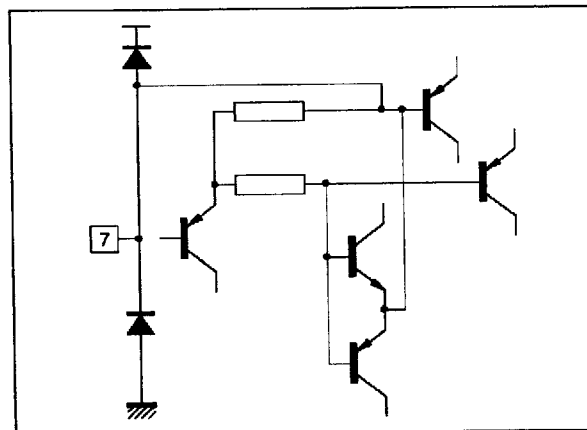
82231-06.EPS

Figure 5 : IFLC

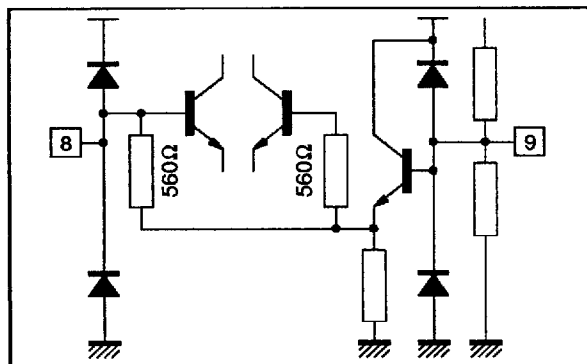


82231-07.EPS

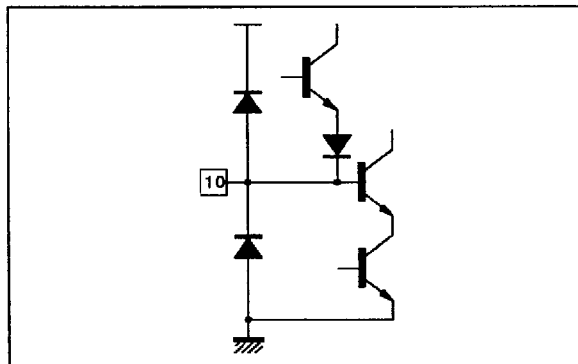
Figure 6 : Audio High Pass Filter



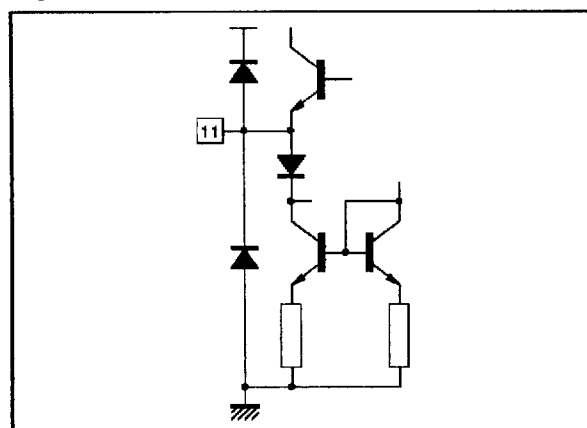
82231-08.EPS

INPUT/OUTPUT PIN CONFIGURATION (continued)**Figure 7 :** 2nd IF Sound Input (Pin 8)
Audio and 2nd IF Decoupling (Pin 9)

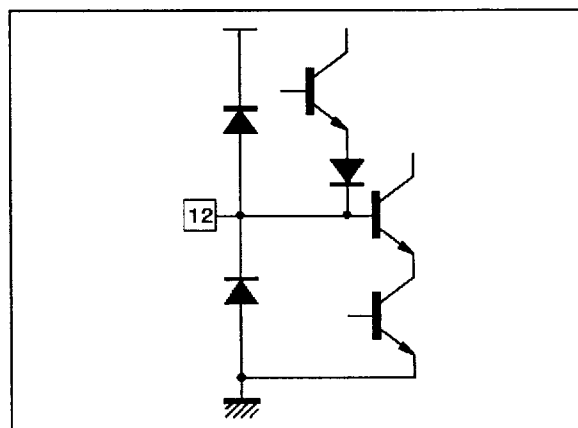
82231-09.EPS

Figure 8 : Internal Video Input

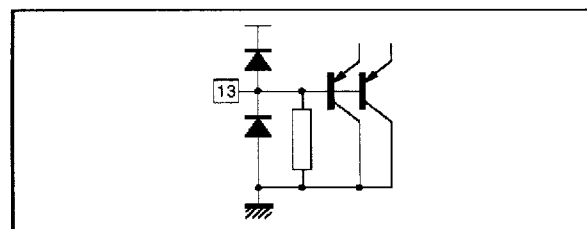
82231-10.EPS

Figure 9 : Video Switch Output

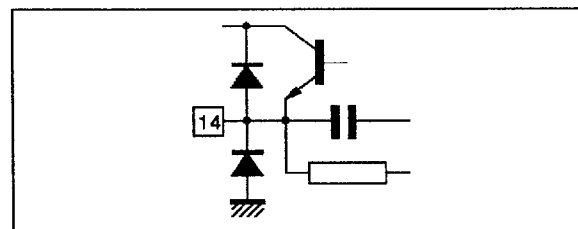
82231-11.EPS

Figure 10 : External Video Input

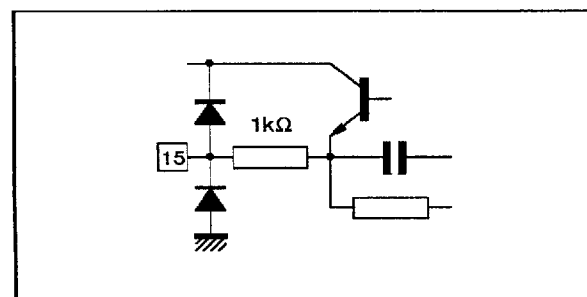
82231-12.EPS

Figure 11 : Volume Control + Mute

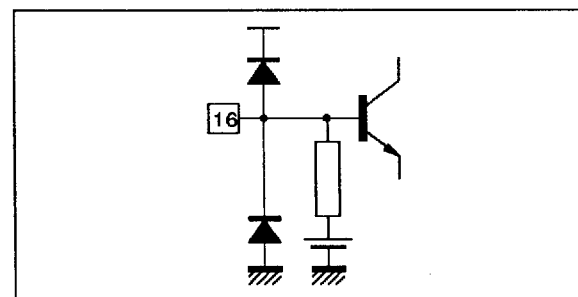
82231-13.EPS

Figure 12 : Audio Switch Output

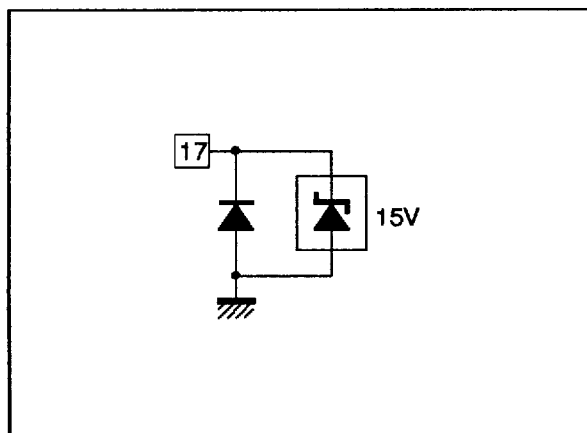
82231-14.EPS

Figure 13 : FM Demodulated Audio Output

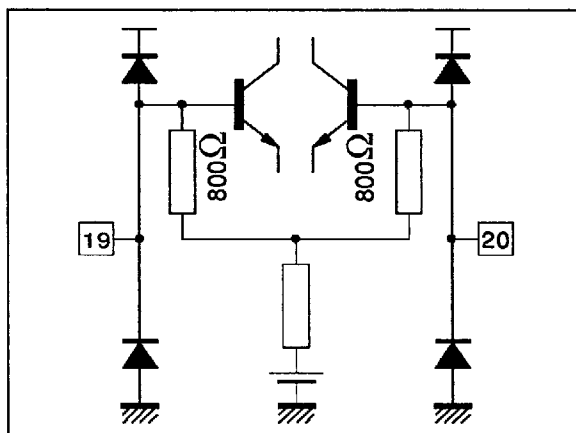
82231-15.EPS

Figure 14 : External Audio Input

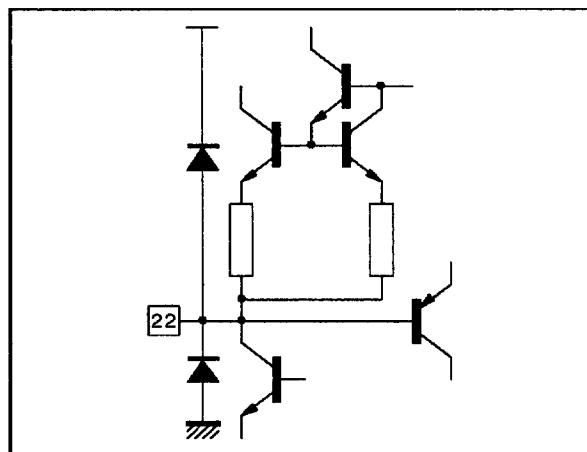
82231-16.EPS

INPUT/OUTPUT PIN CONFIGURATION (continued)**Figure 15 : Vcc**

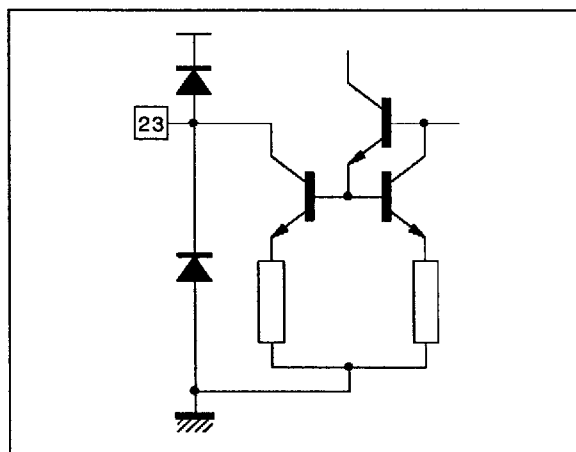
82231-17.EPS

Figure 16 : IF Input

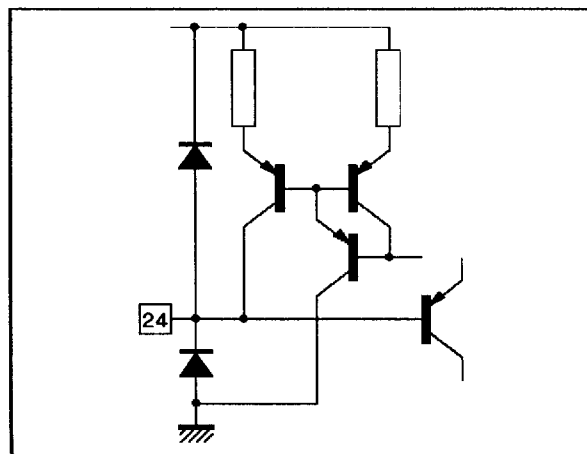
82231-18.EPS

Figure 17 : AGC Capacitor

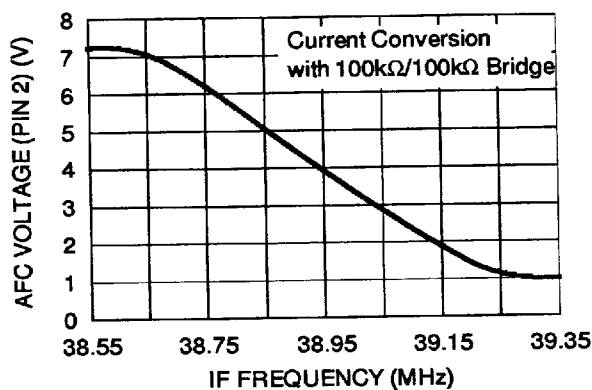
82231-20.EPS

Figure 18 : Tuner AGC Output

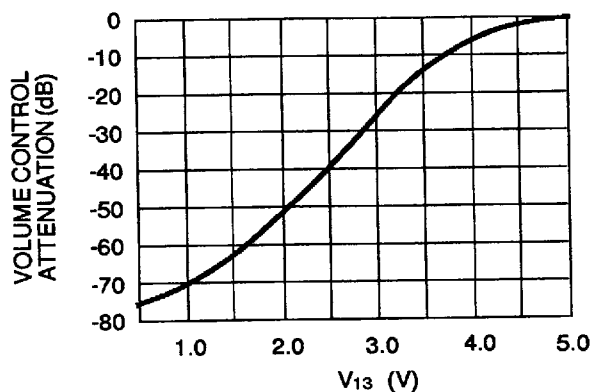
82231-21.EPS

Figure 19 : Tuner AGC Starting Point Adjustment

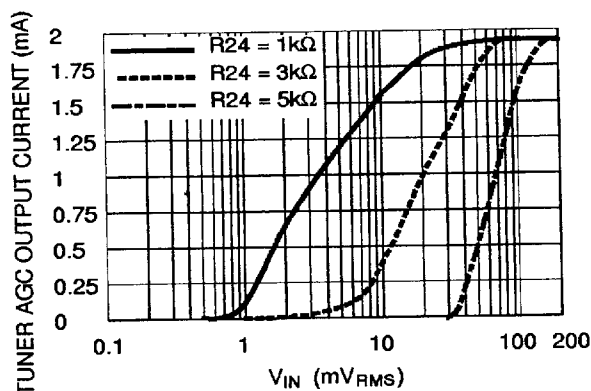
82231-22.EPS

Figure 20 : AFC Voltage Pin 2 vs IF Frequency

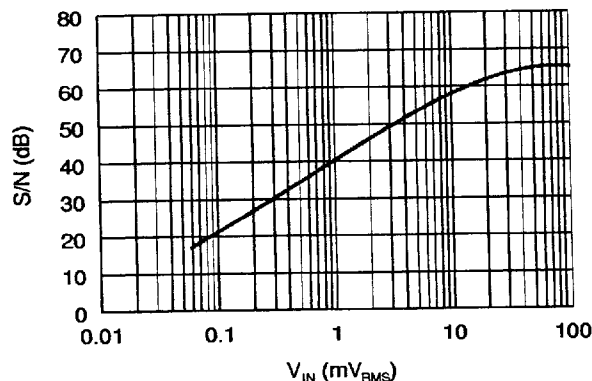
82231-23.EPS

Figure 21 : Volume Control Attenuation vs V_{13} 

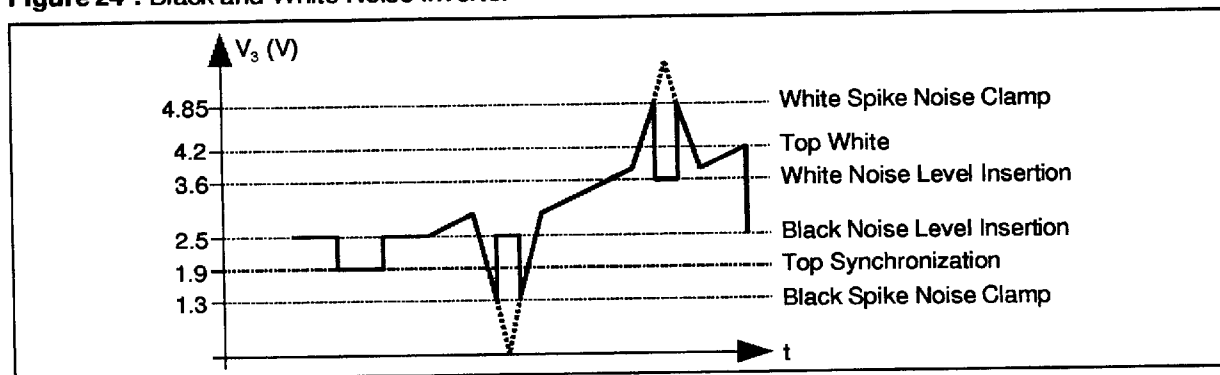
82231-24.EPS

Figure 22 : Tuner AGC Output Current vs V_{IN} (R24 is external adjustment Pin 24)

82231-25.EPS

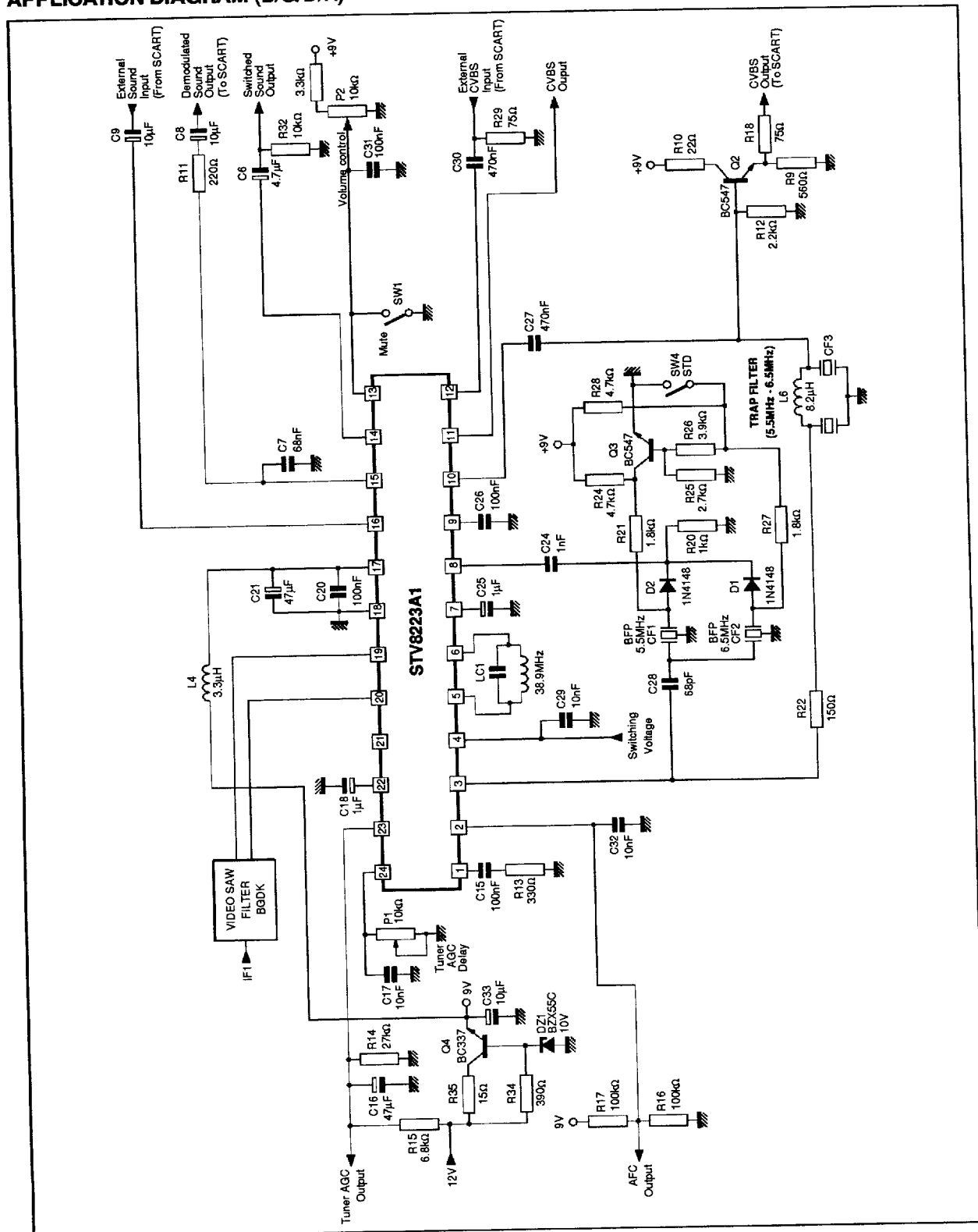
Figure 23 : Video Signal to Noise Ratio (Pin 3) as a function of IF Input Signal (Pins 19-20) Weighted CCIR-567

82231-30.EPS

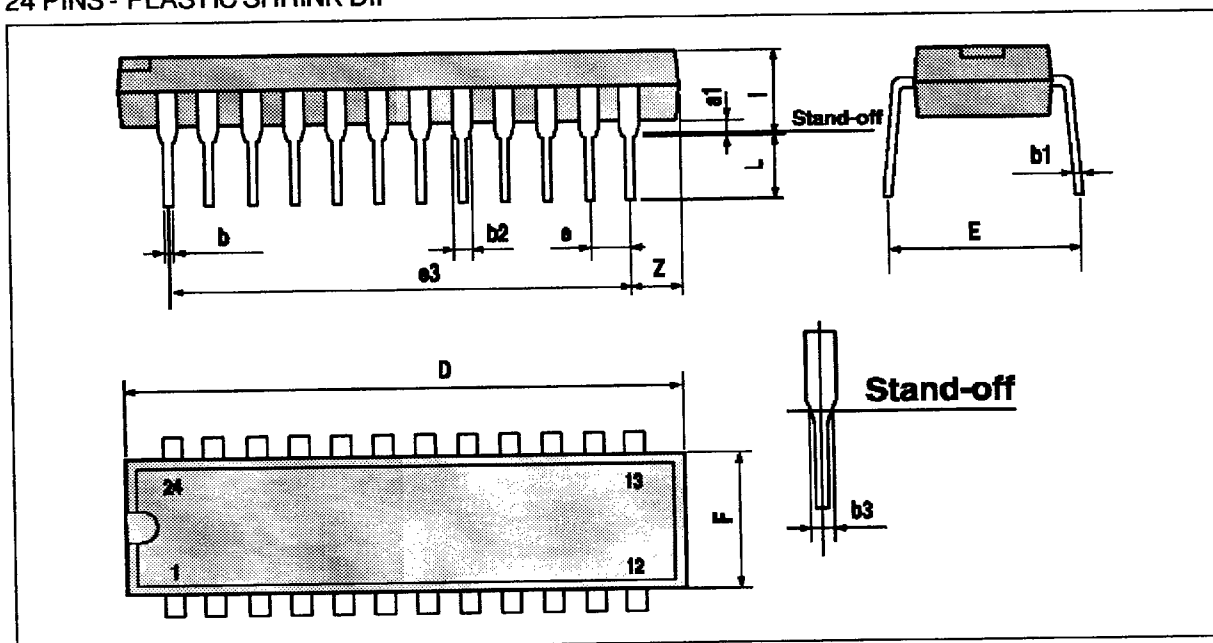
Figure 24 : Black and White Noise Inverter

82231-26.EPS

APPLICATION DIAGRAM (B/G/D/K)



82231-27.EPS

PACKAGE MECHANICAL DATA
24 PINS - PLASTIC SHRINK DIP


Dimensions	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A		3.3			0.130	
a1	0.51			0.020		
b	0.35		0.59	0.014		0.023
b1	0.2		0.36	0.008		0.014
b2	0.75		1.42	0.030		0.056
b3	0.75			0.030		
D			23.11			0.910
E	7.95		9.73	0.313		0.383
e		1.778			0.070	
e3		19.558			0.770	
e4		7.62			0.300	
F			6.86			0.270
i			5.08			0.200
L	2.54			0.100		

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