

Product Features

Common Features

- PI74FCT16841T and PI74FCT162841T are high-speed, low power devices with high current drive
- $V_{CC}=5V\pm 10\%$
- Hysteresis on all inputs
- Packages available:
 - 56-pin 240 mil wide plastic TSSOP (A)
 - 56-pin 300 mil wide plastic SSOP (V)

PI74FCT16841T Features

- High output drive: $I_{OH} = -32\text{ mA}$; $I_{OL} = 64\text{ mA}$
- Power off disable outputs permit “live insertion”
- Typical V_{OLP} (Output Ground Bounce) $< 1.0V$ at $V_{CC}=5V, T_A=25^\circ C$

PI74FCT162841T Features

- Balanced output drivers: $\pm 24\text{ mA}$
- Reduced system switching noise
- Typical V_{OLP} (Output Ground Bounce) $< 0.6V$ at $V_{CC}=5V, T_A=25^\circ C$

Product Description

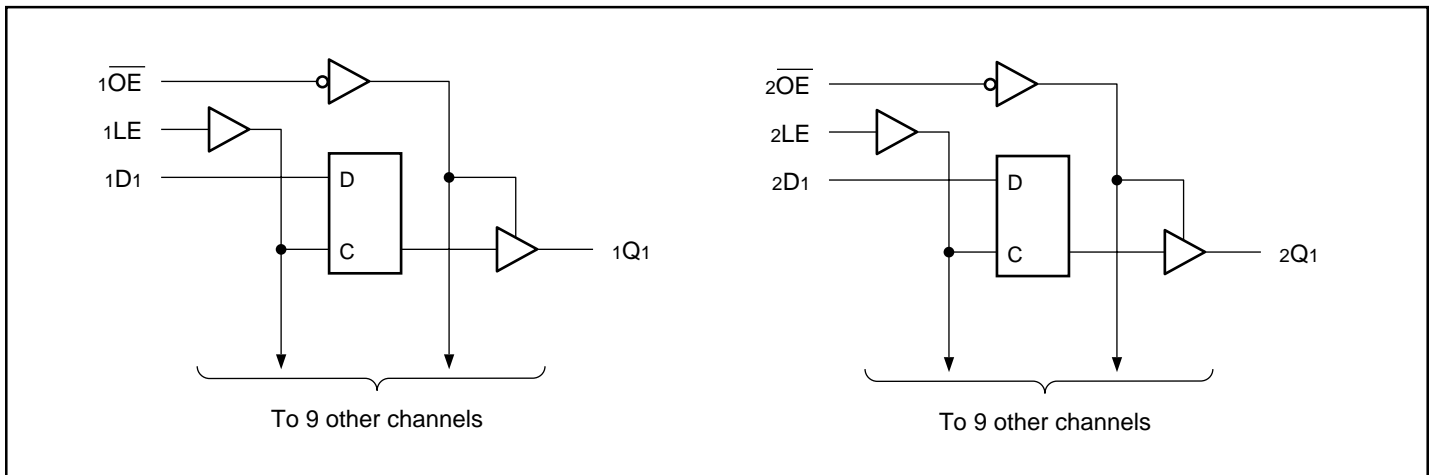
Pericom Semiconductor’s PI74FCT series of logic circuits are produced in the Company’s advanced 0.8 micron CMOS technology, achieving industry leading speed grades.

The PI74FCT16841T and PI74FCT162841T are 20-bit wide transparent latches designed to provide temporary storage of data and can be used as I/O ports, memory address latches, and bus drivers. The Output Enable and Latch Enable controls allow the devices to be operated as two 10-bit latches or one 20-bit latch. Signal pins are arranged in a flow-through organization for ease of layout and hysteresis is designed into all inputs to improve noise margin.

The output buffers on the PI74FCT16841T and PI74FCT162841T are especially designed for driving high-capacitance loads and low impedance backplanes and include a Power-Off Disable function allowing “live insertion” of boards when the devices are used as backplane drivers.

The PI74FCT162841T has $\pm 24\text{ mA}$ balanced output drivers. It is designed with current limiting resistors at its outputs to control the output edge rate resulting in lower ground bounce and undershoot. This eliminates the need for external terminating resistors for most interface applications.

Logic Block Diagram



Product Configuration

$\overline{1OE}$	□	1		56	□	1LE
1Q1	□	2		55	□	1D1
1Q2	□	3		54	□	1D2
GND	□	4		53	□	GND
1Q3	□	5		52	□	1D3
1Q4	□	6		51	□	1D4
VCC	□	7		50	□	VCC
1Q5	□	8		49	□	1D5
1Q6	□	9		48	□	1D6
1Q7	□	10		47	□	1D7
GND	□	11		46	□	GND
1Q8	□	12		45	□	1D8
1Q9	□	13		44	□	1D9
1Q10	□	14	56-PIN V56 A56	43	□	1D10
2Q1	□	15		42	□	2D1
2Q2	□	16		41	□	2D2
2Q3	□	17		40	□	2D3
GND	□	18		39	□	GND
2Q4	□	19		38	□	2D4
2Q5	□	20		37	□	2D5
2Q6	□	21		36	□	2D6
VCC	□	22		35	□	VCC
2Q7	□	23		34	□	2D7
2Q8	□	24		33	□	2D8
GND	□	25		32	□	GND
2Q9	□	26		31	□	2D9
2Q10	□	27		30	□	2D10
$\overline{2OE}$	□	28		29	□	2LE

Product Pin Description

Pin Name	Description
xDx	Data Inputs
xLE	Latch Enable Input (Active LOW)
x \overline{OE}	Output Enable Input (Active LOW)
xQx	3-State Outputs

Truth Table(1)

Inputs			Outputs
xDx	xLE	x \overline{OE}	xQx
H	H	L	H
L	H	L	L
X	L	L	Q ⁽²⁾
X	X	H	Z

1. H = High Voltage Level
L = Low Voltage Level
X = Don't Care
Z = High Impedance
2. Output level before xLE HIGH-to-LOW Transition.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5V to +7.0V
DC Input Voltage	-0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	1.0W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 5.0\text{V} \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level				0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}$	$V_{IN} = V_{CC}$			1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND}$			-1	μA
I_{OZH}	High Impedance	$V_{CC} = \text{Max.}$	$V_{OUT} = 2.7\text{V}$			1	μA
I_{OZL}	Output Current	$V_{CC} = \text{Max.}$	$V_{OUT} = 0.5\text{V}$			-1	μA
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{ mA}$			-0.7	-1.2	V
I_{OS}	Short Circuit Current	$V_{CC} = \text{Max.}^{(3)}, V_{OUT} = \text{GND}$		-80	-140	-200	mA
I_O	Output Drive Current	$V_{CC} = \text{Max.}^{(3)}, V_{OUT} = 2.5\text{V}$		-50		-180	mA
V_H	Input Hysteresis				100		mV

PI74FCT16841T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
V_{OH}	Output HIGH Voltage	$V_{CC} = \text{Min.}, V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -3.0\text{ mA}$	2.5	3.5		V
			$I_{OH} = -15.0\text{ mA}$	2.4	3.5		
			$I_{OH} = -32.0\text{ mA}$	2.0	3.0		
V_{OL}	Output LOW Voltage	$V_{CC} = \text{Min.}, V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 64\text{ mA}$		0.2	0.55	V
I_{OFF}	Power Down Disable	$V_{CC} = 0\text{V}, V_{IN} \text{ or } V_{OUT} \leq 4.5\text{V}$		—	—	± 100	μA

PI74FCT162841T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
V_{OH}	Output HIGH Voltage	$V_{CC} = \text{Min.}, V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -24.0\text{ mA}$	2.4	3.3		V
V_{OL}	Output LOW Voltage	$V_{CC} = \text{Min.}, V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 24\text{ mA}$		0.3	0.55	V
I_{ODL}	Output LOW Current	$V_{CC} = 5\text{V}, V_{IN} = V_{IH} \text{ or } V_{IL}, V_{OUT} = 1.5\text{V}^{(3)}$		60	115	150	mA
I_{ODH}	Output HIGH Current	$V_{CC} = 5\text{V}, V_{IN} = V_{IH} \text{ or } V_{IL}, V_{OUT} = 1.5\text{V}^{(3)}$		-60	-115	-150	mA

Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽⁴⁾	Description	Test Conditions	Typ	Max.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	4.5	6	pF
C_{OUT}	Output Capacitance	$V_{OUT} = 0\text{V}$	5.5	8	pF

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 5.0\text{V}$, $+25^\circ\text{C}$ ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		0.1	500	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.4V ⁽³⁾		0.5	2.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., Outputs Open OE = GND; LE = V _{CC} One Input Toggling 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND		0.15	0.25	mA/ MHz
I _C	Total Power Supply Current ⁽⁶⁾	V _{CC} = Max., Outputs Open f _{CP} = 10 MHz 50% Duty Cycle OE = GND; LE = V _{CC} fi = 5 MHz One Bit Toggling	V _{IN} = V _{CC} V _{IN} = GND		1.7	4.0 ⁽⁵⁾	mA
			V _{IN} = 3.4V V _{IN} = GND		2.0	5.0 ⁽⁵⁾	
		V _{CC} = Max., Outputs Open f _{CP} = 10 MHz 50% Duty Cycle OE = GND; LE = V _{CC} Eight Bits Toggling fi = 2.5 MHz 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND		3.2	6.5 ⁽⁵⁾	
			V _{IN} = 3.4V V _{IN} = GND		5.2	14.5 ⁽⁵⁾	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V_{CC} = 5.0V, +25°C ambient.
- Per TTL driven input (V_{IN} = 3.4V); all other inputs at V_{CC} or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed but not tested.

6. I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}

$$I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP}/2 + f_i N_i)$$

I_{CC} = Quiescent Current

ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = 3.4V)

D_H = Duty Cycle for TTL Inputs High

N_T = Number of TTL Inputs at D_H

I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)

f_{CP} = Clock Frequency for Register Devices (Zero for Non-Register Devices)

f_i = Input Frequency

N_i = Number of Inputs at f_i

All currents are in milliamps and all frequencies are in megahertz.

PI74FCT16841 Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	16841AT		16841BT		16841CT		16841DT		16841ET		Unit
			Com.		Com.		Com.		Com.		Com.		
			Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
tPLH tPHL	Propagation Delay xDx to xQx (LE = HIGH)	C _L = 50 pF R _L = 500Ω	1.5	9.0	1.5	6.5	1.5	5.5	1.5	4.2	1.5	3.4	ns
		C _L = 300 pF ⁽³⁾ R _L = 500Ω	1.5	13.0	1.5	13.0	1.5	13.0	1.5	13.0	1.5	7.5	ns
tPLH tPHL	Propagation Delay xLE to xQx	C _L = 50 pF R _L = 500Ω	1.5	12.0	1.5	8.0	1.5	6.4	1.5	4.0	1.5	3.7	ns
		C _L = 300 pF ⁽³⁾ R _L = 500Ω	1.5	16.0	1.5	15.5	1.5	15.0	1.5	8.0	1.5	7.5	ns
tpZH tpZL	Output Enable Time xOE to xQx	C _L = 50 pF R _L = 500Ω	1.5	11.5	1.5	8.0	1.5	6.5	1.5	4.8	1.5	4.4	ns
		C _L = 300 pF ⁽⁴⁾ R _L = 500Ω	1.5	23.0	1.5	14.0	1.5	12.0	1.5	9.0	1.5	9.0	ns
tPHZ tPLZ	Output Disable Time ⁽³⁾ xOE to xQx	C _L = 5 pF ⁽³⁾ R _L = 500Ω	1.5	7.0	1.5	6.0	1.5	5.7	1.5	4.0	1.5	4.0	ns
		C _L = 50 pF R _L = 500Ω	1.5	8.0	1.5	7.0	1.5	6.0	1.5	5.4	1.5	4.0	ns
tsu	Setup Time HIGH or LOW, xDx to xLE	C _L = 50 pF R _L = 500Ω	2.5	—	2.5	—	2.5	—	1.0	—	1.0	—	ns
th	Hold Time HIGH or LOW, xDx to xLE		2.5	—	2.5	—	2.5	—	1.0	—	1.0	—	ns
tw	xLE Pulse Width HIGH ⁽³⁾		4.0	—	4.0	—	4.0	—	4.0	—	3.0	—	ns
tsk(o)	Output Skew ⁽⁴⁾		—	0.5	—	0.5	—	0.5	—	0.5	—	0.5	ns

Notes:

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter is guaranteed but not production tested.
4. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.

PI74FCT16841 Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	162841AT		162841BT		162841CT		162841DT		162841ET		Unit
			Com.		Com.		Com.		Com.		Com.		
			Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
tPLH tPHL	Propagation Delay xDx to xQx (LE = HIGH)	C _L = 50 pF R _L = 500Ω	1.5	9.0	1.5	6.5	1.5	5.5	1.5	4.2	1.5	3.4	ns
			C _L = 300 pF ⁽³⁾ R _L = 500Ω	1.5	13.0	1.5	13.0	1.5	13.0	1.5	13.0	1.5	7.5
tPLH tPHL	Propagation Delay xLE to xQx	C _L = 50 pF R _L = 500Ω	1.5	12.0	1.5	8.0	1.5	6.4	1.5	4.0	1.5	3.7	ns
			C _L = 300 pF ⁽³⁾ R _L = 500Ω	1.5	16.0	1.5	15.5	1.5	15.0	1.5	8.0	1.5	7.5
tpZH tpZL	Output Enable Time xOE to xQx	C _L = 50 pF R _L = 500Ω	1.5	11.5	1.5	8.0	1.5	6.5	1.5	4.8	1.5	4.4	ns
			C _L = 300 pF ⁽⁴⁾ R _L = 500Ω	1.5	23.0	1.5	14.0	1.5	12.0	1.5	9.0	1.5	9.0
tPHZ tPLZ	Output Disable Time ⁽³⁾ xOE to xQx	C _L = 5 pF ⁽³⁾ R _L = 500Ω	1.5	7.0	1.5	6.0	1.5	5.7	1.5	4.0	1.5	4.0	ns
			C _L = 50 pF R _L = 500Ω	1.5	8.0	1.5	7.0	1.5	6.0	1.5	5.4	1.5	4.0
tsu	Setup Time HIGH or LOW, xDx to xLE	C _L = 50 pF R _L = 500Ω	2.5	—	2.5	—	2.5	—	1.0	—	1.0	—	ns
th	Hold Time HIGH or LOW, xDx to xLE		2.5	—	2.5	—	2.5	—	1.0	—	1.0	—	ns
tw	xLE Pulse Width HIGH ⁽³⁾		4.0	—	4.0	—	4.0	—	4.0	—	3.0	—	ns
tsk(o)	Output Skew ⁽⁴⁾		—	0.5	—	0.5	—	0.5	—	0.5	—	0.5	ns

Notes:

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter is guaranteed but not production tested.
4. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.