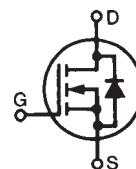


HiPerFET™ Power MOSFETs Q-Class

IXFR66N50Q2

V_{DSS} = 500 V
 I_{D25} = 50 A
 $R_{DS(on)}$ = 74 mΩ

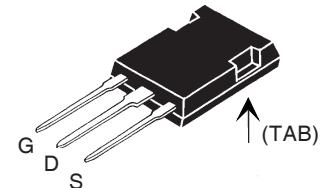
N-Channel Enhancement Mode
Avalanche Rated, High dv/dt, Low Q_g
Low intrinsic R_g , low t_{rr}



$t_{rr} \leq 250$ ns

Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	500	V	
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1\text{ M}\Omega$	500	V	
V_{GS}	Continuous	± 30	V	
V_{GSM}	Transient	± 40	V	
I_{D25}	$T_c = 25^\circ\text{C}$	50	A	
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	264	A	
I_{AR}	$T_c = 25^\circ\text{C}$	66	A	
E_{AR}	$T_c = 25^\circ\text{C}$	75	mJ	
E_{AS}	$T_c = 25^\circ\text{C}$	4.0	J	
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100\text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 2\Omega$	20	V/ns	
P_D	$T_c = 25^\circ\text{C}$	416	W	
T_J		-55 ... +150	$^\circ\text{C}$	
T_{JM}		150	$^\circ\text{C}$	
T_{stg}		-55 ... +150	$^\circ\text{C}$	
T_L	1.6 mm (0.063 in) from case for 10 s	300	$^\circ\text{C}$	
F_c	Mounting force	22...130/5...30	N/lb	
Weight		5	g	

ISOPLUS247™ (IXFR)



G = Gate D = Drain
S = Source TAB = Drain

Features

- Double metal process for low gate resistance
- International standard packages
- Epoxy meet UL 94 V-0, flammability classification
- Avalanche energy and current rated
- Fast intrinsic Rectifier

Advantages

- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values		
		($T_J = 25^\circ\text{C}$, unless otherwise specified)	min.	typ.
V_{DSS}	$V_{GS} = 0\text{ V}$, $I_D = 3\text{ mA}$	500		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 8\text{ mA}$	2.0		4.5 V
I_{GSS}	$V_{GS} = \pm 30\text{ V}_{DC}$, $V_{DS} = 0$		± 200	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0\text{ V}$	$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	50	μA 2 mA
$R_{DS(on)}$	$V_{GS} = 10\text{ V}$, $I_D = I_T$ Pulse test, $t \leq 300\text{ }\mu\text{s}$, duty cycle $d \leq 2\%$		74	mΩ

Symbol	Test Conditions	Characteristic Values		
	($T_J = 25^\circ\text{C}$, unless otherwise specified)	min.	typ.	max.
g_{fs}	$V_{DS} = 10 \text{ V}; I_D = I_T$, pulse test	30	44	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	6800	pF	
		1200	pF	
		270	pF	
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = I_T$ $R_G = 1.0 \Omega$ (External),	32	ns	
		16	ns	
		60	ns	
		10	ns	
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = I_T$	199	nC	
		42	nC	
		92	nC	
R_{thJC}		0.3	K/W	
R_{thCK}			K/W	

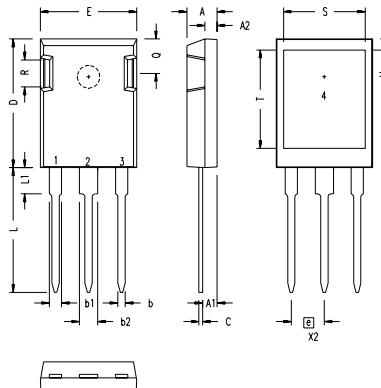
Source-Drain Diode

Characteristic Values
($T_J = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Test Conditions	min.	typ.	max.
I_s	$V_{GS} = 0 \text{ V}$		66	A
I_{SM}	Repetitive; pulse width limited by T_{JM}		264	A
V_{SD}	$I_F = I_S, V_{GS} = 0 \text{ V}$, Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		1.5	V
t_{rr} Q_{RM} I_{RM}	$I_F = 25 \text{ A}, -di/dt = 100 \text{ A}/\mu\text{s}, V_R = 100 \text{ V}$	1 10	250 μC A	ns ns A

NOTE: Test current $I_T = 33 \text{ A}$

ISOPLUS247 Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b1	.075	.084	1.91	2.13
b2	.115	.123	2.92	3.12
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
E	.620	.635	15.75	16.13
e	.215 BSC		5.45 BSC	
L	.780	.800	19.81	20.32
L1	.150	.170	3.81	4.32
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83
S	.520	.540	13.21	13.72
T	.620	.640	15.75	16.26
U	.065	.080	1.65	2.03

1 – GATE
2 – DRAIN (COLLECTOR)
3 – SOURCE (EMITTER)
4 – NO CONNECTION

NOTE: This drawing will meet all dimensions requirement of JEDEC outline TO-247AD except screw hole.