

Complementary PowerTrench Half-Bridge MOSFET

KDS4501H

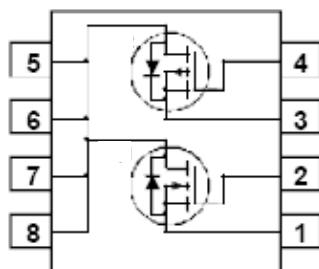
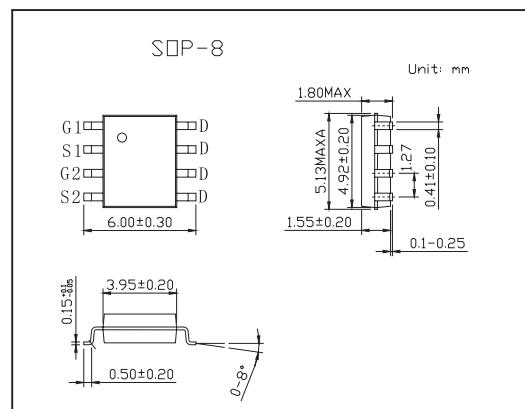
■ Features

- N-Channel

9.3 A, 30 V $R_{DS(ON)} = 18\text{m}\Omega$ @ $V_{GS} = 10\text{V}$
 $R_{DS(ON)} = 23\text{m}\Omega$ @ $V_{GS} = 4.5\text{V}$

- P-Channel

-5.6 A, -20 V $R_{DS(ON)} = 46\text{ m}\Omega$ @ $V_{GS} = -4.5\text{V}$
 $R_{DS(ON)} = 63\text{ m}\Omega$ @ $V_{GS} = -2.5\text{V}$



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	N-Channel	P- Channel	Unit
Drain to Source Voltage	V_{DSS}	30	-20	V
Gate to Source Voltage	V_{GS}	± 20	± 8	V
Drain Current Continuous (Note 1a)	I_D	9.3	-5.6	A
Drain Current Pulsed		20	-20	A
Power Dissipation for Single Operation (Note 1a) (Note 1b) (Note 1c)	P_D	2.5		W
		1.2		
		1		
Operating and Storage Temperature	T_J, T_{STG}	-55 to 150		°C
Thermal Resistance Junction to Ambient (Note 1a)	$R_{\theta JA}$	50		°C/W
Thermal Resistance Junction to Case (Note 1)	$R_{\theta JC}$	25		°C/W

KDS4501H■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	B_{VDSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	N-Ch	30		V	
		$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	P-Ch	-20			
Breakdown Voltage Temperature Coefficient	$\frac{\Delta B_{VDSS}}{\Delta T_J}$	$I_D = 250 \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$	N-Ch		24	$\text{mV/}^\circ\text{C}$	
		$I_D = -250 \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$	P-Ch		-13		
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24\text{V}, V_{GS} = 0 \text{ V}$	N-Ch		1	μA	
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch		-1		
Gate-Body Leakage	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0 \text{ V}$	N-Ch		± 100	nA	
		$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	P-Ch		± 100		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1	1.6	3	V
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-0.4	-0.7	-1.5	
Gate Threshold Voltage Temperature Coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	$I_D = 250 \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$	N-Ch		-4	$\text{mV/}^\circ\text{C}$	
		$I_D = -250 \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$	P-Ch		3		
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10 \text{ V}, I_D = 9.3\text{A}$	N-Ch		14	18	$\text{m}\Omega$
		$V_{GS} = 10 \text{ V}, I_D = 9.3 \text{ A}, T_J = 125^\circ\text{C}$			21	29	
		$V_{GS} = 4.5 \text{ V}, I_D = 7.6 \text{ A}$			17	23	
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}$	P-Ch		36	46	
		$V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}, T_J = 125^\circ\text{C}$			49	80	
		$V_{GS} = -2.5 \text{ V}, I_D = -5.0\text{A}$			47	63	
On-State Drain Current	$I_{D(on)}$	$V_{GS} = 10 \text{ V}, V_{DS} = 5\text{V}$	N-Ch	20			A
		$V_{GS} = -4.5 \text{ V}, V_{DS} = -5\text{V}$	P-Ch	-20			
Forward Transconductance	g_{FS}	$V_{DS} = 5\text{V}, I_D = 9.3\text{A}$	N-Ch		28		S
		$V_{DS} = 5\text{V}, I_D = -5.6\text{A}$	P-Ch		16		
Input Capacitance	C_{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$	N-Ch		1958		pF
Output Capacitance	C_{oss}		P-Ch		1312		
Reverse Transfer Capacitance	C_{rss}		N-Ch		424		pF
			P-Ch		240		
Turn-On Delay Time	$t_{d(on)}$	N-Channel $V_{DD} = 15 \text{ V}, I_D = 1 \text{ A},$ $V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega \text{ (Note 2)}$	N-Ch		15	27	ns
Turn-On Rise Time	t_r		P-Ch		15	27	
Turn-Off Delay Time	$t_{d(off)}$		N-Ch		5	10	ns
Turn-Off Fall Time	t_f		P-Ch		15	27	
Total Gate Charge	Q_g	N-Channel $V_{DS} = 15\text{V}, I_D=9.3\text{A}, V_{GS}=4.5\text{V} \text{ (Note 2)}$	N-Ch		38	61	ns
Gate-Source Charge	Q_{gs}		P-Ch		40	64	
Gate-Drain Charge	Q_{gd}		N-Ch		10	20	ns
			P-Ch		25	40	
		P-Channel $V_{DS}=-15\text{V}, I_D=-2.4\text{A}, V_{GS}=-4.5\text{V} \text{ (Note 2)}$	N-Ch		17	27	nC
			P-Ch		13	21	
			N-Ch		4		nC
			P-Ch		2.5		
			N-Ch		5		nC
			P-Ch		2.0		

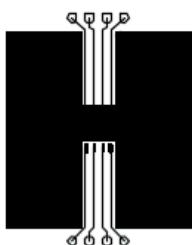
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■ Electrical Characteristics Ta = 25°C

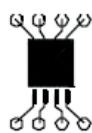
Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Maximum Continuous Drain-Source Diode Forward Current	Is		N-Ch		2.1	A
			P-Ch		-2.1	
Drain-Source Diode Forward Voltage	VSD	V _{GS} = 0 V, Is = 2.1A (Not 2)	N-Ch		1.2	V
		V _{GS} = 0 V, Is = -2.1A (Not 2)	P-Ch		-1.2	

Notes:

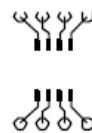
1. R_{SJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{SCA} is guaranteed by design while R_{SJA} is determined by the user's board design.



a) 50°C/W when mounted on a 1 in² pad of 2 oz copper



b) 105°C/W when mounted on a 0.04 in² pad of 2 oz copper



c) 125°C/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width < 300μs, Duty Cycle < 2 0%