

STC403F

NPN Silicon Transistor

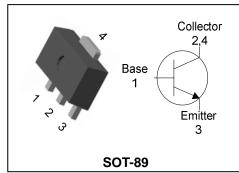
Applications

- Power amplifier application
- High current switching application

Features

- Power transistor General Purpose application
- Low saturation voltage
- : $V_{CE(sat)}=0.4V(Typ.)$
- High Voltage: VCEO= 60V Min

PIN Connection



Ordering Information

Type No.	Marking	Package Code
STC403F	C403 YWW	SOT-89

C403: DEVICE CODE, YWW(Y: Year code, WW: Weekly code)

Absolute Maximum Ratings

[Ta=25°C]

Characteristic	Symbol	Rating	Unit
Collector-Base voltage	V_{CBO}	80	V
Collector-Emitter voltage	V_{CEO}	60	V
Emitter-base voltage	V_{EBO}	5	V
Callagtor augment	I _C	3	A(DC)
Collector current	I _{CP} *	6	A(Pulse)
Collector Dower dissipation	P _C	0.5	W
Collector Power dissipation	P _C **	1	VV
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55~150	°C

^{*:} Single pulse, tp= 300 μ s

KSD-T5B015-001

^{**:} When mounted on ceramic substrate(250 mm2×0.8t)

STC403F

Electrical Characteristics

(Ta=25°C)

Charac	cteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Emitter &	oreakdown voltage	BV _{CEO}	$I_C=50$ mA, $I_B=0$	60	-	-	V
Collector cut-off current		I _{CBO}	V _{CB} =60V, I _E =0	-	-	50	μΑ
Emitter cut-off cur	rent	I _{EBO}	$V_{EB}=5V$, $I_C=0$	-	-	50	μΑ
DC current gain		h _{FE} *	$V_{CE} = 5V, I_{C} = 0.5A$	200	-	400	-
Base-Emitter on v	oltage	V _{BE(ON)}	$V_{CE} = 5V, I_{C} = 0.5A$	-	0.7	1	V
Collector-Emitter	saturation voltage	V _{CE(sat)}	I _C =2A, I _B =0.2A	-	0.4	1	V
Transition frequen	су	f _T	$V_{CB} = 5V, I_{C} = 0.5A$	-	30	-	MHz
Collector output capacitance		C _{ob}	V _{CB} =10V, I _E =0, f=1MHz	-	35	-	pF
Switching Time	Turn-on Time	t _{on}	$\begin{array}{c c} 20\mu\text{pec} & 0\text{UTPUT} \\ \hline I_{\text{B1}} & I\text{NPUT} & I_{\text{B1}} \\ \hline 0 & I_{\text{B2}} & I_{\text{B2}} \\ \hline I_{\text{B2}} & I_{\text{B2}} & v_{\text{CC}} = 30\text{V} \\ \hline \end{array}$	-	0.65	-	
	Storage Time	t _{stg}		-	1.3	-	μs
	Fall Time	t _f		-	0.65	-	

^{*} hFE rank : 200~400 Only

Electrical Characteristic Curves

Fig. 1 P_C - Ta

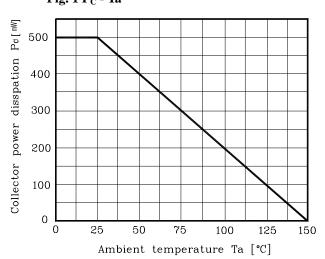


Fig. 2 V_{CE} - I_{C}

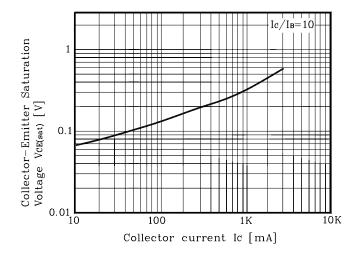


Fig. 3 h_{FE}.I_C

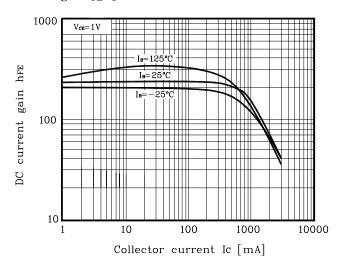


Fig. 4 h_{FE}.I_C

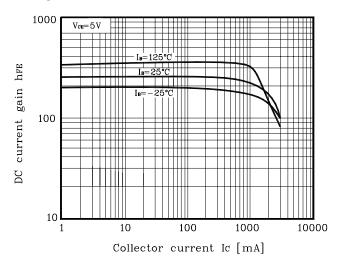


Fig. 5 I_{C} - V_{CE}

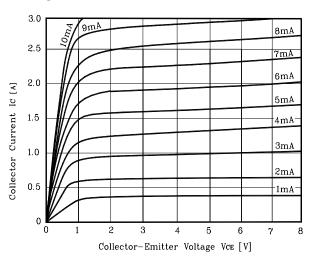
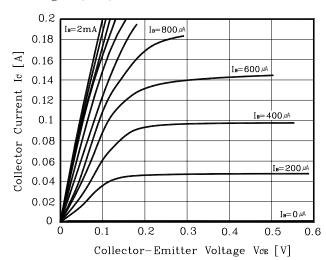
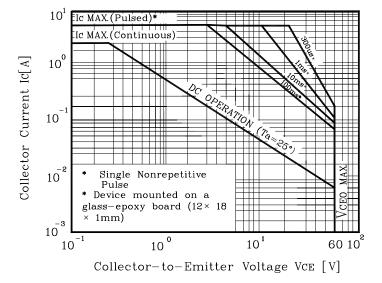


Fig. 6 I_C - V_{CE}

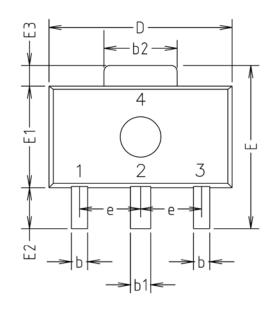


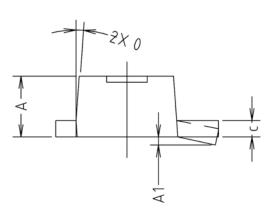
Electrical Characteristic Curves

Fig. 7 Safe operating Area



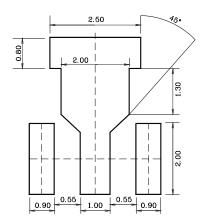
Outline Dimension(mm)





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CYMPOL	MILLIMETERS			NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE
Α	1.40	1.50	1.60	
A1	0.00	_	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
Ε	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.		
0		4° TYP.	·	

***Recommend PCB solder land [Unit: mm]**



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