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Defense Electronics Supply Center Dayton, Ohio	PREPARED BY Monica Gravel	**MILITARY DRAWING** This drawing is available for use by all Departments and Agencies of the Department of Defense
	CHECKED BY DA Di Cenzo	
Original date of drawing: 20 February 1987	APPROVED BY [signature]	TITLE: MICROCIRCUIT, DIGITAL, ADVANCED LOW POWER SCHOTTKY TTL, UNIVERSAL MULTIPLEXER, MONOLITHIC SILICON
	SIZE A / CODE IDENT. NO. 14933	DWG NO. 5962-87533
AMSC N/A	REV	PAGE 1 OF 12

5962-E298

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

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MAY 86

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

5962-87533	01	K	X
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54ALS857	Hex, two-input universal multiplexer with 3-state outputs

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
K	F-6 (24-lead, 3/8" x 5/8"), flat package
3	C-4 (28-terminal, .450" x .450"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range	-0.5 V dc to +7.0 V dc
Input voltage range	-1.5 V dc at -18 mA to +7.0 V dc
Storage temperature range	-65°C to 150°C
Maximum power dissipation (P_D) 1/	198 mW
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-case (θ_{JC}): Cases K and 3	60°C/W
Junction temperature (T_J)	+175°C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})	4.5 V dc to 5.5 V dc
Minimum high level input voltage (V_{IH})	2.0 V dc
Maximum low level input voltage (V_{IL})	0.7 V dc
Normalized fanout (each output) 2/	10 maximum at low logic level 20 maximum at high logic level
Case operating temperature range (T_C)	-55°C to +125°C

1/ Must withstand the added P_D due to short circuit test (e.g., I_O).
2/ Device will fanout in both high and low levels to the specified number of data inputs on the same device type as that being tested.

2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by contractors in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55°C < T_C < +125°C$ 1/ unless otherwise specified	Group A subgroups	Limits Min	Limits Max	Unit
High level output voltage	V_{OH}	V_{CC} = 4.5 V, I_{OH} = -100 µA V_{IN} = 0.7 V or 2.0 V	1, 2, 3	2.4		V
Low level output voltage	V_{OL}	V_{CC} = 4.5 V, I_{OL} = 12 mA V_{IN} = 0.7 V or 2.0 V	1, 2, 3		0.4	V
Input clamp voltage	V_{IC}	V_{CC} = 4.5 V, I_{IN} = -18 mA, T_C = 25°C	1		-1.5	V
Low level input current	I_{IL}	V_{CC} = 5.5 V, V_{IN} = 0.4 V	1, 2, 3		-200	µA
High level input current	I_{IH1}	V_{CC} = 5.5 V, V_{IN} = 2.7 V	1, 2, 3		60	µA
	I_{IH2}	V_{CC} = 5.5 V, V_{IN} = 7.0 V	1, 2, 3		300	µA
Off-state output current, high level voltage applied	I_{OZH}	V_{CC} = 5.5 V, V_O = 2.7 V	1, 2, 3		20	µA
Off-state output current, low level voltage applied	I_{OZL}	V_{CC} = 5.5 V, V_O = 0.4 V	1, 2, 3		-20	µA
Output current 1/	I_O	V_{CC} = 5.5 V, V_O = 2.25 V	1, 2, 3	-15	-110	mA
Supply current outputs high	I_{CCH}	V_{CC} = 5.5 V, V_{IN} = 4.5 V	1, 2, 3		24	mA
Supply current outputs low	I_{CCL}	V_{CC} = 5.5 V, V_{IN} = 0.0 V	1, 2, 3		33	mA
Supply current outputs disabled	I_{CCZ}	V_{CC} = 5.5 V	1, 2, 3		36	mA
Functional tests		See 4.3.1c	7			
Propagation delay time, A to output	t_{PLH1}	V_{CC} = 5.0 V, C_L = 50 pF ±10% $R_L = R_1$ = 500Ω	9, 10, 11		18	ns
	t_{PHL1}		9, 10, 11		18	ns

See footnote at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55°C < T_C < +125°C$ 1/ unless otherwise specified	Group A subgroups	Limits Min	Limits Max	Unit
Propagation delay time, B to output	t_{PLH2}	V_{CC} = 5.0 V, C_L = 50 pF ±10% $R_L = R_1$ = 500Ω	9, 10, 11		18	ns
	t_{PHL2}		9, 10, 11		18	ns
Propagation delay time, S0 to output	t_{PLH3}		9, 10, 11		37	ns
	t_{PHL3}		9, 10, 11		37	ns
Propagation delay time, S1 to output	t_{PLH4}		9, 10, 11		37	ns
	t_{PHL4}		9, 10, 11		37	ns
Propagation delay time, comp to output	t_{PLH5}		9, 10, 11		22	ns
	t_{PHL5}		9, 10, 11		22	ns
Propagation delay time, from A to OP = 0	t_{PLH6}		9, 10, 11		45	ns
	t_{PHL6}		9, 10, 11		45	ns
Propagation delay time, from B to OP = 0	t_{PLH7}		9, 10, 11		45	ns
	t_{PHL7}		9, 10, 11		45	ns
Propagation delay time, from S0 to OP = 0	t_{PLH8}		9, 10, 11		30	ns
	t_{PHL8}		9, 10, 11		30	ns
Propagation delay time, from S1 to OP = 0	t_{PLH9}		9, 10, 11		30	ns
	t_{PHL9}		9, 10, 11		30	ns

See footnote at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55°C \le T_C \le +125°C$ 1/ unless otherwise specified	Group A subgroups	Limits Min	Limits Max	Unit
Enable time from SO to output	t_{PZH1}	V_{CC} = 5.0 V, C_L = 50 pF ±10% $R_L = R_1$ = 500Ω	9, 10, 11		38	ns
	t_{PZL1}		9, 10, 11		38	ns
Enable time from S1 to output	t_{PZH2}		9, 10, 11		38	ns
	t_{PZL2}		9, 10, 11		38	ns
Enable time from S1 to OP = 0	t_{PZH3}		9, 10, 11		28	ns
	t_{PZL3}		9, 10, 11		28	ns
Enable time from comp to output	t_{PZL4}		9, 10, 11		27	ns
Enable time from SO to OP = 0	t_{PZL5}		9, 10, 11		24	ns
Enable time from comp to OP = 0	t_{PZL6}		9, 10, 11		30	ns
Disable time from SO to output	t_{PHZ1}		9, 10, 11		29	ns
	t_{PLZ1}		9, 10, 11		29	ns
Disable time from S1 to output	t_{PHZ2}		9, 10, 11		29	ns
	t_{PLZ2}		9, 10, 11		29	ns
Disable time from S1 to OP = 0	t_{PHZ3}		9, 10, 11		23	ns
	t_{PLZ3}		9, 10, 11		23	ns
Disable time from comp to output	t_{PLZ4}		9, 10, 11		27	ns
Disable time from SO to OP = 0	t_{PLZ5}		9, 10, 11		34	ns
Disable time from comp to OP = 0	t_{PLZ6}		9, 10, 11		24	ns

1/ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_O.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) T_A = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 tests shall verify the truth table.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

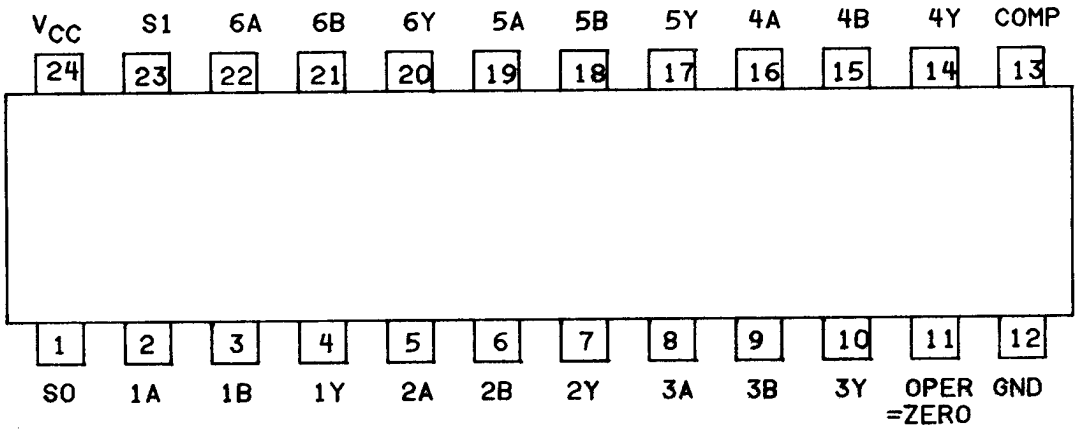
b. Steady-state life test (method 1005 of MIL-STD-883) conditions:

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) T_A = +125°C, minimum.

(3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

Case K



Case 3

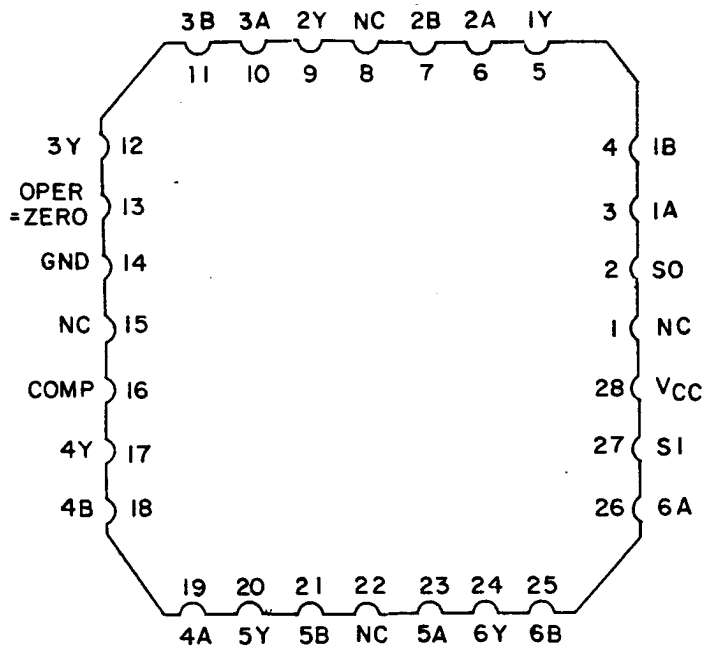


FIGURE 1. Terminal connections.

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Comp	S_1	S_0	A	B	Y	Oper = Zero
L	L	L	L	L	L	H
			L	H	L	H
			H	L	H	L
			H	H	H	L
L	L	H	L	L	H	H
			L	H	L	L
			H	L	H	H
			H	H	L	L
L	H	L	L	L	L	Z
			L	H	L	Z
			H	L	L	Z
			H	H	H	Z
L	H	H	X	X	L	L
			X	X	L	L
			X	X	L	L
			X	X	L	L
H	L	L	L	L	H	H
			L	H	H	H
			H	L	L	L
			H	H	L	L
H	L	H	L	L	H	H
			L	H	L	L
			H	L	H	H
			H	H	L	L
H	H	L	L	L	H	Z
			L	H	H	Z
			H	L	H	Z
			H	H	L	Z
H	H	H	X	X	Z	Z
			X	X	Z	Z
			X	X	Z	Z
			X	X	Z	Z

H = high level
L = low level
X = irrelevant
Z = high impedance state

FIGURE 2. Truth table.

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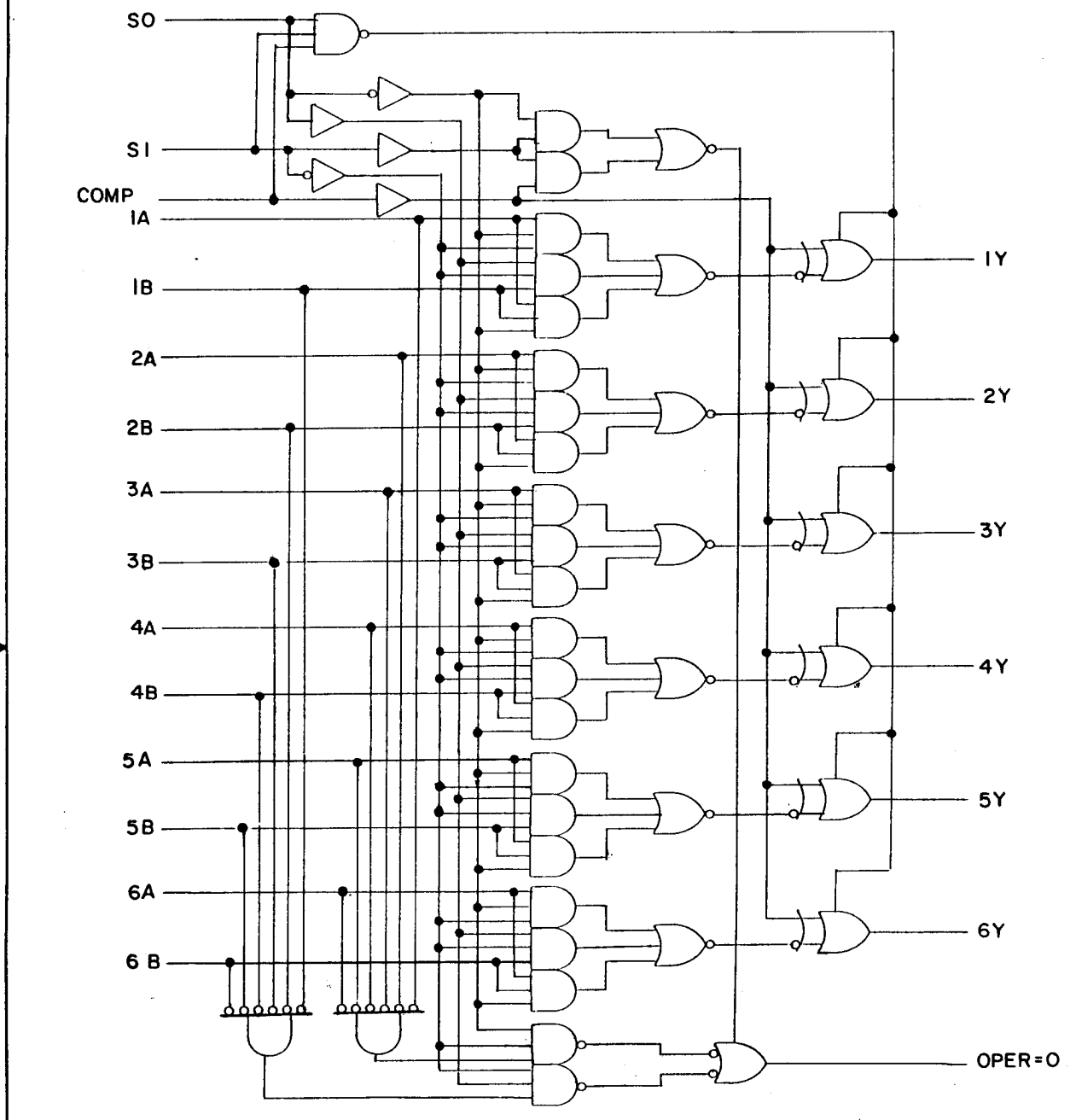

FIGURE 3. Logic diagram.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9 10, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.
** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Replaceability is determined as follows:

a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/37901B--.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.4 Approved sources of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number 1/	Replacement military specification part number
5962-8753301KX	01295	SNJ54ALS857W	M38510/37901BKX
5962-87533013X	01295	SNJ54ALS857FK	M38510/37901B3X

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
01295	Texas Instruments, Inc. P.O. Box 6448 Midland, TX 79701