Advanced Analog

a division of Intech

PRELIMINARY

AHE2800S Series

Hybrid - High Reliability DC/DC Converters

DESCRIPTION

The AHE2800S Series of DC/DC converters feature high power density and an extended temperature range for use in military and industrial applications. Designed to MIL-STD-704D input requirements, these devices have nominal 28Vpc inputs with +5v, +12v and +15v single outputs to satisfy a wide range of requirements. The circuit design incorporates a pulse width modulated push-pull topology operating in the feed-forward mode at a nominal switching frequency of 250 kHz. Input to output isolation is achieved through the use of transformers in the forward and feedback circuits.

The advanced feedback design provides fast loop response for superior line and load transient characteristics and offers greater reliability and radiation tolerance than devices incorporating optical feedback circuits.

Three standard temperature grades are offered with screening options. Refer to Part Number section. They can be provided in a standard plug-in package for PC mounting or in a flanged package for more severe environments.

These converters are manufactured in a facility fully qualified to Mil-Std-1772. All processes used to manufacture these converters have been qualified to enable Advanced Analog to deliver compliant devices. Two screening grades are available to satisfy a wide range of requirements. The CH grade converters are fully compliant to Mil-Std-1772 class B. The HB grade converters are processed to full class B screening but do not have class B element evaluation as required by Mil-Std-1772. Both grades are fully tested and operate over the full military temperature range without derating of output power. Variations in electrical, mechanical and screening can be accommodated. Extensive computer simulation using complex modeling enables rapid design modification to be provided. Contact Advanced Analog with specific requirements.

FEATURES

- 17-40Vpc input range (28Vpc nominal)
- 5V, 12V and 15V outputs available
- Indefinite short circuit and overload protection
- 17W/in³ power density
- 15 and 20 watts output power models
- Fast loop response for superior transient characteristics
- Operating temperature range from -55°C to + 125°C available
- Popular industry standard pin-out
- Resistance seam welded case for superior long term hermeticity
- Efficiencies up to 84%
- Shutdown from external signal
- Military screening
- 325,000 hour MTBF at 85°C (AUC)

SPECIFICATIONS

TCASE = -55°C to +85°C, VIN = + 28V \pm 5% unless otherwise specified.

ABSOLUTE MAXIMUM RATINGS

Input Voltage¹

-0.5V to +50V

Power Output

Internally Limited, 17.5W typical for AHE2805S, 22.5W typical for AHE2812S and AHE2815S

Soldering

300°C for 10 seconds

Temperature Range¹

Operating -55°C to +115°C case

Storage

-65°C to + 135°C

	Conditions -55°C ≤ Tc ≤ +85°C, Vin - 28 Vpc		NHE2805	s		AHE2812S			AHE2815S		
Parameter	±5%, C=0, unless otherwise specified	Min	Тур	Max	Min	Typ	Max	Min	Typ	Max	Units
STATIC CHARACTERIS	TICS	 		ļ		<u> </u>					
OUTPUT Voltage Current Ripple Accuracy Power¹	Vin = 17 TO 40 VDC IOUT = 0 TO Full Load Full Load, DC to 1MHz TCASE = 25°C, IOUT = 0	4.90 0.0 4.95 15	20	3000 60	0.0	30	12.24 1667 60 12.12	0.0	15.00 30 15.00	1333 60	mADC mVpp
REGULATION Line Load	Vin = 17 to 40VDC IOUT = 0 to Full Load		±0.5 ±0.5	±1.0 ±1.0		±0.5 ±0.5	±1.0 ±1.0		±0.5 ±0.5	±1.0 ±1.0	% %
INPUT Voltage Range¹ Current Ripple Current	Inhibited No Load Full Load	17.0	28.0 8 20	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	VDC mADC mADC mV p-p
EFFICIENCY	TCASE = +25°C Half Load to Full Load	78	82		79	83		80	84		%
CAPACITIVE LOAD	No effect on performance	500			200			200			μF
LOAD FAULT POWER DISSIPATION ⁴				6			6	-		6	w
ISOLATION	Input to Output @ 500Vpc	100			100			100			MΩ
DYNAMIC CHARACTER	STICS										
STEP LOAD CHANGES Output Transient Recovery ²	50% Load 100% Load No Load 50% Load 50% Load No Load 50% Load 100% Load No Load 50% Load 50% Load No Load		±150 -300 +300 25 500 7			±200 -400 +400 25 500 7			±200 -400 +400 25 500 7		mVpk mVpk mVpk µsec µsec msec
STEP LINE CHANGES Output Transient Recovery ²	Input step 17 to 40VDC Input step 40 to 17VDC Input step 17 to 40VDC Input step 40 to 17VDC		+180 -600 400 400			+180 -600 400 400			+180 -600 400 400		mVpk mVpk µsec µsec
TURN-ON Overshoot Delay ³	VIN = 17 to 40VDC IOUT = 0 to Full Load		0	500 14		300 8	600 14		300 8	750 14	mVpk msec
LOAD FAULT RECOVERY	Vin = 17 to 40VDC		8	14		8	14		8	14	msec
WEIGHT	Standard Package Flange Package			55 58			55 58			55 58	grams grams

Notes:

- 1. Above +85°C case temperature, derate output power linearly to 0 and maximum input voltage linearly to 42V at 115°C case.
- 2. Recovery time is measured from the initiation of the transient to where Vout has returned to within ±1% of Vout at 50% load. See typical waveforms.
- 3. Turn-on delay time measurement is for either an application of power at the input or a signal at the shutdown pin.
- 4. For operation at 16Vpc, derate output power by 33%.

SPECIFICATIONS

TCASE = -55°C to +105°C, VIN = + 28V ±5% unless otherwise specified.

ABSOLUTE MAXIMUM RATINGS

Input Voltage⁵

-0.5V to +50V

Power Output

Internally Limited, 17.5W typical for AHE2805S/ES, 22.5W typical for AHE2812S/ES and AHE2815S/ES

Soldering 300°C for 10 seconds

Temperature Range¹

Operating -55°C to +125°C case

Storage -65°C to + 135°C

ı	Conditions -55°C ≤ Tc ≤ +105°C, VIN - 28 VDC	AL	IE2805S/	FS	ΔL	{E2815S/	FS	AHE2825S/ES			
Parameter	±5%, C=0, unless otherwise specified	Min	Тур	Max	Min	Тур	Max	Min	Typ	Max	Units
STATIC CHARACTERIST	rics		<u> </u>								
OUTPUT Voltage Current Ripple Accuracy Power'	Vin = 17 TO 40 VDC lout = 0 TO Full Load Full Load, DC to 1MHz TCASE = 25°C, lout = 0	4.90 0.0 4.95 15	20	3000 60	11.76 0.0 11.88 20	30	1667 60	14.70 0.0 14.85 20	15.00 30 15.00	1333 60	mADC mVpp
REGULATION Line Load	VIN = 17 to 40VDC IOUT = 0 to Full Load		±0.5 ±0.5	±1.0 ±1.0		±0.5 ±0.5	±1.0 ±1.0		±0.5 ±0.5	±1.0 ±1.0	% %
INPUT Voltage Range* Current Ripple Current	Inhibited No Load Full Load	17.0	28.0 8 20	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	VDC mADC mADC mV p-p
EFFICIENCY	TCASE = +25°C Half Load to Full Load	78	82		79	83		80	84		%
CAPACITIVE LOAD	No effect on performance	500			200			200			μF
LOAD FAULT POWER DISSIPATION ⁴				6			6			6	w
ISOLATION	Input to Output @ 500VDC	100			100			100			MΩ
DYNAMIC CHARACTER	ISTICS										
STEP LOAD CHANGES Output Transient Recovery ²	50% Load 100% Load No Load 50% Load 50% Load No Load 50% Load 100% Load No Load 50% Load 50% Load No Load		±150 -300 +300 25 500 7			±200 -400 +400 25 500 7			±200 -400 +400 25 500 7		mVpk mVpk mVpk µsec µsec msec
STEP LINE CHANGES Output Transient Recovery ²	Input step 17 to 40VDC Input step 40 to 17VDC Input step 17 to 40VDC Input step 40 to 17VDC		+180 -600 400 400			+180 -600 400 400			+180 -600 400 400		mVpk mVpk µsec µsec
TURN-ON Overshoot Delay ³	Vin = 17 to 40VDC IOUT = 0 to Full Load		0 8	500 14		300 8	600 14		300 8	750 14	mVpk msec
LOAD FAULT RECOVERY	Vin = 17 to 40VDC		8	14		8	14		8	14	msec
WEIGHT	Standard Package Flange Package			55 58			55 58			55 58	grams grams

Notes:

- 1. Above +105°C case temperature, derate output power linearly to 0 at 125°C case.
- 2. Recovery time is measured from the initiation of the transient to where Vour has returned to within ±1% of Vour at 50% load. See typical waveforms.
- 3. Turn-on delay time measurement is for either an application of power at the input or a signal at the shutdown pin.
- 4. For operation at 16Vpc, derate output power by 33%.
- 5. Above +85°C case temperature, derate maximum input voltage linearly to 33V at +125°C case.

SPECIFICATIONS

TCASE = -55℃ to +125℃, VIN = + 28V ±5% unless otherwise specified.

ABSOLUTE MAXIMUM RATINGS

Input Voltage¹

-0.5V to +50V

Power Output

Internally Limited, 17.5W typical for AHE2805S/HB, 22.5W typical for AHE2812S/HB and AHE2815S/HB

Soldering

300°C for 10 seconds

Temperature Range¹

Operating

-55°C to + 135°C case Storage

-65°C to + 135°C

	Conditions		150050			AHE2815S/HB			AHE2825S/HB		
	-55°C ≤ Tc ≤ +125°C, Vin - 28 Vbc		E2805S/		Min						l luia.
Parameter	±5%, C=0, unless otherwise specified	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Units
STATIC CHARACTERIST	rics										
OUTPUT Voltage Current Ripple Accuracy Power ¹	VIN = 17 TO 40 VDC IOUT = 0 TO Full Load Full Load, DC to 1MHz TCASE = 25°C, IOUT = 0	4.90 0.0 4.95 15	20	3000 60	0.0	12.00 30 12.00	12.24 1667 60 12.12	14.70 0.0 14.85 20	15.00 30 15.00	1333 60	mADC mV p-p
REGULATION Line Load	VIN = 17 to 40VDC TCASE = 25°C IOUT = 0 to Full Load		10 10	25 5 50		30 50	60 30 120		40 50	75 35 150	mv mv
INPUT Voltage Range⁴ Current Ripple Current	Inhibited No Load Full Load	17.0	28.0 8 20	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	17.0	28.0 8 25	40.0 18 35 50	VDC mADC mADC mV p-p
EFFICIENCY	TCASE = +25°C Full Load	78	82		79	83		80	84		%
CAPACITIVE LOAD	No effect on performance	500	1000		200	1000		200	1000		μF
LOAD FAULT POWER DISSIPATION	Short Circuit Tc = 25°C Overload Tc = 25°C			4.5 6			4.5 6			4.5 6	W W
ISOLATION	Input to Output @ 500Vpc	100			100			100			MΩ
DYNAMIC CHARACTER	STICS										
STEP LOAD CHANGES Output Tc = 25°C Transient Recovery ²	50% Load 100% Load No Load 50% Load 50% Load No Load 50% Load 100% Load No Load 50% Load 50% Load No Load		±150 -300 +300 25 100 7	±300 -500 +500 100 200		±200 -400 +400 25 500 7	±300 -750 +750 100 1500		±200 -400 +400 25 500 7	±300 -750 +750 100 1500	mVpk mVpk mVpk µsec µsec msec
STEP LINE CHANGES Output Tc = 25°C Transient Recovery ²	Input Step 17 to 40VDC Input Step 40 to 17VDC Input Step 17 to 40VDC Input Step 40 to 17VDC		+180 -600 400 400	+300 -1000 800 800		+180 -600 400 400	+500 -1500 800 800		+180 -600 400 400	+500 -1500 -800 -800	mVpk mVpk µsec µsec
TURN-ON Overshoot Delay ³	VIN = 17 to 40VDC IOUT = 0 to Full Load		0 8	550 10		300 8	600 10		300 8	750 10	mVpk msec
LOAD FAULT RECOVERY	Vin = 17 to 40VDC		8	10		8	10		8	10	msec
WEIGHT	Standard Package Flange Package			55 58			55 58			55 58	grams grams

Notes:

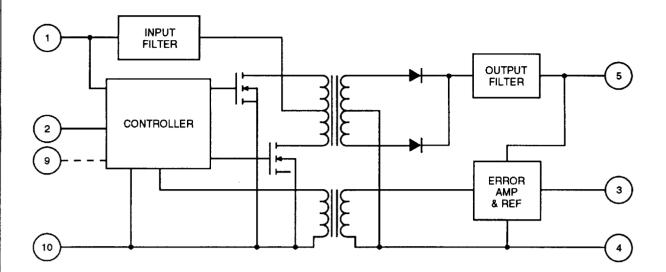
1. Above +125°C case temperature, derate output power linearly to 0 at 135°C case.

3. Turn-on delay time measurement is for either an application of power at the input or a signal at the shutdown pin.

4. For operation at 16Vpc, derate output power by 33%.

^{2.} Recovery time is measured from the initiation of the transient to where VouT has returned to within ±1% of VouT at 50% load. See typical waveforms.

BLOCK DIAGRAM



APPLICATION INFORMATION

Inhibit Function

Connecting the inhibit input (Pin 2) to input common (Pin 10) will cause the converter to shut down. It is recommended that the inhibit pin be driven by an open collector device capable of sinking at least 400μA of current. The open circuit voltage of the inhibit input is 11.5 ± 1 VDC.

EMI Filter

An optional EMI filter (AFC461) will reduce the input ripple current to levels below the limits imposed by MIL-STD-461B CEO3.

Output Adjust (AHE2805) only

The output voltage of the AHE2805 can be adjusted upward by connecting Output Adjust (Pin 3) and Output Common (Pin 4) as shown in Table 1.

Resistance Pin 3 to 4	Output Voltage Increase %
ೲ	0
39 0k	+ 1%
145k	+ 2%
63k	+ 3%
22k	+ 4%
0	+ 5%

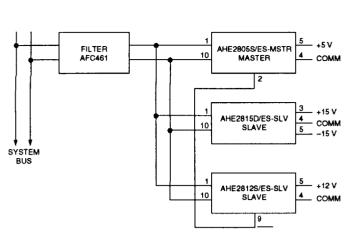
Table 1 Output adjustment resistor values

Device Synchronization

Whenever multiple DC/DC converters are utilized in a single system, significant low frequency noise may be generated due to slight difference in the switching frequencies of the converters (beat frequency noise). Because of the low frequency nature of this noise (typically less than 10kHz), it is difficult to filter out and may interfere with proper operation of sensitive systems (communications, radar or telemetry). Advanced Analog offers an option which provides synchronization of multiple AHE/ATW/ATO type converters, thus eliminating this type of noise.

To take advantage of this capability, the system designer must assign one of the converters as the master. Then, by definition, the remaining converters become slaves and will operate at the masters' switching frequency. The user should be aware that the synchronization system is fail-safe; that is, the slaves will continue operating should the master frequency be interrupted for any reason. The layout must be such that the synchronization output (pin 9) of the master device is connected to the synchronization input (pin 9) of each slave device. It is advisable to keep this run short to minimize the possibility of radiating the 250kHz switching frequency.

The appropriate parts must be ordered to utilize this feature. After selecting the converters required for the system, an 'MSTR' suffix is added for the master converter part number and an 'SLV' suffix is added for slave part number. See Part Number section.



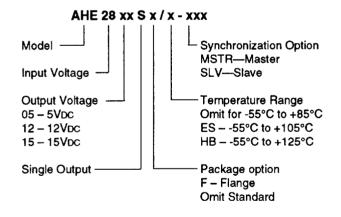
Typical Synchronization Connection Diagram

PIN DESIGNATION

Pin 1 Positive Input	Pin 10 Input common
Pin 2 Inhibit Input	Pin 9 N/C or sync
Pin 3 Output adjust*	Pin 8 Case gnd
Pin 4 Output common	Pin 7 N/C
Pin 5 Positive output	Pin 6 N/C

*AHE2805S only. AHE2812S/2815S have N/C on Pin 3

PART NUMBER



STANDARDIZED MILITARY DRAWING **CROSS REFERENCE**

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN
5962-8968301HXX	52467	AHE2805S/HB
5962-8968301HZX	52467	AHE2805SF/HB
5962-8968302HXX	52467	AHE2805S/HB-SLV
5962-8968302HZX	52467	AHE2805SF/HB-SLV
5962-8968303HXX	52467	AHE2805S/HB-MSTR
5962-8968303HZX	52467	AHE2805SF/HB-MSTR

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN
5962-9158001HXX	52467	AHE2812S/CH
5962-9158001HZX	52467	AHE2812SF/CH
5962-9158002HXX	52467	AHE2812S/CH-SLV
5962-9158002HZX	52467	AHE2812SF/CH-SLV
5962-9158003HXX	52467	AHE2812S/CH-MSTR
5962-9158003HZX	52467	AHE2812SF/CH-MSTR

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN
5962-9162501HXX	52467	AHE2815S/HB
5962-9162501HZX	52467	AHE2815SF/HB
5962-9162502HXX	52467	AHE2815S/HB-SLV
5962-9162502HZX	52467	AHE2815SF/HB-SLV
5962-9162503HXX	52467	AHE2815S/HB-MSTR
5962-9162503HZX	52467	AHE2815SF/HB-MSTR

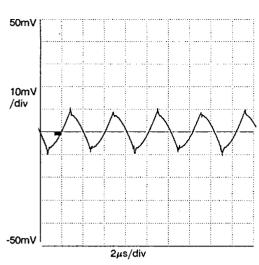
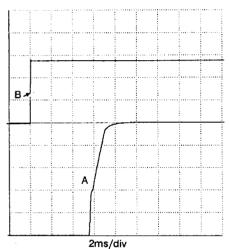


Figure 1 Output Voltage Ripple VIN = 28VDC, Full Load



Turn-on Response
A: Output @ 1V/DIV, Full Load
B: Input @ 10V/DIV (OV → 28V step) Figure 3

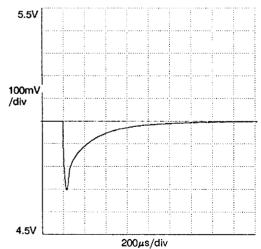


Figure 5 Load Step Response Load Step 0 to 1.5Apc (No Load to Half Load)

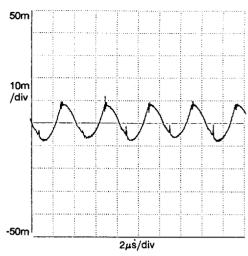
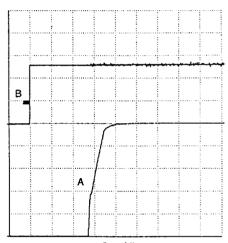


Figure 2 Input Ripple Current Vin = 28V, Full Load



2ms/div Figure 4 Turn-on Response from Inhibit
A: Output @ 1V/DIV, Full Load
B: Inhibit (Pin 2) @ 5V/DIV

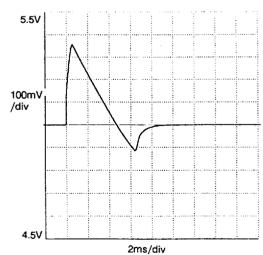


Figure 6 Load Step Response Load Step 1.5Apc to 0 Apc (Half Load to No Load)

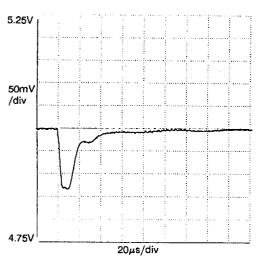
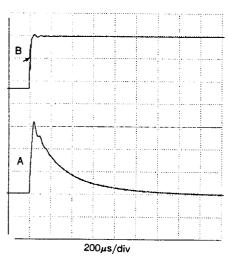
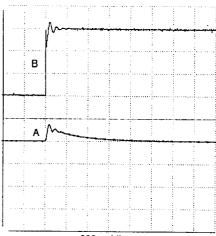


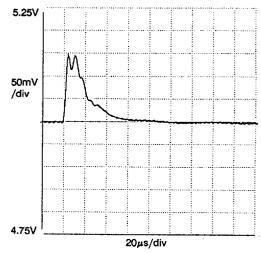
Figure 7 Load Step Response Load Step 1.5Apc to 3.0Apc (Half Load to Full Load)



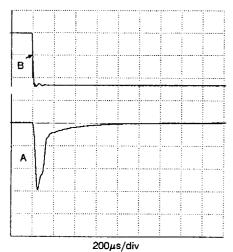
Line Step Response
A: Output @ 50mV/DIV, Full Load
B: Input step 17V∞ to 40V∞ Figure 9



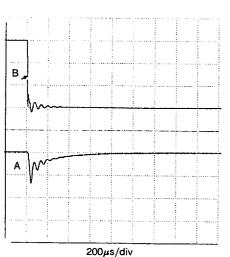
200µs/div Line Step Response
A: Output @ 50mV/DIV, Full Load Figure 11 B: Input Step 28Vpc to 34Vpc



Load Step Response Load Step 3.0ADC to 1.5ADC (Full Load to Half Load) Figure 8

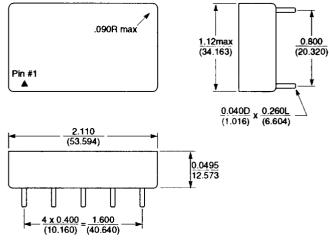


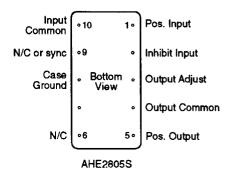
Line Step Response
A: Output @ 200mV/DIV, Full Load
B: Input Step 40Vpc to 17Vpc Figure 10



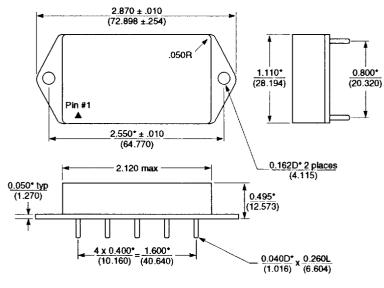
Line Step Response
A: Output @ 50mV/DIV, Full Load Figure 12 B: Input Step 28Vpc to 22Vpc

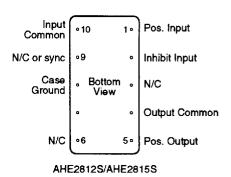
MECHANICAL OUTLINE





Standard





*±.005 (±0.127)

F option

HB SCREENING PROCESS Per MIL-STD-883C

Test Inspection	Method	Condition
Pre-Seal Internal Visual	2017	
Stabilization Bake	1008	С
Temperature Cycling	1010	С
Constant Acceleration	2001	A, Y1 direction
Bum-in	1015	TC = +125°C
Final Electrical Test		TC = -55,+25,+125°C
External Visual	2009	
Gross Leak	1014	С
Fine Leak	1014	Α

ES SCREENING PROCESS Same as HB screening except as follows:

Test Inspection	Method
Constant Acceleration	2001, 500g's
Burn-in	1015, 96 hrs.
Final Electrical	25°C only