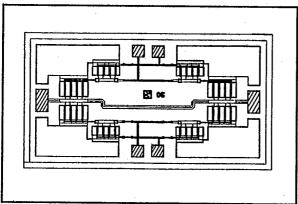


MA4GM251

GaAs MMIC C-Band (4 - 8 GHz) Receiver Protector Switch Chip



Description

The MA4GM251-500 is a C-Band [4-8 GHz] Receiver Protector Switch Chip. It reflectively isolates the Receiver from the incident power, providing approximately 40 dB protection. When the switch is in the through states it presents an Insertion Loss of 0.4 dB and an Input Return Loss of approximately 15 dB across the band. The MA4GM251-500 can be used out of band, from 2 to 12 GHz if lower isolation is adequate. This switch requires two control voltages: -7 and -14 volts for insertion loss state. Typical applications are as low power, fast protector switches for Telecommunications and EW Receivers.

ELECTRICAL CRECIFICATIONS AND CONTROL OF CARDO

ELECTRICAL SPECIFICATIONS @ 25°C Model # - MA4GM251 **Test Conditions Parameters Symbol** (TA -25°C) Frequency Min Nominal Max. Units 5 GHz Insertion Loss⁵ dB See Truth Table 8 GHz 8. 5 GHz Isolation 42 ı dB See Truth Table 8 GHz 40 dB Return Loss LR Insertion Loss State 5 GHz 18 19 P-1 dB Compression Point² See Truth Table 5 GHz 30 dBm Control Voltage ٧c See Truth Table ٧ Leakage Current See Truth Table IGSL 100 μΑ **Switching Time** TS 10% - 90% RF 3 nS

Notes:

- 1. This data is based on performance of sample devices in a chip test fixture.
- 2. Compression point is specified in the insertion loss state.

Features

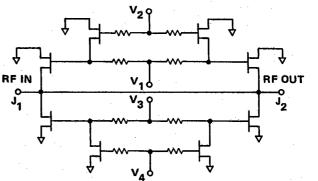
- ISOLATES RECEIVER BY 40 dB
- LOW INSERTION LOSS (0.4 dB)
- RETURN LOSS IN INSERTION LOSS STATE (15 dB)
- OPERATING POWER CAPABILITY
 4 WATTS IN ISOLATION STATE
 1 WATT IN INSERTION LOSS STATE

ABSOLUTE MAXIMUM RATINGS @ 25°C

Symbol	Parameters	Units	Recommended Maximum	
V _C	Control Voltage	\ \ \	$V_1, V_3 = -9 V V_2, V_4 = -15 V$	
Pin	Isolation State Insertion Loss State	W	4	
T _{st}	Storage Temperature	•c	-55 to +150	
Тор	Top Operating Temperature		-55 to +125	

SWITCH SCHEMATIC DIAGRAM

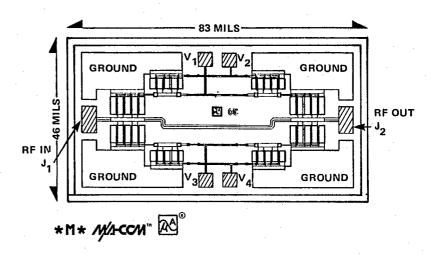
M/A-COM ADV SEMICONDUCTOR 27E D



TRUTH TABLE FOR SWITCH CONTROL FUNCTION

CONTROL LEVEL				CONDITION OF	
V ₁	V ₂	V ₃	V ₄	RF OUTPUT J₂	
-7	-14	-7	-14	LOW LOSS	
0	0	0	0	ISOLATION	

BONDING DIAGRAM



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TYPICAL PERFORMANCE CURVES

