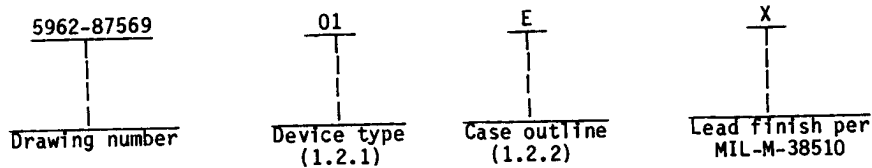




## 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

| Device type | Generic number | Circuit function           |
|-------------|----------------|----------------------------|
| 01          | 10H609         | Dual 4-5 input OR/NOR gate |

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

| Outline letter | Case outline  |
|----------------|---|
| E              | D-2 (16-lead, 1/4" x 7/8"), dual-in-line package              |
| F              | F-5 (16-lead, 1/4" x 3/8"), flat package                      |
| 2              | C-2 (20-terminal, .350" x .350"), square chip carrier package |

## 1.3 Absolute maximum ratings.

|  |                               |
|--|-------------------------------|
| Supply voltage range                                     | -8.0 V dc to 0.0 V dc         |
| Input voltage range                                      | 0.0 V dc to -5.2 V dc         |
| Storage temperature range                                | -65°C to +165°C               |
| Lead temperature (soldering, 10 seconds)                 | +300°C                        |
| Junction temperature (T <sub>J</sub> )                   | +165°C                        |
| Maximum power dissipation (P <sub>D</sub> )              | 211 mW                        |
| Thermal resistance, junction-to-case (θ <sub>JC</sub> ): | (See MIL-M-38510, appendix C) |
| Cases E and F  | 80°C/W                        |
| Case 2   | 1/                            |

## 1.4 Recommended operating conditions.

|   |  |
|---|--|
| Supply voltage (V <sub>CC</sub> )                     | -5.46 V dc minimum to -4.94 V dc maximum |
| Ambient operating temperature range (T <sub>A</sub> ) | -55°C to +125°C                          |
| Minimum high level input voltage (V <sub>IH</sub> ):  |  |
| T <sub>A</sub> = +25°C                                | -0.780 V                                 |
| T <sub>A</sub> = +125°C                               | -0.650 V                                 |
| T <sub>A</sub> = -55°C                                | -0.840 V                                 |
| Maximum low level input voltage (V <sub>IL</sub> )    | -1.950 V                                 |

1/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C, that value shall supersede the value specified herein.

|  |           |                                 |                       |
|--|-----------|---------------------------------|-----------------------|
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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth tables. The truth tables shall be as specified on figure 2.

3.2.3 Logic diagrams. The logic diagrams shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table 1 and apply over the full recommended ambient operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

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TABLE I. Electrical performance characteristics.

| Test  | Symbol           | Conditions<br>-55°C ≤ T <sub>A</sub> ≤ +125°C<br>unless otherwise specified                    | Group A<br>subgroups | Limits          |            | Unit   |        |    |
|---|------------------|--|----------------------|-----------------|------------|--------|--------|----|
|   |                  |  |                      | Min             | Max        |        |        |    |
| Cases E, F, and 2   |                  | Quiescent conditions 1/  |                      |                 |            |        |        |    |
| High level output voltage   | V <sub>OH</sub>  | Outputs terminated through 100Ω to -2 V<br>V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V | V <sub>IH</sub>      | V <sub>IL</sub> |            |        |        |    |
|   |                  |  | -0.780               | -1.950          | 1          | -1.010 | -0.780 | V  |
|   |                  |  | -0.650               | -1.950          | 2          | -0.860 | -0.650 |    |
| Low level output voltage  | V <sub>OL</sub>  | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -0.840               | -1.950          | 3          | -1.060 | -0.840 |    |
|   |                  |  | -0.780               | -1.950          | 1          | -1.950 | -1.580 | V  |
|   |                  |  | -0.650               | -1.950          | 2          | -1.950 | -1.565 |    |
| High level threshold output voltage                                   | V <sub>OHA</sub> | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -0.840               | -1.950          | 3          | -1.950 | -1.610 |    |
|   |                  |  | -1.110               | -1.480          | 1          | -1.010 | -0.780 | V  |
|   |                  |  | -0.960               | -1.465          | 2          | -0.860 | -0.650 |    |
| Low level threshold output voltage                                    | V <sub>OLA</sub> | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -1.160               | -1.510          | 3          | -1.060 | -0.840 |    |
|   |                  |  | -1.110               | -1.480          | 1          | -1.950 | -1.580 | V  |
|   |                  |  | -0.960               | -1.465          | 2          | -1.950 | -1.565 |    |
| Power supply drain current  | I <sub>EE</sub>  | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -1.160               | -1.510          | 3          | -1.950 | -1.610 |    |
|   |                  |  |                      |                 | 1          | -30    |        | mA |
|   |                  |  |                      |                 | 2, 3       | -33    |        |    |
| High level input current  | I <sub>IH</sub>  | Outputs terminated through 100Ω to -2 V<br>V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V |                      |                 | 1          |        | 350    | μA |
|   |                  |  |                      |                 | 2          |        | 915    |    |
|   |                  |  |                      |                 | 3          |        | 560    |    |
| Low level input current   | I <sub>IL</sub>  | All inputs   |                      |                 | 1, 3       |        | 0.5    | μA |
|   |                  |  |                      |                 | 2          |        | 0.3    |    |
|   |                  |  |                      |                 |            |        |        |    |
| Cases E and F   |                  | DC rapid test conditions 2/  |                      |                 |            |        |        |    |
| High level output voltage   | V <sub>OH</sub>  | Outputs terminated through 100Ω to -2 V<br>V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V | V <sub>IH</sub>      | V <sub>IL</sub> |            |        |        |    |
|   |                  |  | -0.789               | -1.950          | 1          | -1.018 | -0.789 | V  |
|   |                  |  | -0.660               | -1.950          | 2          | -0.869 | -0.660 |    |
| Low level output voltage  | V <sub>OL</sub>  | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -0.850               | -1.950          | 3          | -1.069 | -0.850 |    |
|   |                  |  | -0.789               | -1.950          | 1          | -1.950 | -1.583 | V  |
|   |                  |  | -0.660               | -1.950          | 2          | -1.950 | -1.568 |    |
| High level threshold output voltage                                   | V <sub>OHA</sub> | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -0.850               | -1.950          | 3          | -1.950 | -1.613 |    |
|   |                  |  | -1.118               | -1.483          | 1          | -1.018 | -0.789 | V  |
|   |                  |  | -0.969               | -1.468          | 2          | -0.869 | -0.660 |    |
| Low level threshold output voltage                                    | V <sub>OLA</sub> | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -1.169               | -1.513          | 3          | -1.069 | -0.850 |    |
|   |                  |  | -1.118               | -1.483          | 1          | -1.950 | -1.583 | V  |
|   |                  |  | -0.969               | -1.468          | 2          | -1.950 | -1.568 |    |
| Power supply drain current  | I <sub>EE</sub>  | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  | -1.169               | -1.513          | 3          | -1.950 | -1.613 |    |
|   |                  |  |                      |                 | 1          | -29    |        | mA |
|   |                  |  |                      |                 | 2, 3       | -32    |        |    |
| See footnotes at end of table.  |                  |  |                      |                 |            |        |        |    |
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TABLE I. Electrical performance characteristics - Continued.

| Test                                      | Symbol   | Conditions<br>-55°C < T <sub>A</sub> < +125°C<br>unless otherwise specified  | Group A<br>subgroups               | Limits               |                      | Unit |
|---|--|--|------------------------------------|----------------------|----------------------|------|
|   |  |  |                                    | Min                  | Max                  |      |
| Cases E and F DC rapid test conditions 2/ |  |  |                                    |                      |                      |      |
| High level input current                  | I <sub>IH</sub>  | Outputs terminated through 100Ω to -2 V  | 1<br>2<br>3                        |                      | 335<br>900<br>545    | μA   |
| Low level input current                   | I <sub>IL</sub>  | V <sub>EE</sub> = -5.2 V<br>V <sub>CC</sub> = 0.0 V<br>All inputs  | 1, 3<br>2                          |                      | 0.5<br>0.3           | μA   |
| Case 2 DC rapid test conditions 2/        |  |  |                                    |                      |                      |      |
| High level output voltage                 | V <sub>OH</sub>  | Outputs terminated through 100Ω to -2 V  | V <sub>IH</sub><br>V <sub>IL</sub> |                      |                      |      |
| Low level output voltage                  | V <sub>OL</sub>  | V <sub>CC</sub> = 0.0 V<br>V <sub>EE</sub> = -5.2 V  |                                    |                      |                      |      |
|   |  |  |                                    |                      |                      |      |
|   |  |  |                                    |                      |                      |      |
| High level threshold output voltage       | V <sub>OHA</sub>   |  |                                    |                      |                      |      |
| Low level threshold output voltage        | V <sub>OLA</sub>   |  |                                    |                      |                      |      |
| Power supply drain current                | I <sub>EE</sub>  |  |                                    |                      |                      |      |
| High level input current                  | I <sub>IH</sub>  | Outputs terminated through 100Ω to -2 V  |                                    |                      |                      |      |
| Low level input current                   | I <sub>IL</sub>  | V <sub>EE</sub> = -5.2 V<br>V <sub>CC</sub> = 0.0 V<br>All inputs  |                                    |                      |                      |      |
| Cases E, F, and 2 AC test conditions      |  |  |                                    |                      |                      |      |
| Transition time                           | t <sub>TLH</sub><br>t <sub>THL</sub><br>t <sup>+</sup> , t <sup>-</sup>      | V <sub>EE</sub> = -2.94<br>V <sub>CC</sub> = 2.0 V<br>C <sub>L</sub> ≤ 5 pF<br>Load all outputs through 100Ω to ground<br>All inputs | 9<br>10<br>11                      | 0.40<br>0.40<br>0.40 | 1.50<br>1.60<br>1.35 | ns   |
| Propagation delay time                    | t <sub>PHH</sub><br>t <sub>PLL</sub><br>t <sub>PHL</sub><br>t <sub>PLH</sub> |  | 9<br>10<br>11                      | 0.40<br>0.40<br>0.40 | 1.40<br>1.90<br>1.30 | ns   |

See footnotes on next page.

|  |      |                 |            |
|--|------|-----------------|------------|
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1/ The quiescent limits are determined after a device has reached thermal equilibrium. This is defined as the reading taken with the device in a socket with > 500 LFPM of +25°C air blowing on the unit and with power applied at least 4 minutes before the reading is taken.

2/ The dc rapid test forcing functions and limits are used for all dc testing. These limits are determined for each device type based on the power dissipation and package type. The rapid test (delta V) limits and forcing functions are skewed allowing rapid testing to be performed at standard temperatures without the addition of delta T's.

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2)  $T_A = +125^\circ\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

##### 4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 4 ( $C_{IN}$  measurement) shall be measured only for the initial test and after process or design changes which may affect input capacitance.

d. Subgroup 7 tests shall verify the truth table.

##### 4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test (method 1005 of MIL-STD-883) conditions:

(1) Test condition D using the circuit submitted with the certificate of compliance (see 3.5 herein).

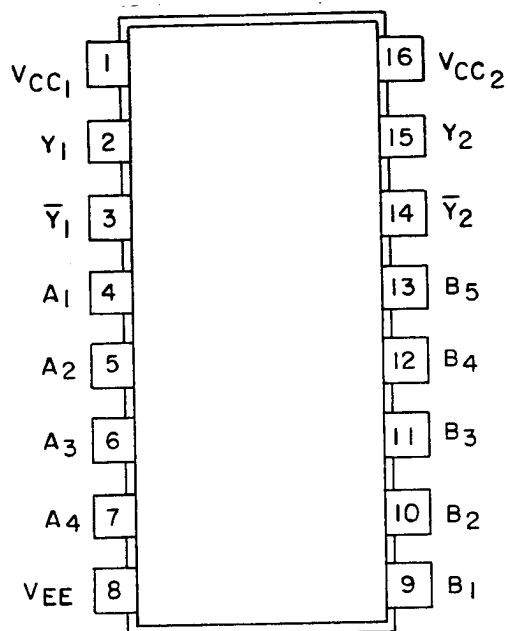
(2)  $T_A = +125^\circ\text{C}$ , minimum.

(3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

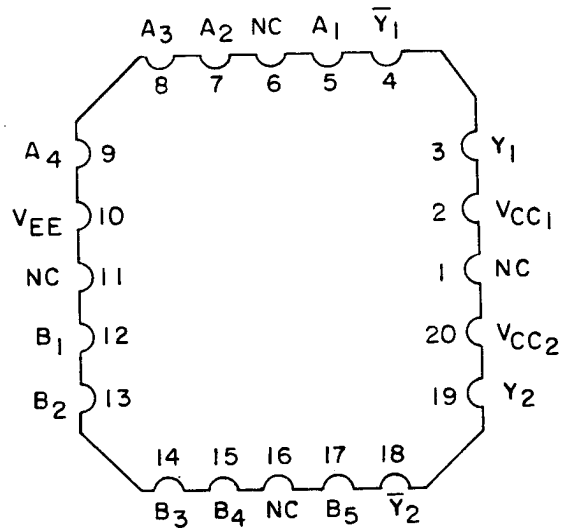
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Cases E and F



Case 2



Top view

FIGURE 1. Terminal connection.

|  |           |                                 |                       |
|--|-----------|---------------------------------|-----------------------|
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| A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | A <sub>4</sub> | Y <sub>1</sub> | $\overline{Y}_1$ |
|----------------|----------------|----------------|----------------|----------------|------------------|
| L              | L              | L              | L              | L              | H                |
| L              | L              | L              | H              | H              | L                |
| L              | L              | H              | L              | H              | L                |
| L              | L              | H              | H              | H              | L                |
| L              | H              | L              | L              | H              | L                |
| L              | H              | L              | H              | H              | L                |
| L              | H              | H              | L              | H              | L                |
| L              | H              | H              | H              | H              | L                |
| H              | L              | L              | L              | H              | L                |
| H              | L              | L              | H              | H              | L                |
| H              | L              | H              | L              | H              | L                |
| H              | L              | H              | H              | H              | L                |
| H              | H              | L              | L              | H              | L                |
| H              | H              | L              | H              | H              | L                |
| H              | H              | H              | L              | H              | L                |
| H              | H              | H              | H              | H              | L                |

| B <sub>1</sub> | B <sub>2</sub> | B <sub>3</sub> | B <sub>4</sub> | B <sub>5</sub> | $\overline{Y}_2$ | Y <sub>2</sub> |
|----------------|----------------|----------------|----------------|----------------|------------------|----------------|
| L              | L              | L              | L              | L              | H                | L              |
| L              | L              | L              | H              | L              | L                | H              |
| L              | L              | L              | H              | H              | L                | H              |
| L              | L              | H              | L              | L              | L                | H              |
| L              | L              | H              | H              | L              | L                | H              |
| L              | L              | H              | H              | H              | L                | H              |
| L              | H              | L              | L              | L              | L                | H              |
| L              | H              | L              | L              | H              | L                | H              |
| L              | H              | H              | L              | L              | L                | H              |
| L              | H              | H              | L              | H              | L                | H              |
| L              | H              | H              | H              | L              | L                | H              |
| L              | H              | H              | H              | H              | L                | H              |
| L              | H              | H              | H              | H              | L                | H              |
| H              | L              | L              | L              | L              | L                | H              |
| H              | L              | L              | L              | H              | L                | H              |
| H              | L              | L              | H              | L              | L                | H              |
| H              | L              | L              | H              | H              | L                | H              |
| H              | L              | H              | L              | L              | L                | H              |
| H              | L              | H              | L              | H              | L                | H              |
| H              | L              | H              | H              | L              | L                | H              |
| H              | L              | H              | H              | H              | L                | H              |
| H              | H              | L              | L              | L              | L                | H              |
| H              | H              | L              | L              | H              | L                | H              |
| H              | H              | L              | H              | L              | L                | H              |
| H              | H              | L              | H              | H              | L                | H              |
| H              | H              | H              | L              | L              | L                | H              |
| H              | H              | H              | L              | H              | L                | H              |
| H              | H              | H              | H              | L              | L                | H              |
| H              | H              | H              | H              | H              | L                | H              |

FIGURE 2. Truth tables.

|  |           |                                 |                       |
|--|-----------|---------------------------------|-----------------------|
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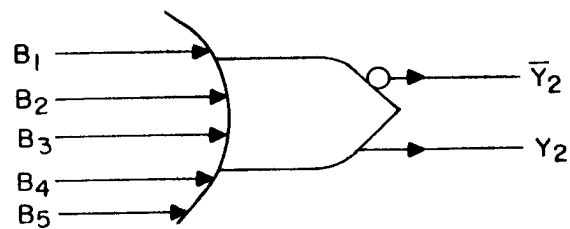
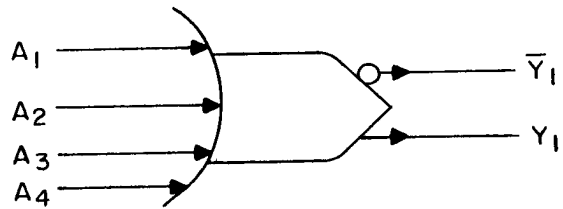


FIGURE 3. Logic diagrams.

|  |           |                                 |                    |
|--|-----------|---------------------------------|--------------------|
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TABLE II. Electrical test requirements.

| MIL-STD-883 test requirements                                      | Subgroups<br>(per method<br>5005, table I) |
|--|--|
| Interim electrical parameters<br>(method 5004)                     | 1  |
| Final electrical test parameters<br>(method 5004)                  | 1*,2,3,7*,9                                |
| Group A test requirements<br>(method 5005)                         | 1,2,3,7,9,10<br>11                         |
| Groups C and D end-point<br>electrical parameters<br>(method 5005) | 1,2,3                                      |

\* PDA applies to subgroups 1 and 7.

## 5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

## 6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

|  |           |                                 |                       |
|--|-----------|---------------------------------|-----------------------|
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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

| Military drawing part number | Vendor CAGE number | Vendor similar part number 1/ |
|------------------------------|--------------------|-------------------------------|
| 5962-8756901EX               | 04713              | 10H609/BEAJC                  |
| 5962-8756901FX               | 04713              | 10H609/BFAJC                  |
| 5962-87569012X               | 04713              | 10H609M/B2CJC                 |

Fig 16.3

Fig 16

in type C 20

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

04713

Vendor name and address

Motorola, Incorporated  
7402 South Price Road  
Tempe, AZ 85283

|  |           |                                 |                       |
|--|-----------|---------------------------------|-----------------------|
| <b>MILITARY DRAWING</b><br>DEFENSE ELECTRONICS SUPPLY CENTER<br>DAYTON, OHIO | SIZE<br>A | CODE IDENT. NO.<br><b>67268</b> | DWG NO.<br>5962-87569 |
|  |           | REV                             | PAGE 11               |

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