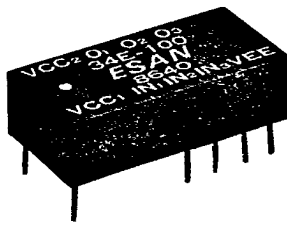


Active Delay Line (ECL Interface)

34E Series

- ULTRA-Fast Rise Time
- Triple In/Out
- 16 Pin Dip Package

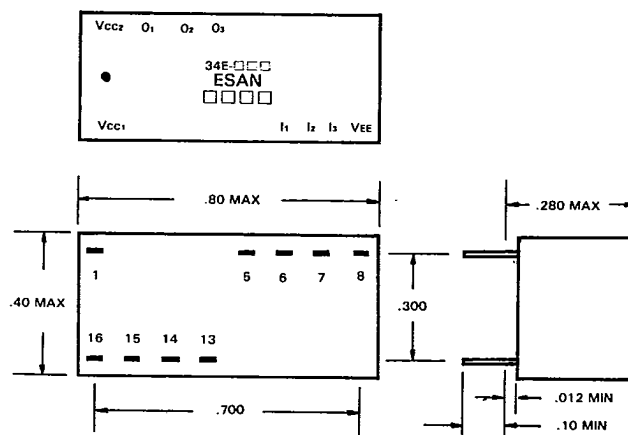
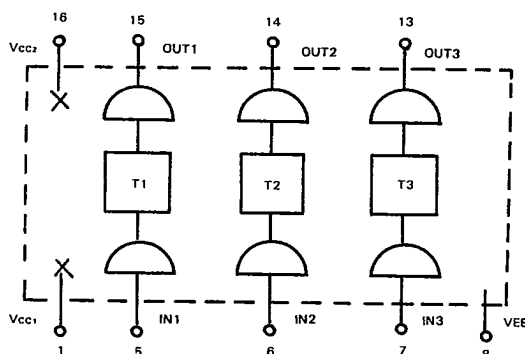
**Specifications:**

- Delay tolerance : $\pm 5\%$ or 1ns
- Rise time : 3ns max.
- Operating Temp. : 0°C to 70°C
- Supply Voltage : $-5.2\text{V} \pm 5\%$
- Logic 1 Output : -0.96V min
- Logic 0 Output : -1.65 max.
- Logic 1 Input : -0.98V min
- Logic 0 Input : -1.63V max
- Power Dissipation : 200mW typ

Test Conditions:

- Input pulse Amplitude : -1.0V (-0.75V to -1.75V)
- Pulse width : $3 \times \text{max } T_d$
- Pulse Spacing : $10 \times \text{max } T_d$
- Time delay measured : @ -1.3V level
- Output connected through an external pull down resistor of 100Ω to 2.0VDC
- Input pulse provide by standard open emitter ECL 10K gate.

PART NO.	TOTAL DELAY (ns)
34E-005	5
34E-010	10
34E-015	15
34E-020	20
34E-025	25
34E-030	30
34E-040	40
34E-050	50



Unit: Inch