

DIGITAL DELAY LINE SERIES 0453 ECL 10KH PROGRAMMABLE LOGIC DELAY MODULE 3 BIT

TECHNICAL INFORMATION

TEST CONDITIONS

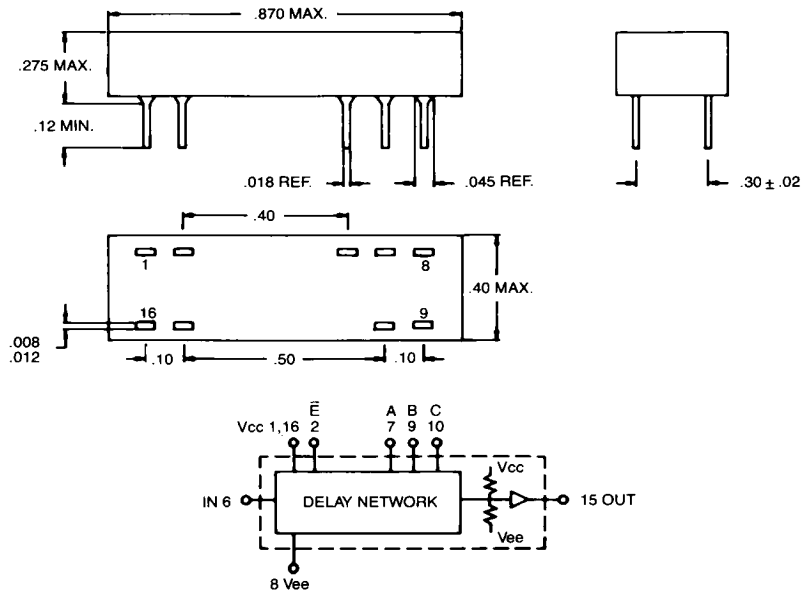
Driving Signal ECL 10KH Buffer
Pulse Width $1.5 \times \text{Total Delay}$
Pulse Period 1000 Nsec
Supply Voltage, Vee -5.2 Volts
Output Terminations 50 Ohm
 $\pm 1\%$ to -2 Volts
Ambient Temperature 25°C

PERFORMANCE CHARACTERISTICS

Delay tolerances
As Specified In Table
Performance Characteristics apply at
above listed Test Conditions.

ELECTRICAL CHARACTERISTICS

Supply Voltage, Vcc
-4.94 To 5.46 Volts
Logic 1 Input Voltage
-1.13 Volts min.
Logic 1 Output Voltage
-0.98 Volts min.
Logic 0 Input Voltage
-1.48 Volts max.
Logic 0 Output Voltage
-1.63 Volts max.
Input Impedance
100 Ohms $\pm 5\%$
Operating Temperature Range
0°C To 75°C
Temperature Coefficient Of Total Delay
150PPM/°C Typical
—Compatible with ECL 10KH circuits.
—Other delays and tolerances upon
request.



Part Number	*Min. Delay (Nom.) 1	** Max. Delay (Nom.) 1	Δ Delay /Step 1	Total Programmable Delay and its Tolerance 1
0453-0009-03	1.5NS	8.5NS	$1 \pm .3$ NS	$7 \pm .4$ NS
0453-0016-03	1.5NS	15.5NS	$2 \pm .4$ NS	$14 \pm .6$ NS
0453-0023-03	1.5NS	22.5NS	$3 \pm .5$ NS	$21 \pm .8$ NS
0453-0030-03	1.5NS	29.5NS	$4 \pm .5$ NS	$28 \pm .9$ NS
0453-0037-03	1.5NS	36.5NS	$5 \pm .5$ NS	35 ± 1 NS
0453-0044-03	1.5NS	43.5NS	$6 \pm .6$ NS	42 ± 1.2 NS
0453-0051-03	1.5NS	50.5NS	$7 \pm .7$ NS	49 ± 1.4 NS
0453-0058-03	1.5NS	57.5NS	$8 \pm .8$ NS	56 ± 1.6 NS
0453-0065-03	1.5NS	64.5NS	$9 \pm .9$ NS	63 ± 1.8 NS
0453-0072-03	1.5NS	71.5NS	10 ± 1 NS	70 ± 2.0 NS

1 Delays measured at 50% of the pulse on leading edge only.

CONTROL SIGNAL TABLE

C	B	A
0	0	0 *
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1
1	1	0
1	1	1 **

* Minimum delay code
** Maximum delay code

Specifications Subject To Change Without Notice