

8-BIT MICROCONTROLLERS

TMP68HC05/11 Series

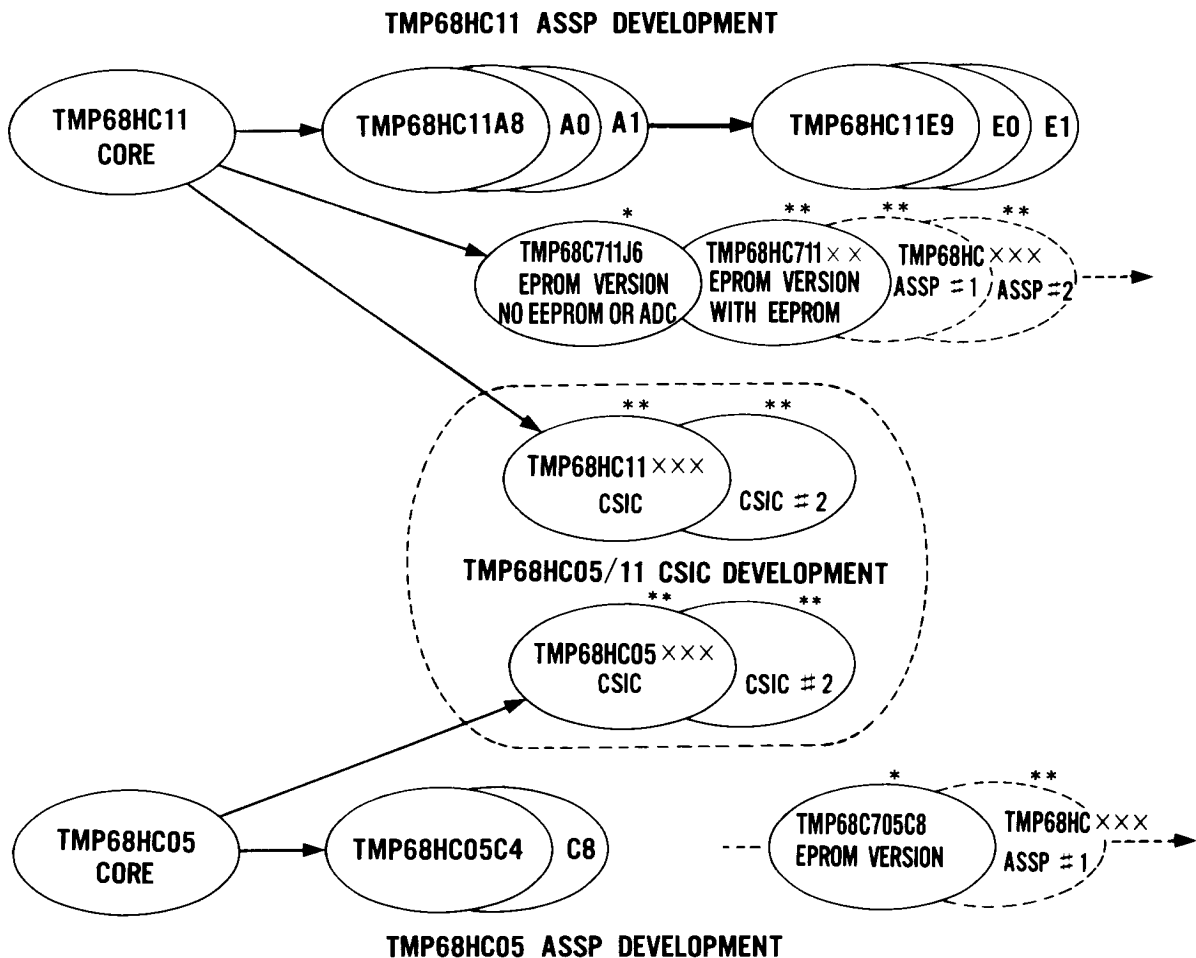
The TMP68HC05/11 is the Toshiba family of 8-bit single chip microcomputers fully mask compatible with Motorola's MC68HC05/11 Family.

The TMP68HC05 is a low-cost single chip microcontroller with a highly sophisticated on-chip two way serial peripheral capability which allows it to also be used as a slave controller in a system.

The TMP68HC11 is an advanced single chip microcontroller which contains 512Kbytes of EEPROM (except the TMP68HC11A0/E0), an 8-channel 8-bit A/D Converter, enhanced bidirectional serial interfaces, and an enhanced timer system.

Toshiba plans to broaden the line-up of the TMP68HC05/11 family and develop CSIC (Customer Specific IC) products which combine the 68HC05/11 core with user required peripherals.

TMP68HC05/11 FAMILY LINE-UP



ASSP : Application Specific Standard Product
CSIC : Customer Specific IC

* : Under Development
** : Planned Product

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Product List

PRODUCT NUMBER	ROM (bytes)	EEPROM (bytes)	RAM (bytes)	I/O PORT (bits)	MINIMUM INSTRUCTION EXECUTION TIME (μs)	CLOCK FREQUENCY FOSC (MHz)	SUPPLY VOLTAGE (V)	OPERATING TEMPERATURE (°C)	PACKAGE TYPE
TMP68HC05C4	4160	—	176	31	0.95	D.C.~4.2	3.0~5.5	0~+70	40 pin DIP *44 pin QFP *44 pin PLCC
TMP68HC05C8	7744	—	176	32	0.95	D.C.~4.2	3.0~5.5	0~+70	*40 pin DIP *44 pin PLCC *44 pin QFP
TMP68C705C8	7744	—	304	32	0.95	D.C.~4.2	3.0~5.5	0~+70	*40 pin DIP *44 pin DIC *44 pin PLCC
TMP68HC11A8	8K	512	256	38	0.95	D.C.~8.4	4.5~5.5	-40~+85	64 pin SDIP *64 pin QFP 52 pin PLCC 48 pin DIP
TMP68HC11A1	—	512							
TMP68HC11A0	—	—							
TMP68HC11E9	12K	512	512	38	0.95	D.C.~8.4	4.5~5.5	-40~+85	64 pin SDIP *64 pin QFP 52 pin PLCC
TMP68HC11E1	—	512							
TMP68HC11E0	—	—							
TMP68C711J6	16K (EPROM or OTP)	—	512	54	0.67	D.C.~12	4.5~5.5	-40~+85	64 pin SDIP (OTP) 68 pin PLCC (OTP) 64 pin CERSDIP (EPROM)

* : Planned Product

Package DIP : Plastic Dual-in-line Package (Pin pitch : 2.54mm)
 PLCC : Plastic Leaded Chip Carrier
 SDIP : Shrink DIP (Pin pitch : 1.78mm)
 QFP : Plastic Quad Flat Package