

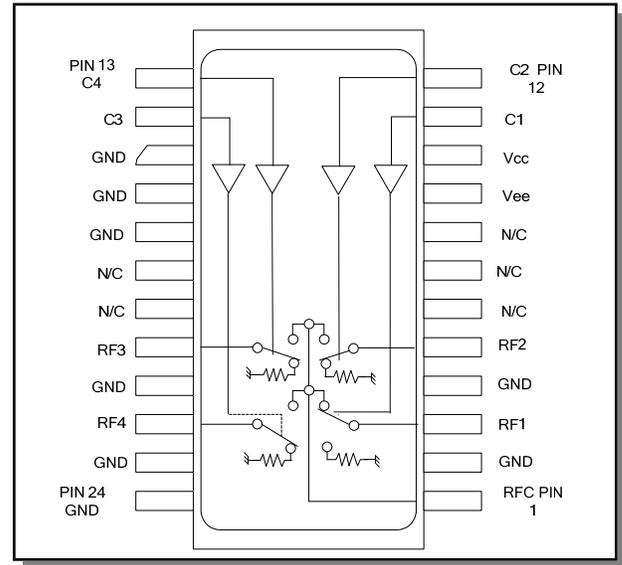
Features

- Typical Isolation: 35 dB (2.0 GHz)
- Typical Insertion Loss: 1.2 dB (2.0 GHz)
- Integral ASIC/CMOS Driver
- 50 Ohm Nominal Impedance
- Low DC Power Consumption
- Test Boards Available
- QSOP-24 Package

Description

M/A-COM's SW65-0440 is a GaAs MMIC absorptive SP4T switch with an integral silicon ASIC driver. This device is in a 24-lead plastic package. This switch offers excellent broadband performance and repeatability from DC to 3 GHz, while maintaining low DC power dissipation. The SW65-0440 is ideally suited for wireless infrastructure applications.

Functional Schematic



Ordering Information

Part Number	Package
SW65-0440	Bulk Packaging
SW65-0440TR	1000 piece reel
SW65-0440-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Pin Configuration

Pin No.	Function	Pin No.	Function
1	RFC	13	C4
2	GND	14	C3
3	RF1	15	GND
4	GND	16	GND
5	RF2	17	GND
6	NC	18	NC
7	NC	19	NC
8	NC	20	RF3
9	V _{EE}	21	GND
10	V _{CC}	22	RF4
11	C1	23	GND
12	C2	24	GND

NC = No Connection

Electrical Specifications: $T_A = 25^\circ\text{C}$

Parameter	Test Conditions	Units	Min	Typ	Max
Insertion Loss	DC - 2.0 GHz	dB	—	1.2	1.8
	DC - 3.0 GHz	dB	—	1.3	2.5
Isolation	DC - 2.0 GHz	dB	32	35	—
	DC - 3.0 GHz	dB	25	29	—
VSWR	RF1-RF4 On	DC - 3.0 GHz	—	1.2:1	1.6:1
	RF1- RF4 Off	DC - 3.0 GHz	—	1.4:1	1.8:1
	RFC	DC - 2.0 GHz	—	1.2:1	1.5:1
	RFC	DC - 3.0 GHz	—	1.6:1	2.2:1
Switching Speed ¹	T_{rise} T_{fall}	10%/90%, 90%/10%	—	15	50
	T_{on} T_{off}	50% TTL to 90%/10% RF	—	50	150
	Transients	In-band (peak to peak)	—	50	150
1 dB Compression	.05 GHz	dBm	—	+20	—
	.5 - 3.0 GHz	dBm	—	+27	—
Input IP ₃	Two tone inputs 0.05 GHz	dBm	—	+35	—
	up to +5 dBm 0.5 - 3.0 GHz	dBm	—	+46	—
V _{CC}	—	V	+4.5	+5.0	+5.5
V _{EE}	—	V	-8.0	-5.0	-4.75
V _{IL} V _{IH}	LOW-level input voltage	V	0.0	—	0.8
	HIGH-level input voltage	V	2.0	—	5.0
I _{in} (Input Leakage Current)	V _{in} = V _{CC} or GND	uA	-1.0	—	1.0
I _{cc} (Quiescent Supply Current)	V _{cntrl} = V _{CC} or GND	uA	—	250	400
ΔI_{cc} (Additional Supply Current Per TTL Input Pin)	V _{CC} = Max, V _{cntrl} = V _{CC} - 2.1 V	mA	—	—	1.0
I _{EE}	V _{EE} min to max, V _{in} = V _{IL} or V _{IH}	mA	-1.0	-0.2	—

Absolute Maximum Ratings ^{2,3,4}

Parameter	Absolute Maximum
Max. Input Power 0.05 GHz 0.5 - 3.0 GHz	+27 dBm +34 dBm
V _{CC}	-0.5V ≤ V _{CC} ≤ +7.0V
V _{EE}	-8.5V ≤ V _{EE} ≤ +0.5V
V _{CC} - V _{EE}	-0.5V ≤ V _{CC} - V _{EE} ≤ 14.5V
V _{in} ⁵	-0.5V ≤ V _{in} ≤ V _{CC} + 0.5V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +125°C

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- When the RF input is applied to the terminated port, the absolute maximum power is +30 dBm.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

1. Decoupling capacitors (.1 μF) are required on the power supply lines.

Truth Table (Switch)

TTL				RF Common To:			
C1	C2	C3	C4	RF1	RF2	RF3	RF4
1	0	0	0	On	Off	Off	Off
0	1	0	0	Off	On	Off	Off
0	0	1	0	Off	Off	On	Off
0	0	0	1	Off	Off	Off	On

0 = TTL Low; 1 = TTL High

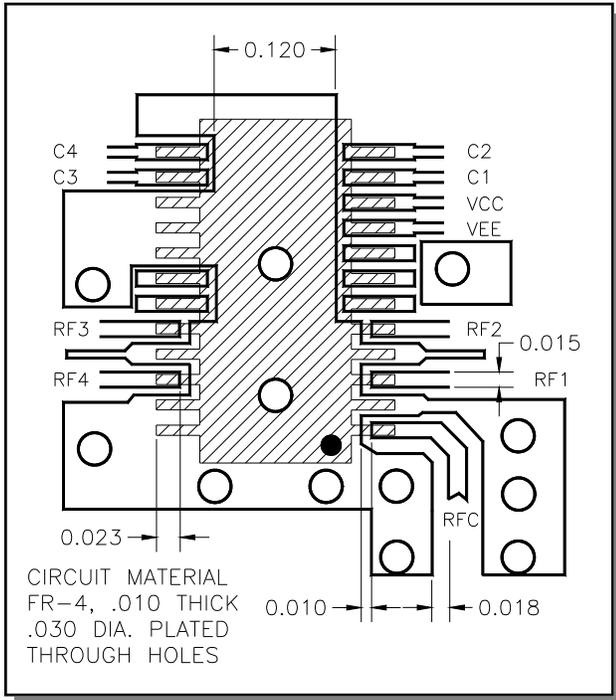
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ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.
PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology Solutions has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

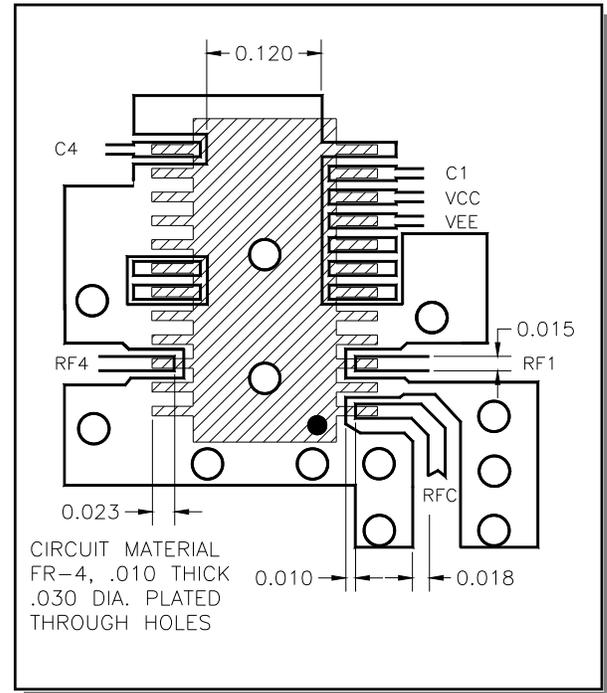
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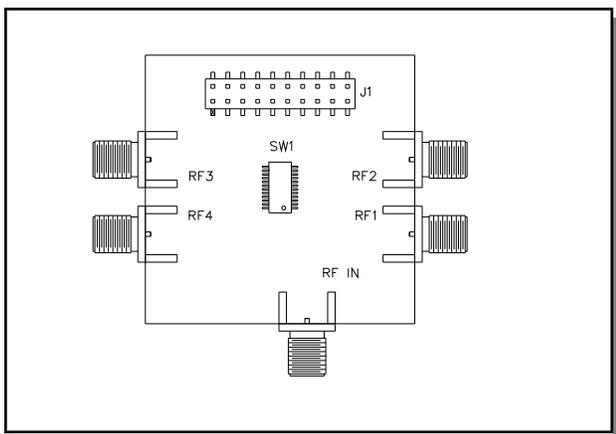
Recommended PCB Layout—SP4T



Recommended PCB Layout—SP2T



Evaluation Board - SW65-0440-TB



Handling Procedures

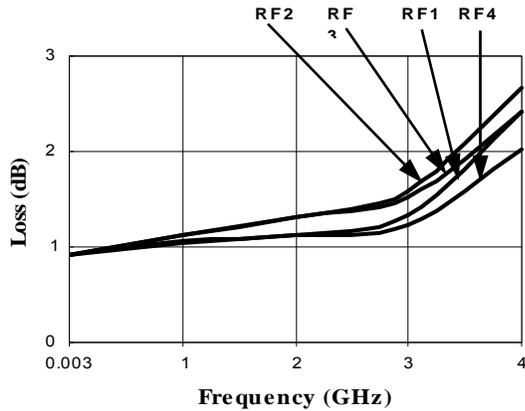
Please observe the following precautions to avoid damage:

Static Sensitivity

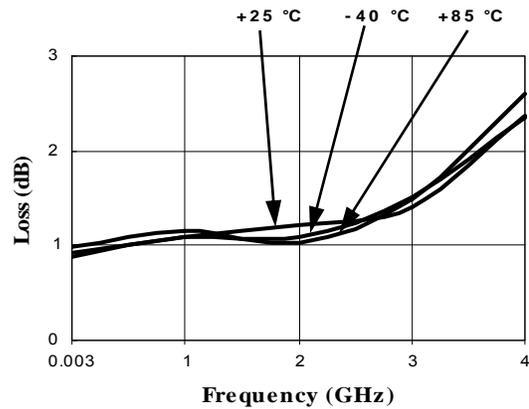
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Typical Performance Curves

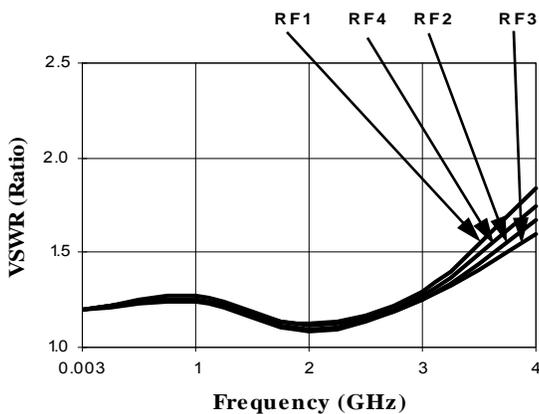
Insertion Loss (dB) @ +25°C



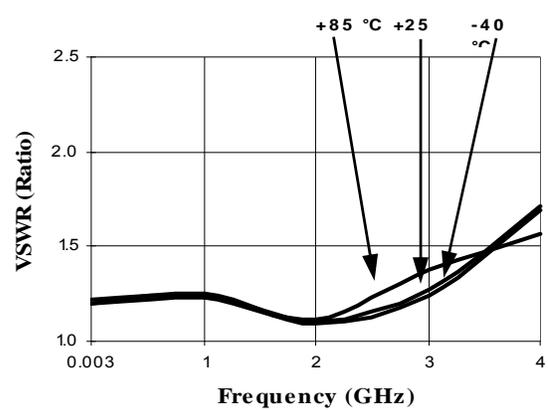
Loss Variation Over Temp. (dB)



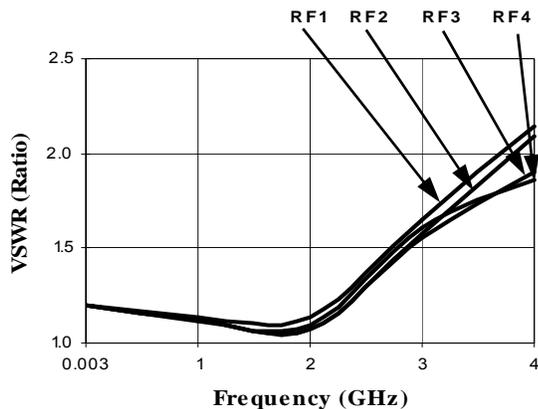
RF1 - RF4 On VSWR @ +25°C



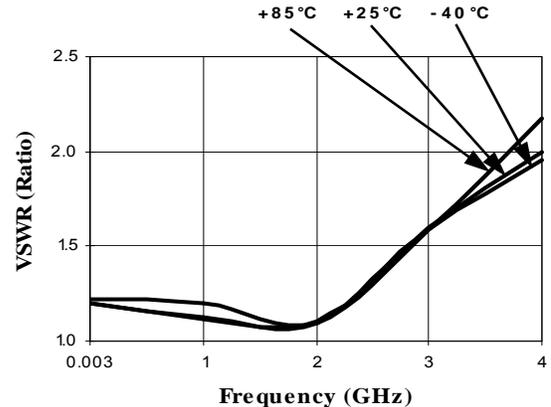
RF1 - RF4 On VSWR Temp. Variation



RFC On VSWR @ +25°C

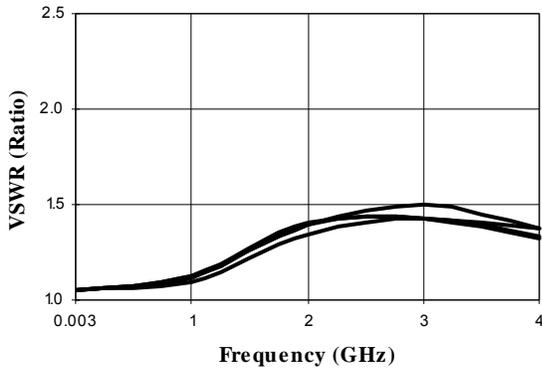


RFC On VSWR Temp. Variation

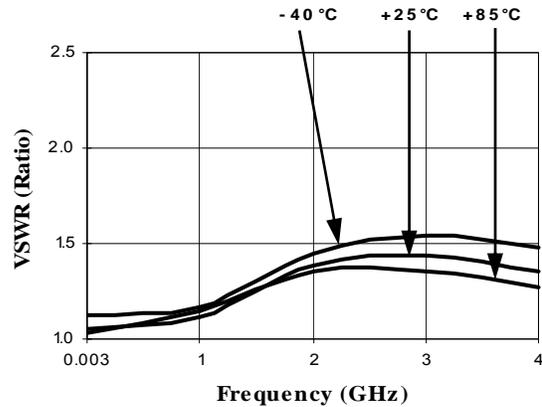


Typical Performance Curves

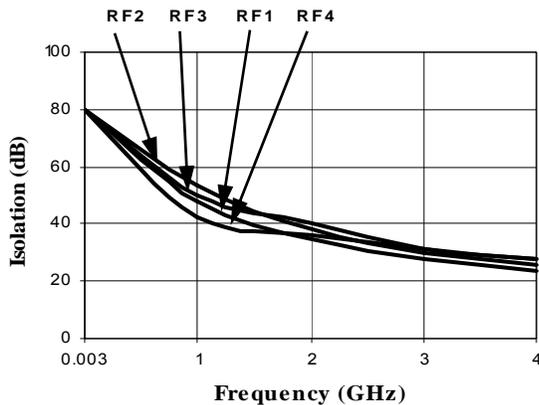
RF1 - RF4 Off VSWR @ +25°C



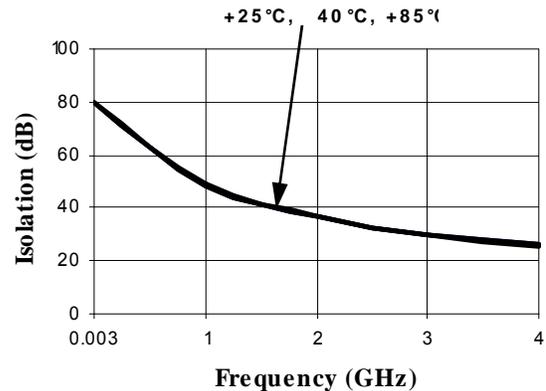
RF1 - RF4 Off VSWR Temp. Variation



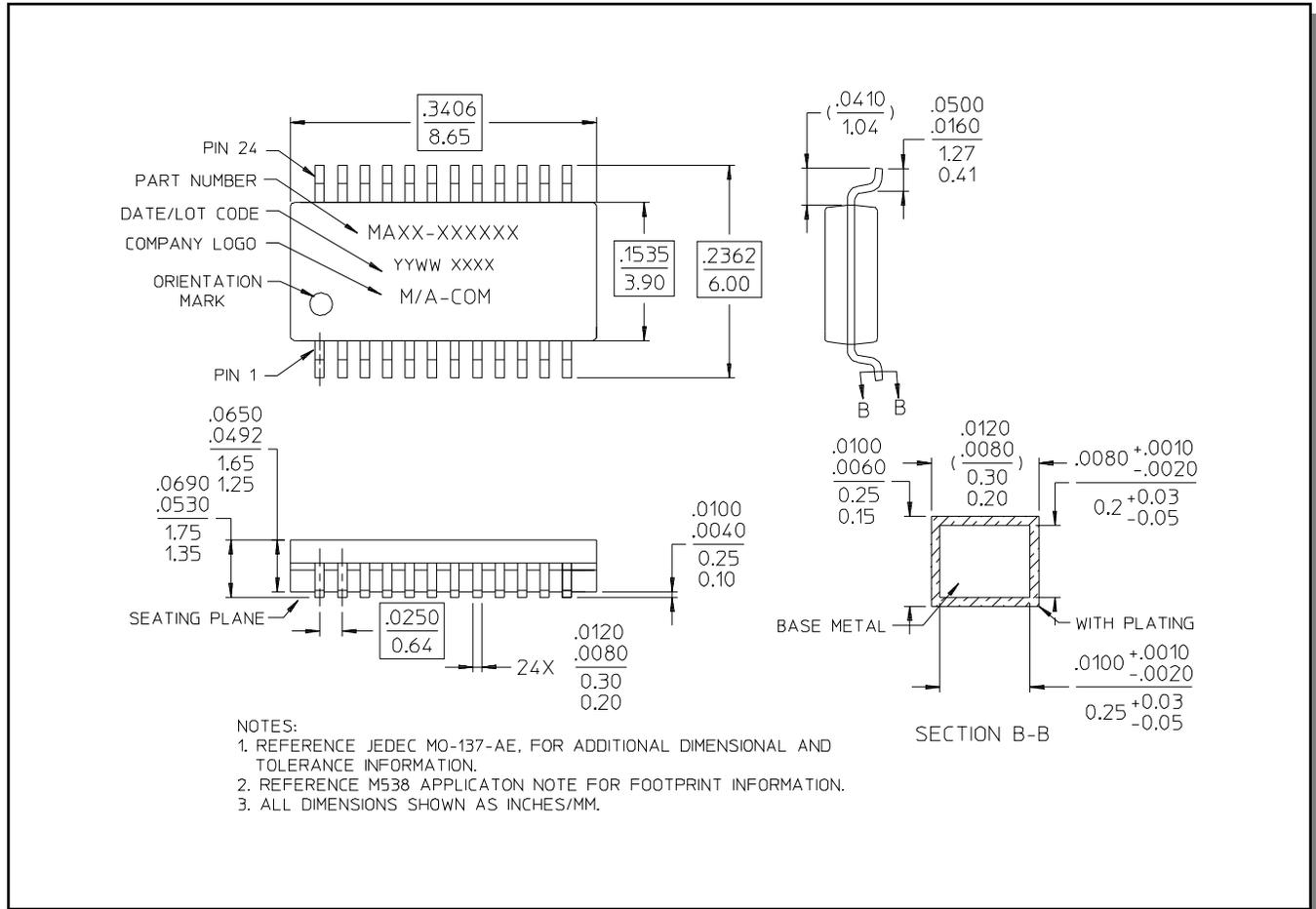
Isolation (dB) @ +25°C



Isolation Temp. Variation (dB)



QSOP-24†



† Reference Application Note M538 for lead-free solder reflow recommendations.