High Voltage Transistor NPN Silicon

MAXIMUM RATINGS

THERMAL CHARACTERISTICS

Characteristic

Thermal Resistance, Junction to Ambient

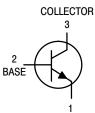
Thermal Resistance, Junction to Case

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	400	Vdc
Collector-Base Voltage	V _{CBO}	500	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current — Continuous	۱ _C	300	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

Symbol

 $R_{\theta JA}$

 $\mathsf{R}_{\theta \mathsf{JC}}$



CASE 29-11, STYLE 1 TO-92 (TO-226AA)

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ON Semiconductor Preferred Device

EMITTER

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 1.0 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	400	—	Vdc
Collector–Emitter Breakdown Voltage $(I_C = 100 \ \mu Adc, \ V_{BE} = 0)$	V _{(BR)CES}	500	—	Vdc
Collector–Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	500	—	Vdc
Emitter–Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	6.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 400 \text{ Vdc}, I_E = 0$)	Ісво	_	0.1	μAdc
Collector Cutoff Current ($V_{CE} = 400 \text{ Vdc}, V_{BE} = 0$)	I _{CES}	_	500	nAdc
Emitter Cutoff Current ($V_{EB} = 4.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	0.1	μAdc

Max

200

83.3

Unit

°C/W

°C/W

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS ⁽¹⁾				
DC Current Gain ⁽¹⁾ ($I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 50 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 100 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$)	h _{FE}	40 50 45 40	 200 	_
Collector–Emitter Saturation Voltage ⁽¹⁾ ($I_C = 1.0 \text{ mAdc}, I_B = 0.1 \text{ mAdc}$) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)	V _{CE(sat)}		0.4 0.5 0.75	Vdc
Base–Emitter Saturation Voltage $(I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc})$	V _{BE(sat)}	—	0.75	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	
Output Capacitance ($V_{CB} = 20 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C _{obo}	—	7.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz}$)	C _{ibo}	—	130	pF
Small–Signal Current Gain $(I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 20 \text{ MHz})$	h _{fe}	1.0	—	-

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

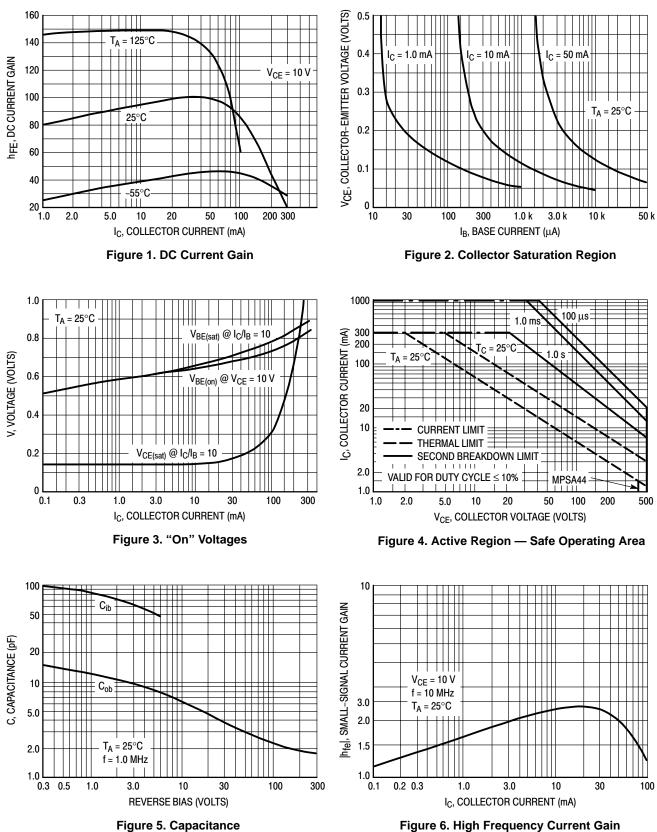


Figure 6. High Frequency Current Gain

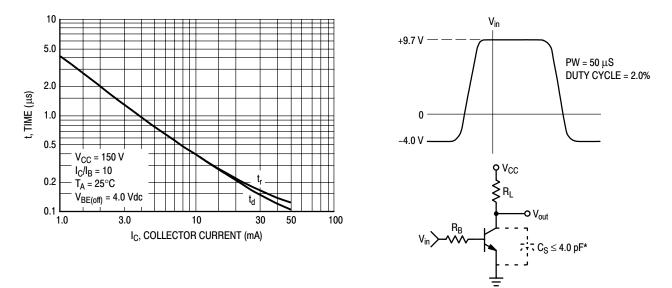
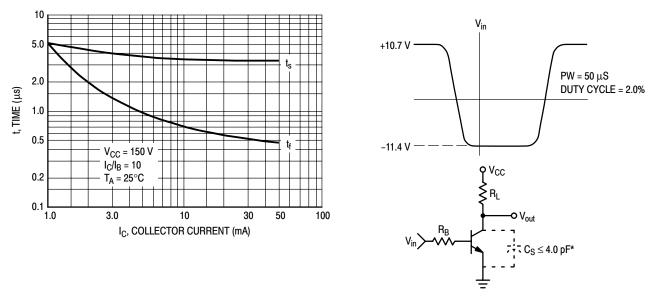


Figure 7. Turn–On Switching Times and Test Circuit



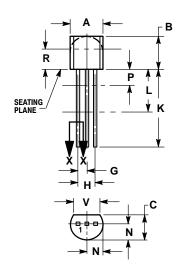


*Total Shunt Capacitance or Test Jig and Connectors.

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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL





STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

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<u>Notes</u>

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