



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

VOLTAGE 60 Volts CURRENT 36 Ampere

**CHM50N06PAPT**

Lead free devices

**APPLICATION**

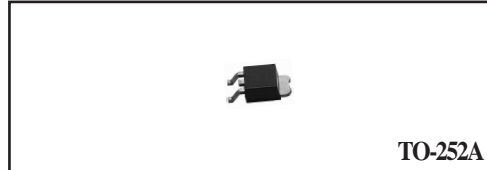
- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

**FEATURE**

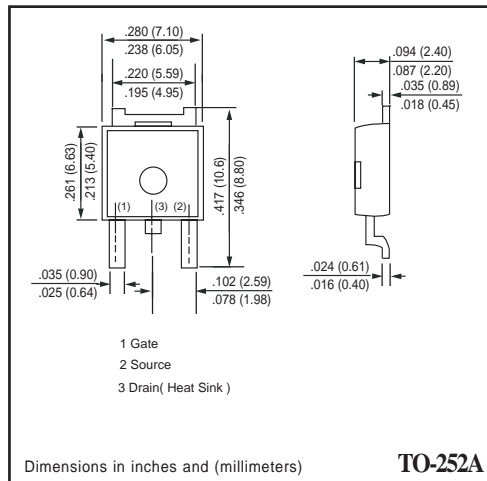
- \* Small package. (TO-252A)
- \* Super high dense cell design for extremely low  $R_{DS(ON)}$ .
- \* High power and current handling capability.

**CONSTRUCTION**

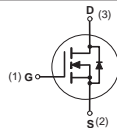
- \* N-Channel Enhancement



**TO-252A**



**CIRCUIT**



**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	CHM50N06PAPT	Units
$V_{DSS}$	Drain-Source Voltage	60	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Maximum Drain Current - Continuous	36	A
	- Pulsed (Note 3)	105	
$P_D$	Maximum Power Dissipation at $T_c = 25^\circ\text{C}$	68	W
$T_J$	Operating Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

- Note : 1. Surface Mounted on FR4 Board ,  $t \leq 10\text{sec}$   
 2. Pulse Test , Pulse width  $\leq 300\mu\text{s}$  , Duty Cycle  $\leq 2\%$   
 3. Repetitive Rating , Pulse width limited by maximum junction temperature  
 4. Guaranteed by design , not subject to production trsting

**Thermal characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1)	50	$^\circ\text{C/W}$
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## RATING CHARACTERISTIC CURVES ( CHM50N06PAPT )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	60			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
$I_{GSSR}$	Gate-Body Leakage	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2		4	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 15\text{ A}$		18	23	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 15\text{ A}$		15		S

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{DS} = 48\text{ V}, I_D = 50\text{ A}$ $V_{GS} = 10\text{ V}$		31	40	nC
$Q_{gs}$	Gate-Source Charge			8		
$Q_{gd}$	Gate-Drain Charge			13		
$t_{on}$	Turn-On Time	$V_{DD} = 30\text{ V}$ $I_D = 50\text{ A}, V_{GS} = 10\text{ V}$ $R_{GEN} = 3.6\ \Omega$		21	45	nS
$t_r$	Rise Time			13	33	
$t_{off}$	Turn-Off Time			40	80	
$t_f$	Fall Time			9	27	

### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_S$	Drain-Source Diode Forward Current	(Note 1)			35	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$I_S = 15\text{ A}, V_{GS} = 0\text{ V}$ (Note 2)			1.5	V