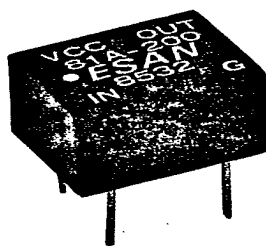


Active Delay Line (TTL Interface)

81A Series

- ULTRA-Miniature Package
- 8 Pin Dip Package
- Single Output

**Specifications:**

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp coefficient : 100 ppm/ $^{\circ}\text{C}$
- Operating Temp. Range : -0°C to 70°C
(-55°C to $+125^{\circ}\text{C}$ on request)
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : $5\text{Vcc} \pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

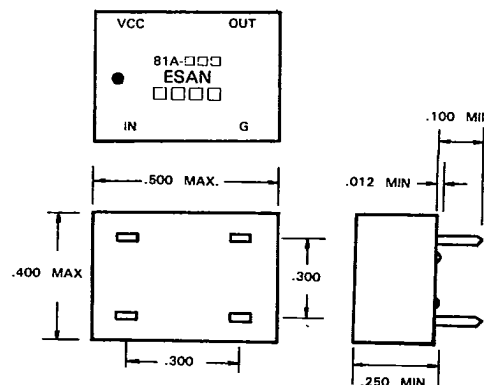
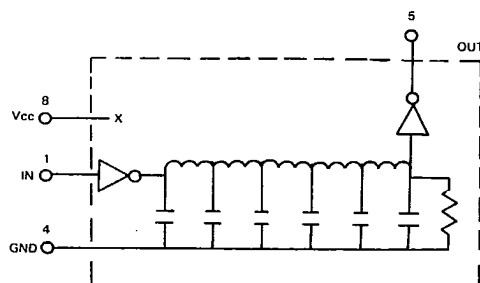
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times$ total delay
- Input pulse spacing : $2 \times$ total delay
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @ $V_{\text{cc}} = 5.0\text{V}$, $T_A = 25^{\circ}\text{C}$

Features:

- Schottky TTL interface
- Low power schottky available named "81AL- xxx" series
- Both Leading and Trailing edge available under request named "81B- xxx" series
- Ceramic IC meet MIL-STD 883 level B available named "81AM- xxx" series

PART NO.	DELAY TIME (ns)
81A-025	25
81A-050	50
81A-075	75
81A-100	100
81A-125	125
81A-150	150
81A-200	200
81A-250	250
81A-300	300
81A-400	400
81A-500	500



Unit: Inch