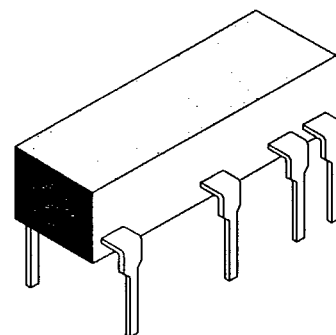


AKL-SERIES

T-47-13

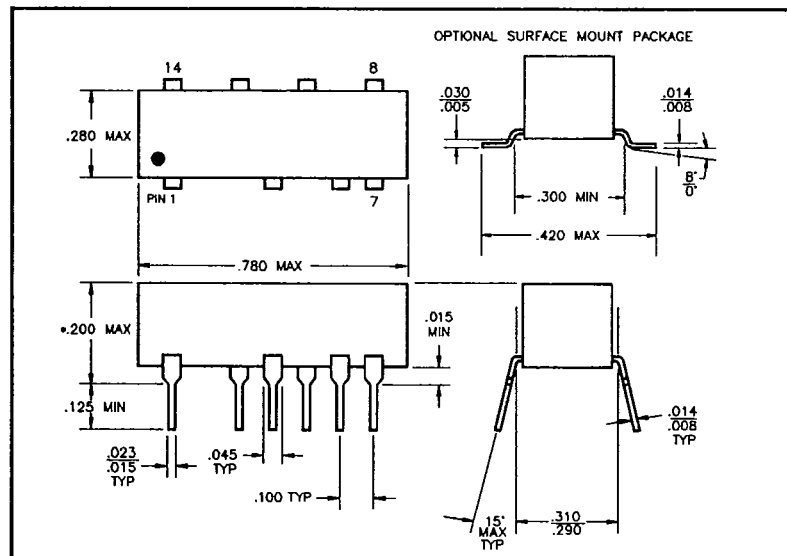
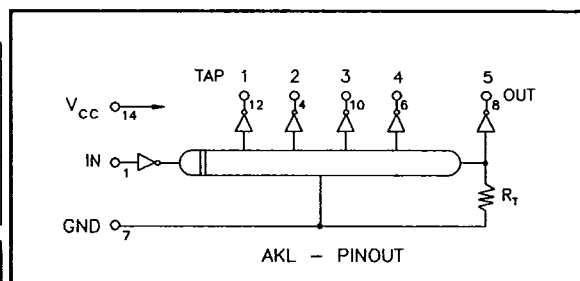
**LOW PROFILE AUTO-INSERTABLE
14 PIN, LOW POWER 5 TAP TTL ACTIVES****POLARA**

DC Electrical Characteristics		Test Conditions	Min	Max	Unit
VOH	High-Level Output Voltage	VCC=min. VIL=max. IOH=max	2.7		V
VOL	Low Level Output Voltage	VCC=min. VIH=min. IOL=max		0.5	V
VIK	Input Clamp Voltage	VCC=min. II=IIK		-1.5	V
IIH	High-Level Input Current	VCC=max. VIN=2.7V		20	uA
IIl	Low-Level Input Current	VCC=max. VIN=5.25V		0.1	mA
IOS	Short Circuit Output Current	VCC=max. VIN=0.5V		-0.36	mA
		VCC=max. VOUT=0 (One output at a time)	-5	-42	mA
ICCH	High-Level Supply Current	VCC=max. VIN=OPEN		30	mA
ICCL	Low-Level Supply Current	VCC=max. VIN=0		30	mA
TRO	Output Rise Time	Td ≤ 500ns(0.75 to 2.4 Volts)		8	ns
		Td > 500ns		8	ns
NH	Fanout High-Level Output	VCC=max. VOH=2.7V		20 TTL LOAD	
NL	Fanout Low-Level Output	VCC=max. VOL=0.5V		10 TTL LOAD	

Recommended Operating Conditions		Min	Max	Unit
VCC	Supply Voltage	4.75	5.25	V
VIH	High-level Input voltage	2.0		V
VIL	Low-Level Input Voltage		0.8	V
IIK	Input Clamp Current		-18	mA
IOH	High-Level Output Current		-0.4	mA
IOL	Low-Level Output Current		8	mA
PW*	Pulse Width of Total Delay	20		%
d*	Duty Cycle	0	40	%
TA	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent Min. PW for Total Delays greater than 250 ns is 40%

Input Pulse Test Conditions @ 25°C		Unit
EIN	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	110 %
TRI	Pulse Rise Time(0.75 - 2.4 Volts)	2.0 ns
PRR	Pulse Repetition Rate @ Td ≤ 500ns	1.0 MHz
	Pulse Repetition Rate @ Td > 500 ns	500 KHz
VCC	Supply Voltage	5.0 Volts



TOTAL DELAY ns ± 5%	TAP DELAYS ± 2 ns OR 5%	POLARA P/N AKL - PINOUT
50	10	AKL-0050
60	12	AKL-0060
75	15	AKL-0075
100	20	AKL-0100
125	25	AKL-0125
150	30	AKL-0150
175	35	AKL-0175
200	40	AKL-0200
225	45	AKL-0225
250	50	AKL-0250
300	60	AKL-0300
350	70	AKL-0350
400	80	AKL-0400
450	90	AKL-0450
500	100	AKL-0500
600	120	AKL-0600
700	140	AKL-0700
800	160	AKL-0800
900	180	AKL-0900
1000	200	AKL-1000

ADD "-S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.

DELAYS TIMES REFERENCED FROM INPUT.

*FOR DELAYS ABOVE 250 NS, HEIGHT IS .250 MAX.