

DUAL HIGH CURRENT LOW SIDE DRIVER

- HIGH OUTPUT CURRENT
- INPUT COMPARATOR WITH WIDE RANGE COMMON MODE OPERATION AND GROUND COMPATIBLE INPUTS
- INPUT COMPARATOR HYSTERESIS
- SHORT CIRCUIT PROTECTION WITH SOA PROTECTION OF OUTPUT
- INTERNAL THERMAL PROTECTION WITH HYSTERESIS
- SINGLE SUPPLY VOLTAGE (3.5 V to 28V)

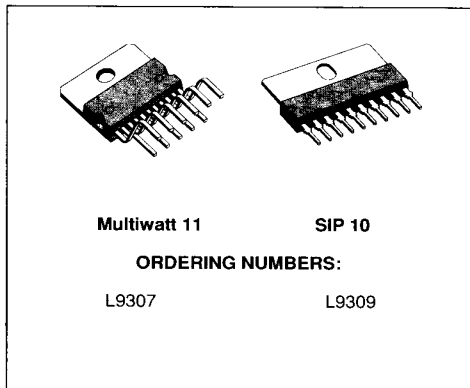
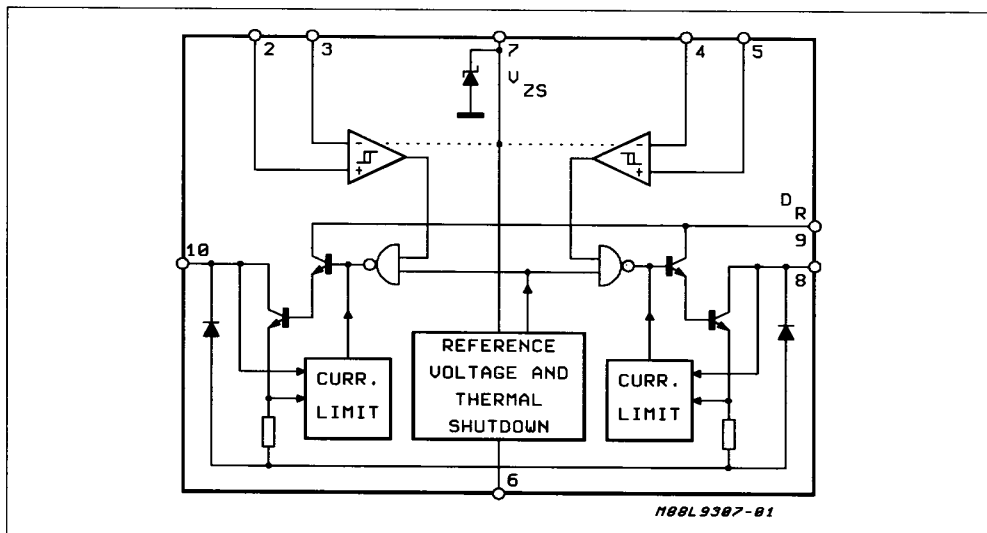
DESCRIPTION

The L9307/9 is a monolithic integrated circuit with differential input comparator and open collector output able to sink high current specially to drive relays, lamps, d.c. motors.

Particular care has been taken to protect the device against destructive failures, i.e. short circuit of outputs to V_s , SOA protection, supply overvoltage.

A built in thermal shut-down switches off the device when the IC's internal dissipation becomes

BLOCK DIAGRAM



too high and the chip temperature exceeds security threshold.

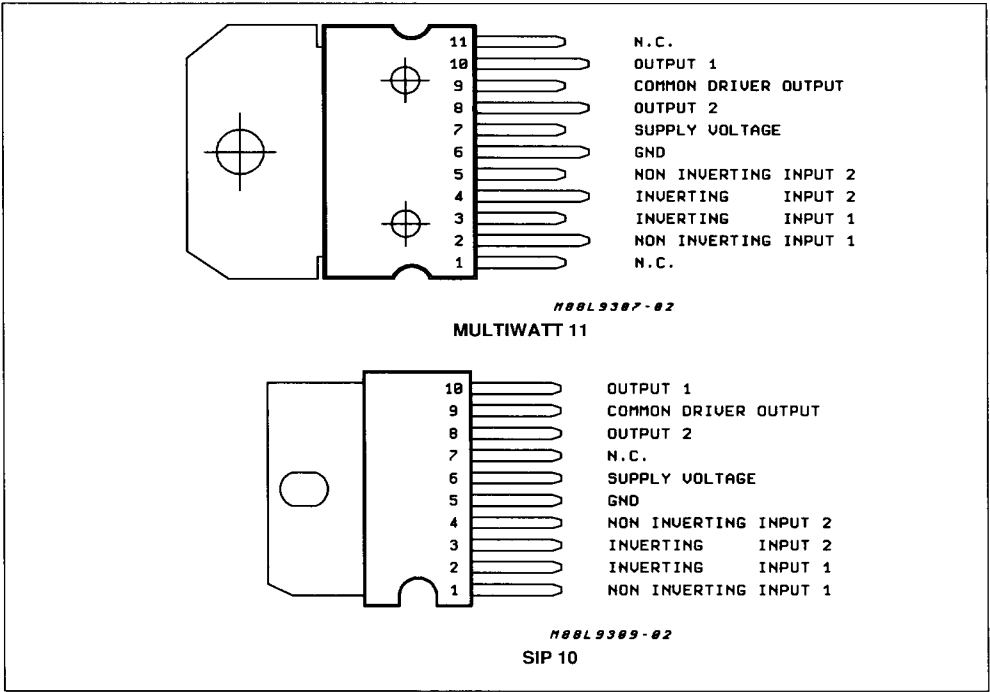
The input comparator hysteresis increases the interface's noise immunity allowing the correct use critical environments as automotive applications.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
I_{ZS}	Current Into Supply Clamp Zener Diode DC Conditions Pulsed: $T_{on} < 2.5ms$; $d < 8\%$	30	mA
		80	mA
V_S	Supply Voltage	28	V
I_O	Output Current	Internally Limited	°C
T_{JL}, T_{stg}	Junction and Storage Temperature Range	- 55 to 150	°C
$V_O 1,2$	Output Voltage	- 0.3 to 28	V
P_{tot}	Power Dissipation at $T_{amb} = 85^{\circ}C$ for Multiwatt 11 for SIP 10	1.7	W
		1.3	W

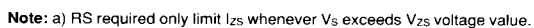
(*) T_{ON} 2.5ms; repetition time $\leq 30ms$
(**) The maximum allowed supply voltage without limiting resistor is limited by the built-in protection zener diode; see V_{ZS} spec. values if V_S higher than V_{ZS} a resistor R_S is necessary to limit the zener current I_{ZS} .

PIN CONNECTIONS (Top view)



THERMAL DATA

Symbol	Parameter	Multiwatt	SIP	Unit
$R_{th j-amb}$	Thermal Resistance Junction-ambient	Max. 38	50	°C/W
$R_{th j-case}$	Thermal Resistance Junction-case	Max. 3	10	°C/W



ELECTRICAL CHARACTERISTICS ($V_S = 14.4V$; $T_{amb} = -40^{\circ}C$ to $85^{\circ}C$; $R_S = 100\Omega$ Refer to the block diagram, unless otherwise specified.)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_{IH}	Hysteresis of the Input Comparator	$V_{in} = 200mV_{pp}$; $f = 1KHz$	20		80	mV
I_b	Input Bias Current			0.2	1	μA
I_{OS}	Input Offset Current			± 50	± 400	nA
CMR	Input Common Mode Range	$V_S = 6$ to $18V$	0		$V_{ST} - 1.6$	V
I_{SC}	Output Short Circuit Channel (typ. SOA curve, see fig.2)	$V_i - V_i^* > 70mA$ $V_{out\ 1, 2} = 16V$ $T_{amb} = 25$ to $85^{\circ}C$ $T_{amb} = -40$ to $25^{\circ}C$ $V_{out\ 1, 2} = 6V$			0.8 0.9 2.5	A A A
I_D	Driver Transistor Current Capability	$V_i - V_i^* > 70mA$ $V_S = 6 - 16V$ DC Conditions Pulsed: $T_{on} = 2.5ms$; $d < 8\%$			300 600	mA mA
$I_{O\ 1, 2}$	Output Current for Each Channel	(see fig. 2) $V_{out\ 1, 2} < 2V$; $V_i - V_i^* > 70mV$ $I_d = 100mA$	1.5			A
V_{Csat}	On Status Saturation Voltage	$V_i - V_i^* > 70mV$ $I_d = 100mA$ $I_{out\ 1, 2} = 1.2A$			1.2	V
I_{OL}	Output Leakage Current	$V_i^* - V_i > 70mV$ $V_S = 18V$		10	250	μA
V_{st}	Supply Voltage (pin 7)		3.5		18	V
$I_{st.by}$	Supply Current	$V_i^* - V_i > 70mV$		5	8	mA
I_{ON}	Supply Current	$V_i^* - V_i > 70mV$		18		mA
V_{zs}	Voltage Clamp Supply Protection	$I_{zs} = 10mA$	20		27	V

Figure 2: SOA Protection.

