

Dual H-Driver For Instrumentation

Electrical Characteristics: $T_A = 25^\circ\text{C}$, $V_{CC} = 16\text{V}$, except as noted

Type	Characteristic	Symbol	Limits			Units
			Minimum	Typical	Maximum	
CA3275	Operating Supply Voltage Range	V_{CC}	8	-	16	V
	Supply Current, Note 1	I_{CC}	-	8	20	mA
	Input Levels:					
	Logic Input, Low Voltage	V_{IL}	-	-	0.8	V
	Logic Input, High Voltage	V_{IH}	3.5	-	-	V
	Logic Input, Low Current, $V_{IH} = 0\text{V}$	I_{IL}	-10	-	-	μA
	Logic Input, High Current, $V_{IH} = 5\text{V}$	I_{IH}	-	-	10	μA
	Output: $R_{LA} = R_{LB} = 138\Omega$					
	Maximum Source Saturated Voltage	$V_{SAT} - \text{High}$	-	1.2	1.75	V
	Maximum Sink Saturated Voltage	$V_{SAT} - \text{Low}$	-	0.25	0.5	V
	Diff. V_{SAT} Voltage, Both Outputs Saturated	$\text{Diff} - V_{SAT}$	-	10	100	mV
	Propagation Delay: $T_A = 25^\circ\text{C}$					
	Source Current					
	Turn-Off Delay	t_{sc-off}	-	-	2	μs
	Fall Time	t_{sc-f}	-	-	2.2	μs
	Turn-On Time	t_{sc-on}	-	-	1	μs
	Rise Time	t_{sc-r}	-	-	0.4	μs
	Sink Current					
	Turn-Off Delay	t_{sk-off}	-	-	1.6	μs
	Fall Time	t_{sk-f}	-	-	0.4	μs
	Turn-On Delay	t_{sk-on}	-	-	0.6	μs
	Rise Time	t_{sk-r}	-	-	0.2	μs

Note 1: No load, $PWMA = PWMB = 5\text{V}$, $DIRA = DIRB = 0\text{V}$.

Voltage Regulator With Reverse Battery Protection

Functional Operating Ranges at $T_A = 25^\circ\text{C}$, $V_{IN} = 5.7$ to 16V Volts V_{CC1} & V_{CC2} Filter Capacitors = $3.3\mu\text{F}$, $CRST = 0.47\mu\text{F}$, unless otherwise specified.

Type	Characteristic	Symbol	Limits		Units
			Minimum	Maximum	
CA3276	Standby Regulator:				
	Output Voltage	V_{CC1}	4.75	5.25	V
	Output Current	I_{CC1}	-	50	mA
	Dropout Voltage:				
	$V_{IN} = 4.75\text{V}$, $I_{CC1} = 50\text{mA}$	$V_{IN} - V_{CC1}$	-	0.6	V
	Output Clamp	V_{CP1}	-	6	V
	Power Supply Rejection Ratio	$PSSR$	-48	-	dB
	Enable Input High	V_{IH}	3.5	$V_{CC} - 0.5$	V
	Enable Input Low	V_{IL}	-0.5	0.9	V
	Enable Regulator:				
	Output Voltage	V_{CC2}	4.75	5.25	V
	Output Current	I_{CC2}	-	100	mA
	Enable Input High	V_{IH}	3.5	$V_{CC} - 0.5$	V
	Enable Input Low	V_{IL}	-0.5	0.9	V
	Output Clamp	V_{CP2}	-	6	V
	I_{SK} Saturation Voltage: $I_{SK} = 100\text{mA}$	V_{SAT}	-	0.5	V
	Low Voltage Reset ($V_{ARY} V_{IN}$)	V_{IN}	4	4.5	V
	Reset Output Low: $RST = 300\Omega$ to 5V , $V_{IN} = 4\text{V}$	V_{RST}	-	0.8	V
	Reset Delay Time: ($C_{rst} = 0.47\mu\text{F}$; $V_{IN} = 10\text{V}$ step turn-on meas. V_{CC1})	T_{RST}	100	200	ms