TRIACS

Glass-passivated 4 Ampere triacs intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance with very low thermal resistances. These triacs feature a high surge current capability and a range of gate current sensitivities between 5 and 50 mA. Typical applications include AC power control circuits such as motor, industrial lighting, industrial and domestic heating control and static switching systems.

QUICK REFERENCE DATA

			BT134–500	600	700	800	
Repetitive peak off-state voltage	V_{DRM}	max.	500	600	700	800	V
RMS on-state current	$I_{T(RMS)}$	max.		4			A
Non-repetitive peak on-state current							
at 50 Hz	I_{TSM}	max.		25			A
at 60 Hz	I_{TSM}	max.		30			A

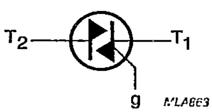
MECHANICAL DATA

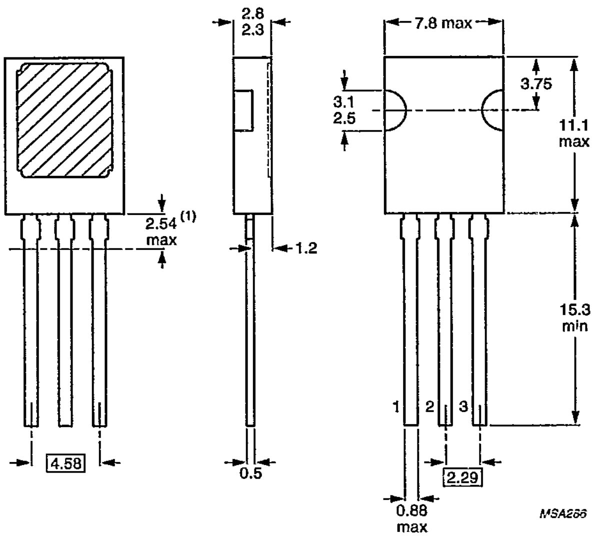
Dimensions in mm

Fig.1 SOT-82.

Pinning:

1 = Terminal 1
2 = Terminal 2
3 = Gate





(1) Within this region the cross-section of leads is uncontrolled.

Net mass: 0.8 g.
Note: The exposed metal mounting base is directly connected to terminal T_2.
Supplied on request: accessories (see data sheets Mounting instructions and accessories for SOT-82 envelopes).

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134).

Voltages (in either direction)			BT134–500	600	700	800	
Non-repetitive peak off-state voltage ($t \leqslant 10$ ms)†	V_{DSM}	max.	500*	600*	700*	800	V
Repetitive peak off-state voltage ($\delta \leqslant 0.01$)†	V_{DRM}	max.	500	600	700	800	V
Crest working off-state voltage	V_{DWM}	max.	400	400	400	400	V

Currents (in either direction)

RMS on-state current (conduction angle 360°) up to T_{mb} = 102 °C	$I_{T(RMS)}$	max.	4	A
Average on-state current for half-cycle operation (averaged over any 20 ms period) up to T_{mb} = 92 °C	$I_{T(AV)}$	max.	2.5	A
Repetitive peak on-state current	I_{TRM}	max.	25	A
Non-repetitive peak on-state current; T_j = 120 °C prior to surge; full sinewave				
t = 20 ms	I_{TSM}	max.	25	A
t = 16.7 ms	I_{TSM}	max.	30	A
I^2t for fusing (t = 10 ms)	I^2t	max.	4	A^2s
Rate of rise of on-state current after triggering with I_G = 200 mA to I_T = 6 A; dI_G/dt = 0.2 A/µs	dI_T/dt	max.	10	A/µs

Gate to terminal 1

Power dissipation

Average power dissipation (averaged over any 20 ms period)	$P_{G(AV)}$	max.	0.5	W
Peak power dissipation	P_{GM}	max.	5	W

Temperatures

Storage temperature	T_{stg}		−40 to +125	°C
Operating junction temperature				
full-cycle operation	T_j	max.	120	°C
half-cycle operation	T_j	max.	110	°C

†For BT134-500D/600D use $R_{(G\text{-}T_1)}$ = 1 kΩ.

*Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch into the on-state. The rate of rise of on-state current should not exceed 3 A/µs.

THERMAL RESISTANCE

From junction to mounting base				
full-cycle operation	$R_{th\ j\text{-}mb}$	=	3.0	K/W
half-cycle operation	$R_{th\ j\text{-}mb}$	=	3.7	K/W
Transient thermal impedance; t = 1 ms	$Z_{th\ j\text{-}mb}$	=	0.6	K/W

Influence of mounting method

1. Heatsink mounted with clip (see mounting instructions)

Thermal resistance from mounting base to heatsink

a. with heatsink compound	$R_{th\ mb\text{-}h}$	=	0.3	K/W
b. with heatsink compound and 0.06 mm maximum mica insulator (56354)	$R_{th\ mb\text{-}h}$	=	1.4	K/W
c. without heatsink compound	$R_{th\ mb\text{-}h}$	=	1.4	K/W

2. Free-air operation

The quoted value of $R_{th\ j\text{-}a}$ should be used only when no leads of other dissipating components run to the same tie-point.

Thermal resistance from junction to ambient in free air: mounted on a printed-circuit board at any lead length	$R_{th\ j\text{-}a}$	=	100	K/W

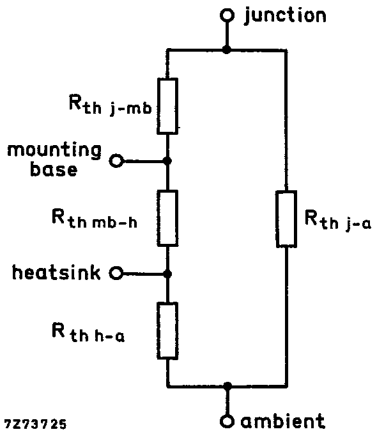


Fig.2 Components of thermal resistance.

CHARACTERISTICS

Polarities, positive or negative, are identified with respect to T_1.

Voltages and currents (in either direction)

On-state voltage (measured under pulse conditions to prevent excessive dissipation)				
I_T = 5 A; T_j = 25 °C	V_T	<	1.70	V
Rate of rise of off-state voltage that will not trigger any device; T_j = 120 °C; gate open circuit				
BT134 series	dV_D/dt	<	100	V/µs
BT134 series G	dV_D/dt	<	200	V/µs
BT134 series F	dV_D/dt	<	50	V/µs
BT134 series E	dV_D/dt	typ.	50	V/µs
BT134–500D/600D	dV_D/dt*	typ.	5	V/µs
Rate of change of commutating voltage that will not trigger any device when $-dI_{com}/dt$ = 1.8 A/ms; $I_{T(RMS)}$ = 4 A; T_{mb} = 85 °C; gate open circuit; $V_D = V_{DWMmax}$				
BT134 series	dV_{com}/dt	typ.	10	V/µs
BT134 series G	dV_{com}/dt	<	10	V/µs
BT134 series F	dV_{com}/dt	typ.	10	V/µs
Off-state current				
$V_D = V_{DWMmax}$; T_j = 120 °C	I_D	<	0.5	mA
Gate voltage that will trigger all devices	V_{GT}	>	1.5	V
Gate voltage that will not trigger any device $V_D = V_{DWMmax}$; T_j = 120 °C; T_2 and G positive or negative	V_{GD}	<	250	mV

Gate current that will trigger all devices (I_{GT}); G to T_1

Holding current (I_H) Latching current (I_L); V_D = 12 V; T_j = 25 °C			T_2+ G+	T_2+ G−	T_2− G−	T_2− G+	
BT134 series	I_{GT}	>	35	35	35	70	mA
	I_H	<	15	15	15	15	mA
	I_L	<	20	30	20	30	mA
BT134 series G	I_{GT}	>	50	50	50	100	mA
	I_H	<	30	30	30	30	mA
	I_L	<	30	45	30	45	mA
BT134 series F	I_{GT}	>	25	25	25	70	mA
	I_H	<	15	15	15	15	mA
	I_L	<	20	30	20	30	mA
BT134 series E	I_{GT}	>	10	10	10	25	mA
	I_H	<	15	15	15	15	mA
	I_L	<	15	20	15	20	mA
BT134–500D/600D	I_{GT}	>	5	5	5	10	mA
	I_H	<	10	10	10	10	mA
	I_L	<	10	15	10	15	mA

*$R_{(G\text{-}T_1)}$ = 1 kΩ.

MOUNTING INSTRUCTIONS

1. The device may be soldered directly into the circuit. The maximum permissible soldering temperature is 275 °C. Heat must not be applied for more than 5 seconds. Soldered joints must be at least 4.7 mm from the body of the device.
2. The leads must not be bent less than 2.4 mm from the body of the device and should be supported during bending. The leads can be bent, twisted or straightened by 90° maximum. The minimum bending radius is 1 mm.
3. Any heatsink used must have a flatness in the mounting area of 0.02 mm maximum per 10 mm. Mounting holes must be deburred.
4. For spring-clip mounting a metallic-oxide loaded heatsink compound must be used to ensure good thermal contact between the mounting-base and heatsink. Ordinary silicone grease is not recommended.
5. Body mounting of the device into a hybrid circuit is possible either by soldering or by adhesive. For soldering either a copper-plate or anodized aluminium-plate with a copper layer are recommended. The device can be adhesively mounted to a ceramic substrate.
6. Any solder or adhesive layer used for body mounting must be as thin as possible with all air voids eliminated. This prevents excessive heat rise within the device.

OPERATING NOTES

Dissipation and heatsink considerations:

a. The method of using Fig.3 is as follows:
Starting with the required current on the $I_{T(AV)}$ or $I_{T(RMS)}$ axis, trace upwards to meet the appropriate form factor or conduction angle curve. Trace horizontally and upwards from the appropriate value on the T_{amb} scale. The intersection determines the $R_{th\ mb\text{-}a}$. The heatsink thermal resistance value ($R_{th\ h\text{-}a}$) can now be calculated from:

$R_{th\ h\text{-}a} = R_{th\ mb\text{-}a} - R_{th\ mb\text{-}h}$.

b. Any measurement of heatsink temperature should be made immediately adjacent to the device.

FULL-CYCLE OPERATION

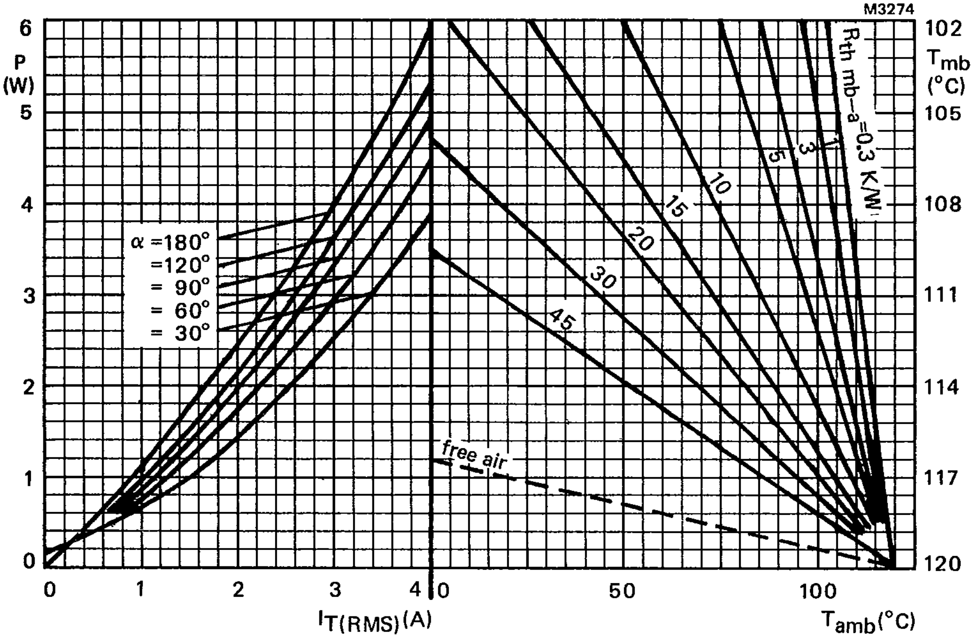
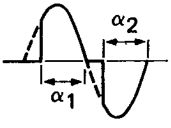


Fig.3 The right-hand part shows the interrelationship between the power (derived from the left-hand part) and the maximum permissible temperatures.



$\alpha = \alpha_1 = \alpha_2$: conduction angle per half cycle

OVERLOAD OPERATION

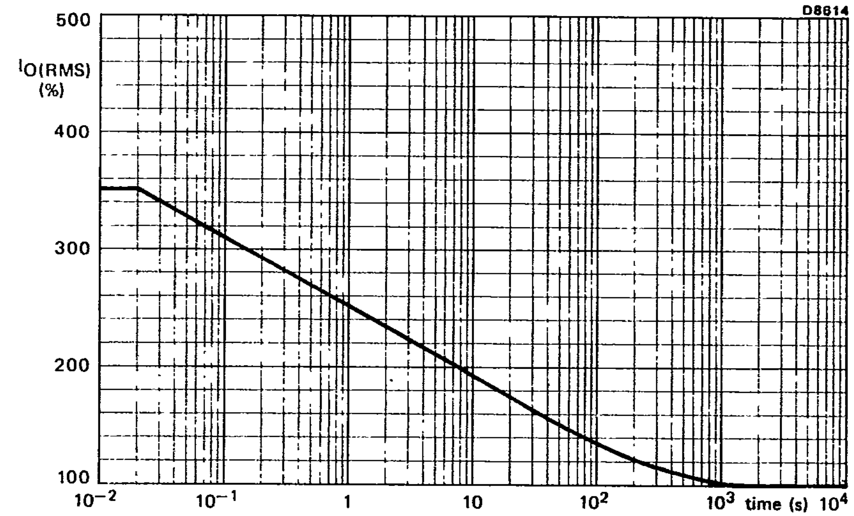


Fig.4 Maximum permissible duration of steady overload (provided that T_{mb} does not exceed 120 °C during and after overload) expressed as a percentage of the steady state r.m.s. rated current. For high r.m.s. overload currents precautions should be taken so that the temperature of the terminals does not exceed 125 °C. During these overload conditions the triac may lose control. Therefore the overload should be terminated by a separate protection device.

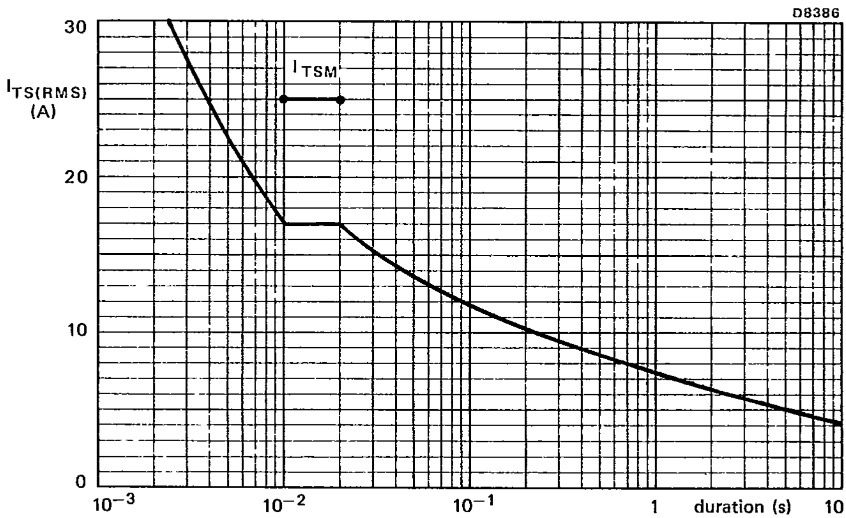
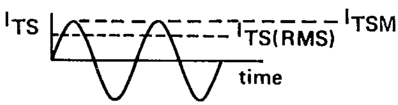


Fig.5 Maximum permissible non-repetitive r.m.s. on-state current based on sinusoidal currents (f = 50 Hz); T_j = 120 °C prior to surge. The triac may temporarily lose control following the surge.



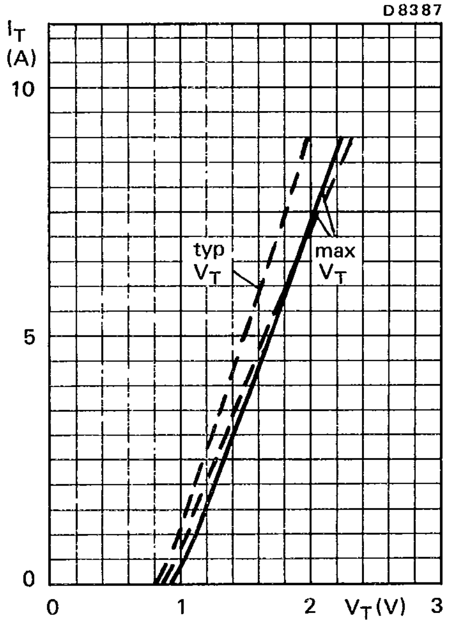


Fig.6 On state voltage drop.

—— T_j = 25 °C; – – – T_j = 120 °C

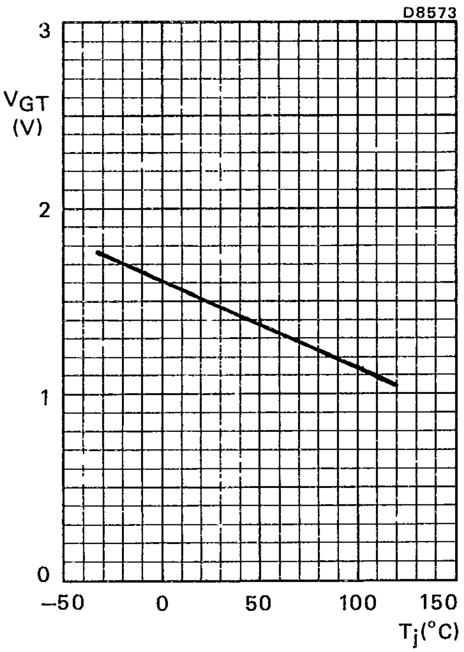


Fig.7 Minimum gate voltage that will trigger all devices; all conditions.

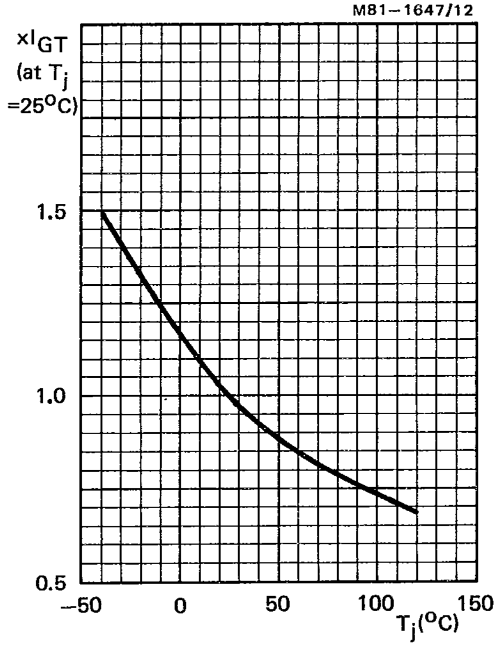


Fig.8 Normalised gate current that will trigger all devices; all conditions.

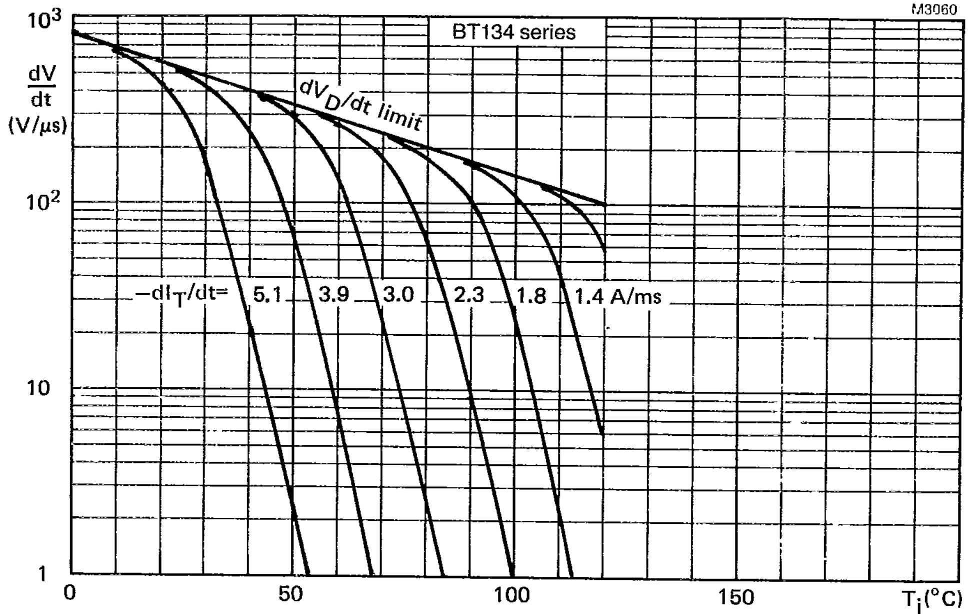


Fig.9 Typical commutation dV/dt for BT134 series versus T_j. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dI_T/dt.

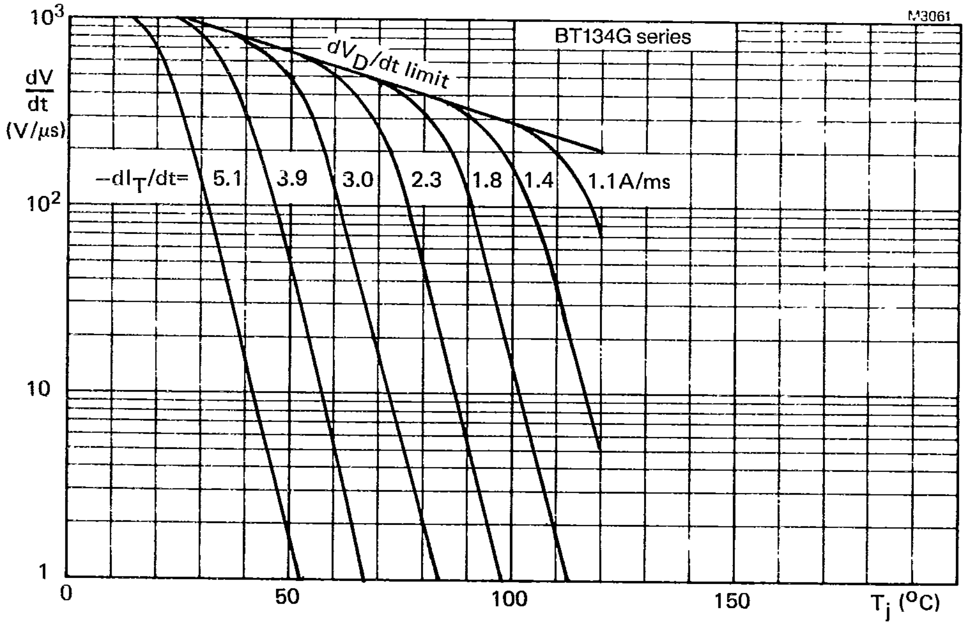


Fig.10 Limit commutation dV/dt for BT134G series versus T_j. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dI_T/dt.

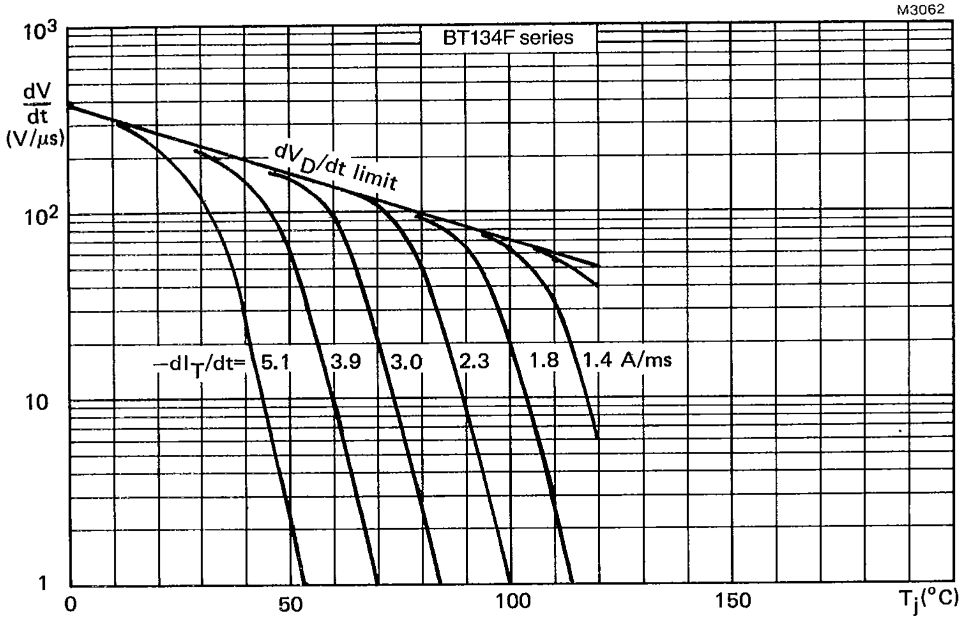


Fig.11 Typical commutation dV/dt for BT134F series versus T_j. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dI_T/dt.

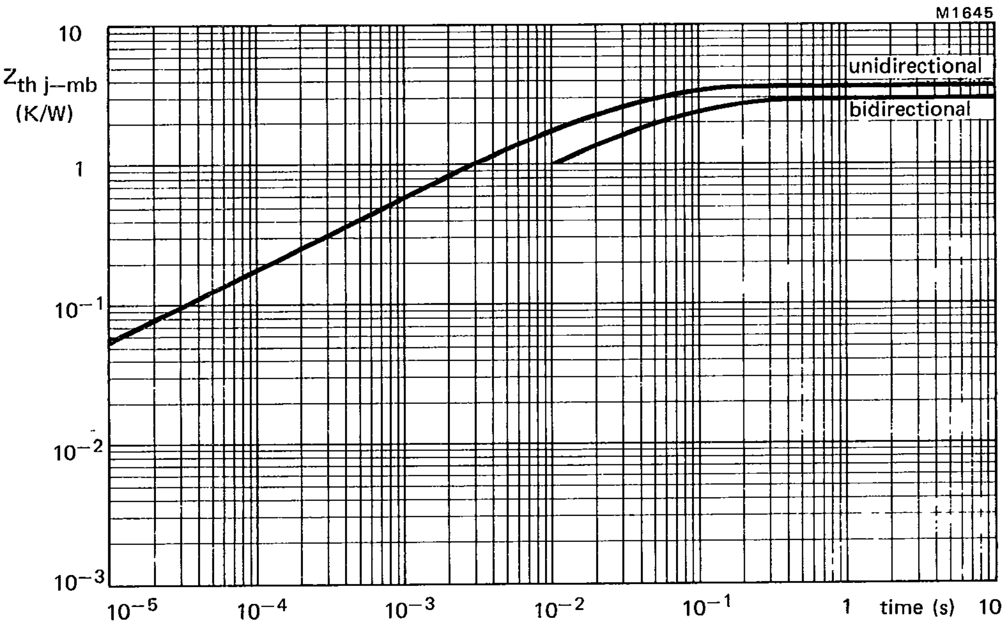


Fig.12 Transient thermal impedance.