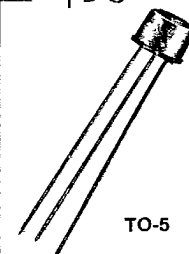


5 AMP/NPN-PNP**Pirgo Silicon Planar
Power Transistors**

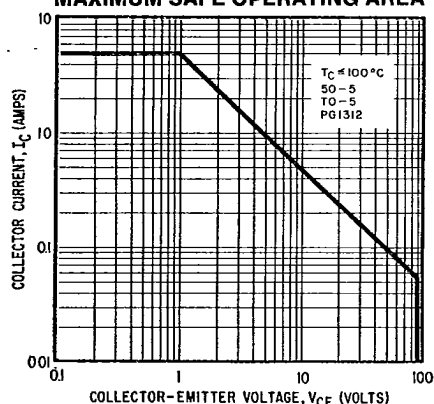
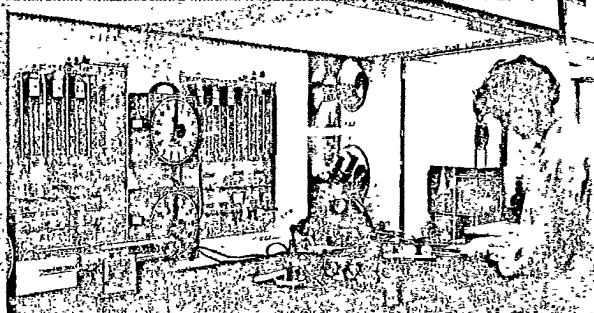
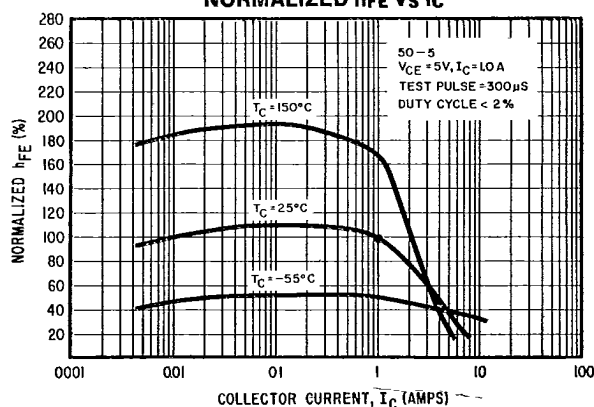
**5 AMP
90 VOLT
5 WATT
80 MHz
TO-5**



TO-5

- Linear h_{FE} from 10 mA to 5 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 80$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 90 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}		h_{FE} @ $I_C A$	$V_{CE(sat)}$ Max @ $I_C A$	C_{obMax} pf	P_{DW} @ 100° C	PNP Complement
					Min	Max					
2N2657	TO-5	70	50	8	40	120	1	15	5	4	PG2166
2N2658	TO-5	90	70	8	40	120	1	15	5	4	PG2167
PG1150	TO-5	120	90	8	40	120	1	15	5	5	PG2150
PG1151	TO-5	70	50	8	20	60	1	10	5	5	PG2151
PG1152	TO-5	90	70	8	20	60	1	10	5	5	PG2152
PG1153	TO-5	120	90	8	20	60	1	10	5	5	PG2153
PG1154	TO-5	70	50	8	100	300	1	20	5	5	PG2154
PG1155	TO-5	90	70	8	100	300	1	20	5	5	PG2155
PG1156	TO-5	120	90	8	100	300	1	20	5	5	PG2156
PG1157	TO-5	70	50	6	20	60	1	.7	1	5	PG2157
PG1158	TO-5	90	70	6	20	60	1	.7	1	5	PG2158
PG1159	TO-5	120	90	6	20	60	1	.7	1	5	PG2159
PG1160	TO-5	70	50	6	40	120	1	.7	1	5	PG2160
PG1161	TO-5	90	70	6	40	120	1	.7	1	5	PG2161
PG1162	TO-5	120	90	6	40	120	1	.7	1	5	PG2162
PG1163	TO-5	70	50	6	100	300	1	.7	1	5	PG2163
PG1164	TO-5	90	70	6	100	300	1	.7	1	5	PG2164
PG1165	TO-5	120	90	6	100	300	1	.7	1	5	PG2165

MAXIMUM SAFE OPERATING AREA**NORMALIZED h_{FE} Vs I_C** 

Diffusing and inspecting slices



Aligning



Evaporating contact metallization