

**Qualification Test Report
on Si MMIC
(use UHS0 Process)**

Prepared on : June 26, 2003

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1. Introduction

This report presents UHS0 (fT=25GHz) Process Qualification Test result. The Process Qualification Test was performed by UPC8182B(B).

2. Qualification Test items and failure criteria

- 2.1 Thermal Environmental Test (Table. 1,2)
- 2.2 Mechanical Environmental Test (Table. 1,2)
- 2.3 High Temperature DC Bias Test (Table. 1,2)

3. Result

3.1 Thermal and Mechanical Environmental Test

As shown Table 3, no failure was observed with respect to thermal environmental test and mechanical environmental test.

3.2 High Temperature DC Bias Test

High temperature DC bias test at $T_a=200^{\circ}\text{C}$ was performed for UPC8182B(B) using 100 samples. The test was performed for 3000 hours. The test results are shown Table 3. No failure has been observed for 3000 hours. ΔI_{cc} change is shown in Fig.1.

Table 1 Test Item and Test Condition

Test Items	Test Condition (MIL-STD 883 Method)	Sample Size
Thermal Environmental Test a) Solderability b) Temperature Cycling c) Thermal Shock d) Moisture Resistance e) Hermetic Seal	2003 1010: Cond.D -65°C ~ +200°C, 100cycles 1011: Cond.A 0°C ~ +100°C, 15cycles 1004: Omit initial conditioning 1071 Fine Leak (Cond.A1) $\sim 1 \times 10^{-9}$ Pa m ³ /s ($\sim 1 \times 10^{-8}$ atm cc/sec) Gross Leak (Cond.C) no stream bubble	8
Mechanical Environmental Test a) Mechanical Shock b) Vibration, Variable Frequency c) Constant Acceleration d) Hermetic Seal	2002: 1.47×10^4 m/s ² (1500G), 0.5ms, 3axis, 5times 2007: 100 ~ 2000Hz, 196m/s ² (20G), 3axis, 4min, 4times 2001: 1.96×10^5 m/s ² (20000G), 3axis, 1min., 1time 1071 Fine Leak (Cond.A1) $\sim 1 \times 10^{-9}$ Pa m ³ /s ($\sim 1 \times 10^{-8}$ atm cc/sec) Gross Leak (Cond.C) no stream bubble	8
High Temperature DC Bias Test	1005: Ta=200°C, Vcc=3V, t=3000Hrs	100

Table 2 Parameters and Criteria

Parameters	Symbols	Test Condition	Limits		Delta Criteria
			Min	Max	
Circuit Current	I _{cc}	V _{cc} =3V (no signal)	—	38mA	±15%
Power Gain	G _{p1}	V _{cc} =3V, f=0.9GHz	19dB	25dB	—
	G _{p2}	V _{cc} =3V, f=1.9GHz	18dB	24dB	—
	G _{p3}	V _{cc} =3V, f=2.4GHz	18dB	24dB	—
Output Power	P _{out}	V _{cc} =3V, f=2.4GHz P _{in} =-5dBm	7dBm	—	—
Noise Figure	NF	V _{cc} =3V, f=2.4GHZ	—	6.5dB	—

Table 3 Qualification Test Results

Test Items	Results (failure/sample)	Reference
Thermal Environmental Test	0/8	—
Mechanical Environmental Test	0/8	—
High Temp. DC Bias Test	0/100 (at 3000Hrs)	—

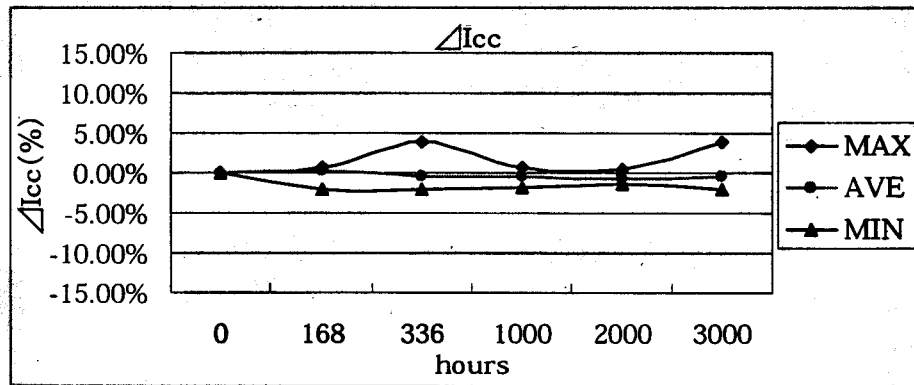


Fig.1 Icc changes on high temperature DC Bias Test.