
2SK2121

Silicon N-Channel MOS FET

HITACHI

November 1996

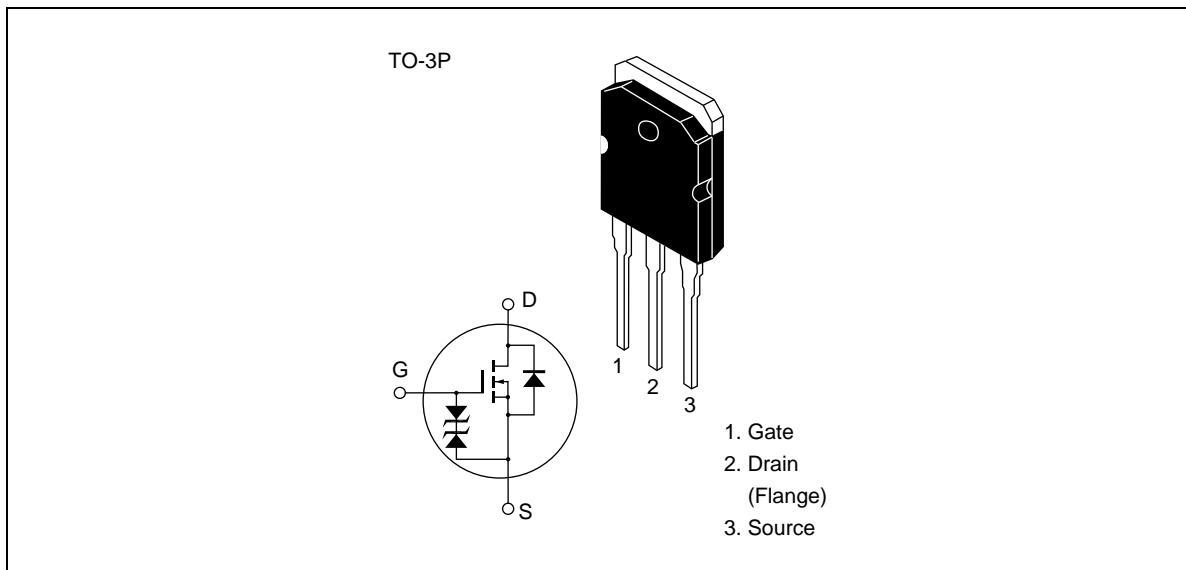
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC-DC converter
- Avalanche ratings

Outline



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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	50	A
Drain peak current	I _{D(pulse)} ^{*1}	200	A
Body to drain diode reverse drain current	I _{DR}	50	A
Avalanche current	I _{AP} ^{*3}	50	A
Avalanche energy	E _{AR} ^{*3}	214	mJ
Channel dissipation	Pch ^{*2}	100	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

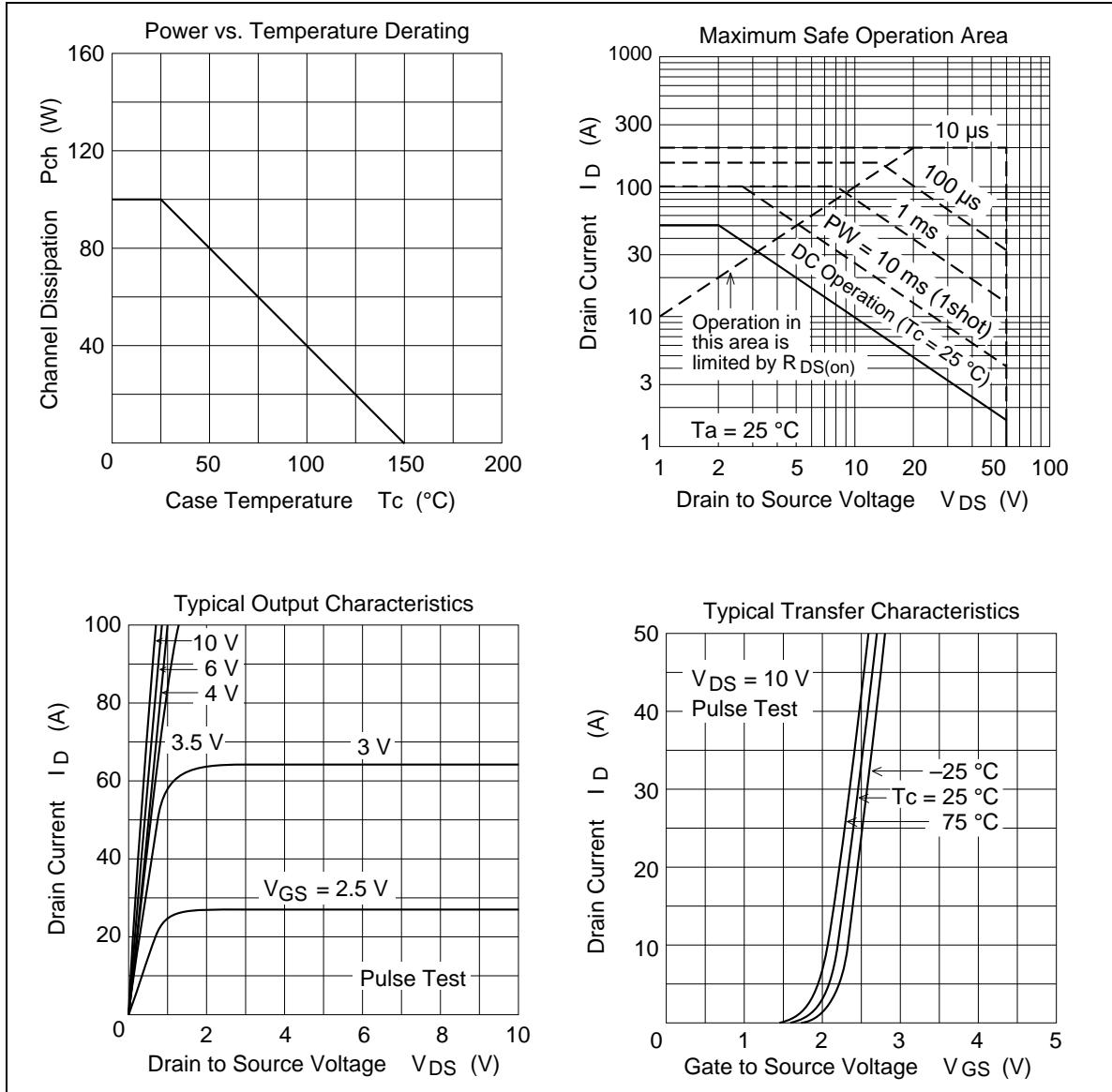
- Notes
1. PW ≤ 10 µs, duty cycle ≤ 1 %
 2. Value at Tc = 25 °C
 3. Value at Tch = 25 °C, Rg ≥ 50 Ω

Electrical Characteristics (Ta = 25°C)

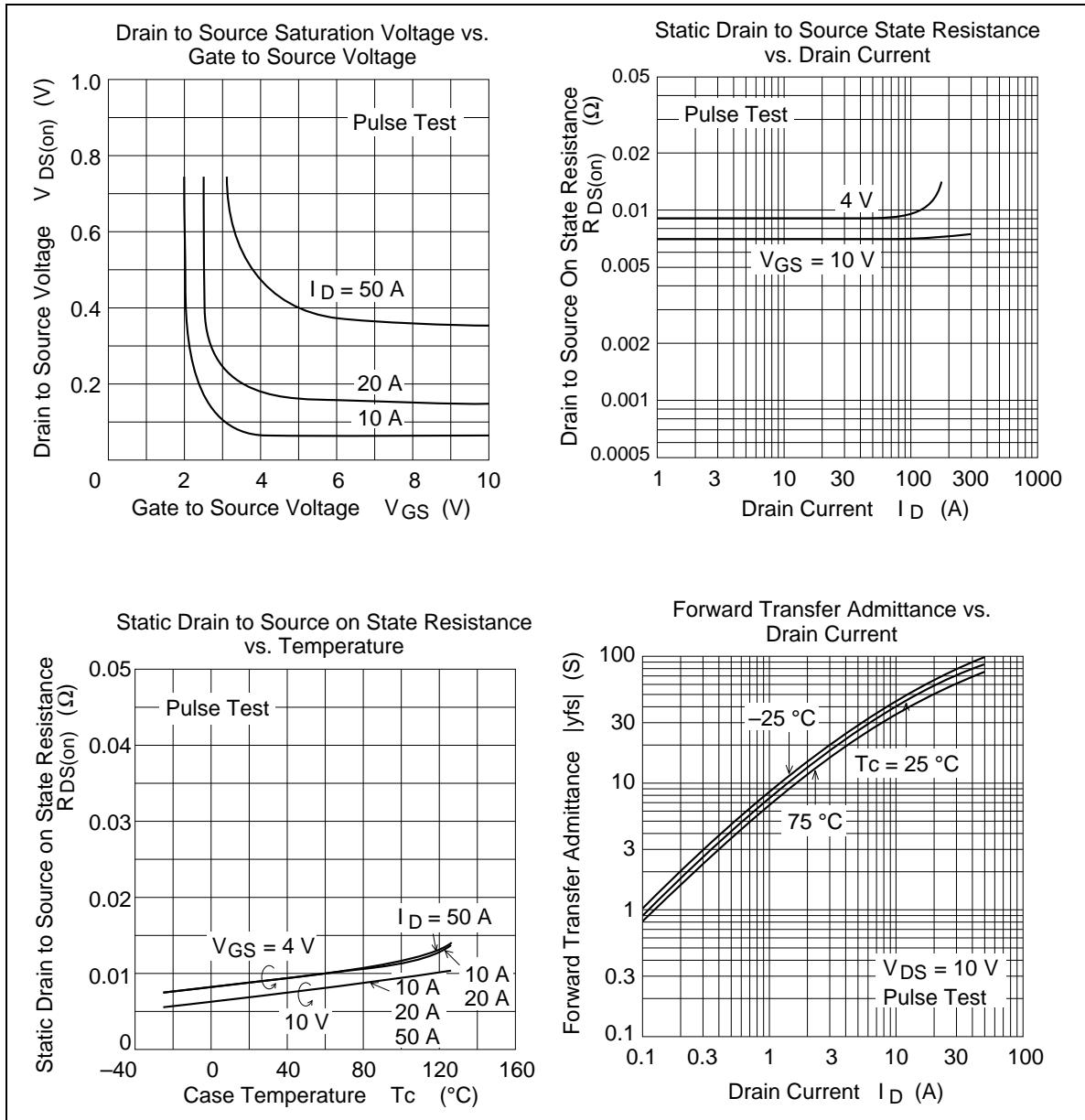
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 µA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	µA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	µA	V _{DS} = 50 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.25	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.007	0.01	Ω	I _D = 25 A V _{GS} = 10 V ^{*1}
		—	0.009	0.016	Ω	I _D = 25 A V _{GS} = 4 V ^{*1}
Forward transfer admittance	y _{fs}	40	65	—	S	I _D = 25 A V _{DS} = 10 V ^{*1}
Input capacitance	C _{iss}	—	8330	—	pF	V _{DS} = 10 V V _{GS} = 0 f = 1 MHz
Output capacitance	C _{oss}	—	3500	—	pF	
Reverse transfer capacitance	C _{rss}	—	550	—	pF	
Turn-on delay time	t _{d(on)}	—	50	—	ns	I _D = 25 A V _{GS} = 10 V R _L = 1.2 Ω
Rise time	t _r	—	270	—	ns	
Turn-off delay time	t _{d(off)}	—	1400	—	ns	
Fall time	t _f	—	560	—	ns	
Body to drain diode forward voltage	V _{DF}	—	0.95	—	V	I _F = 50 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	150	—	ns	I _F = 50 A, V _{GS} = 0, dI _F / dt = 50 A / µs

Note 1. Pulse Test

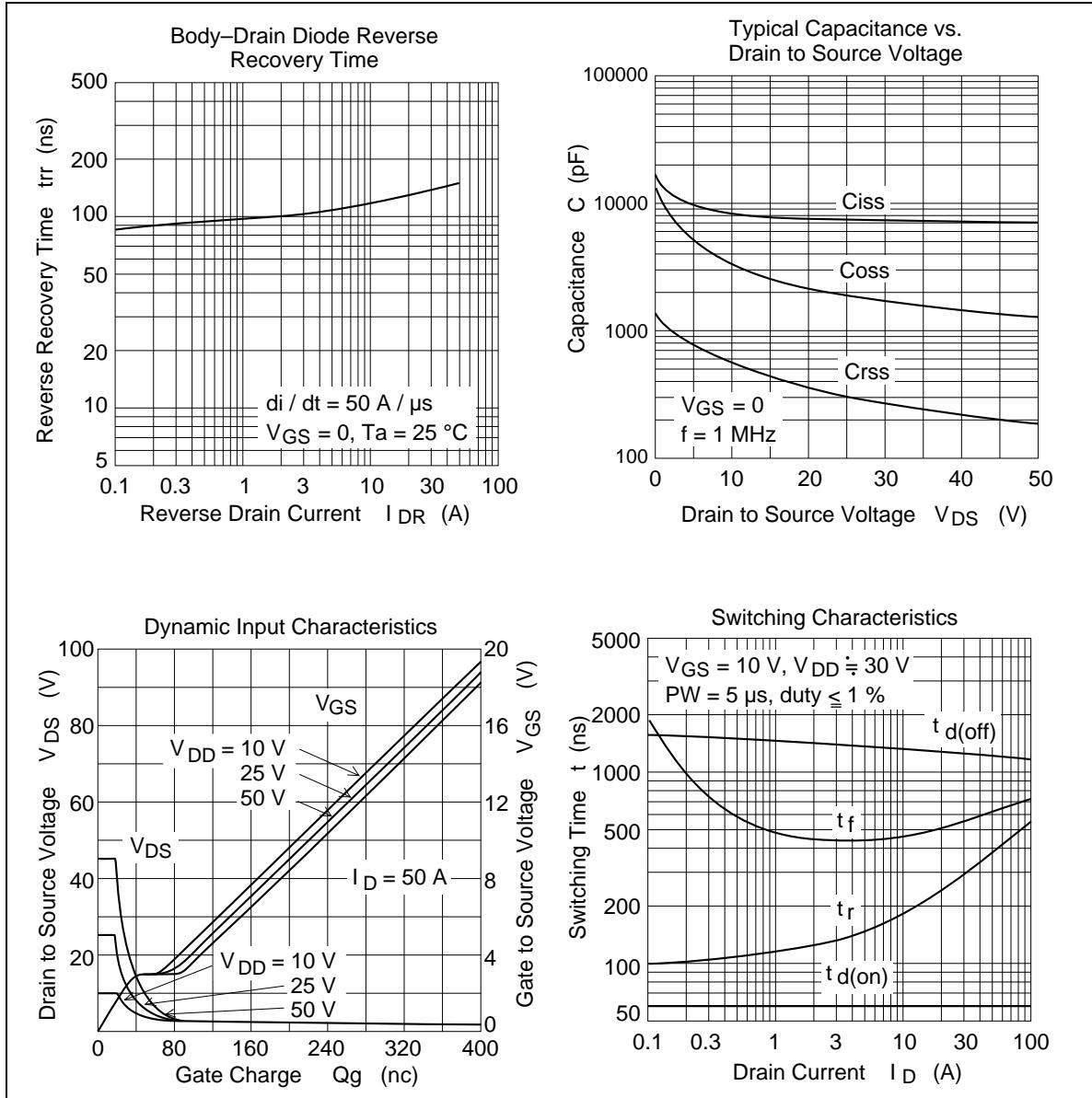
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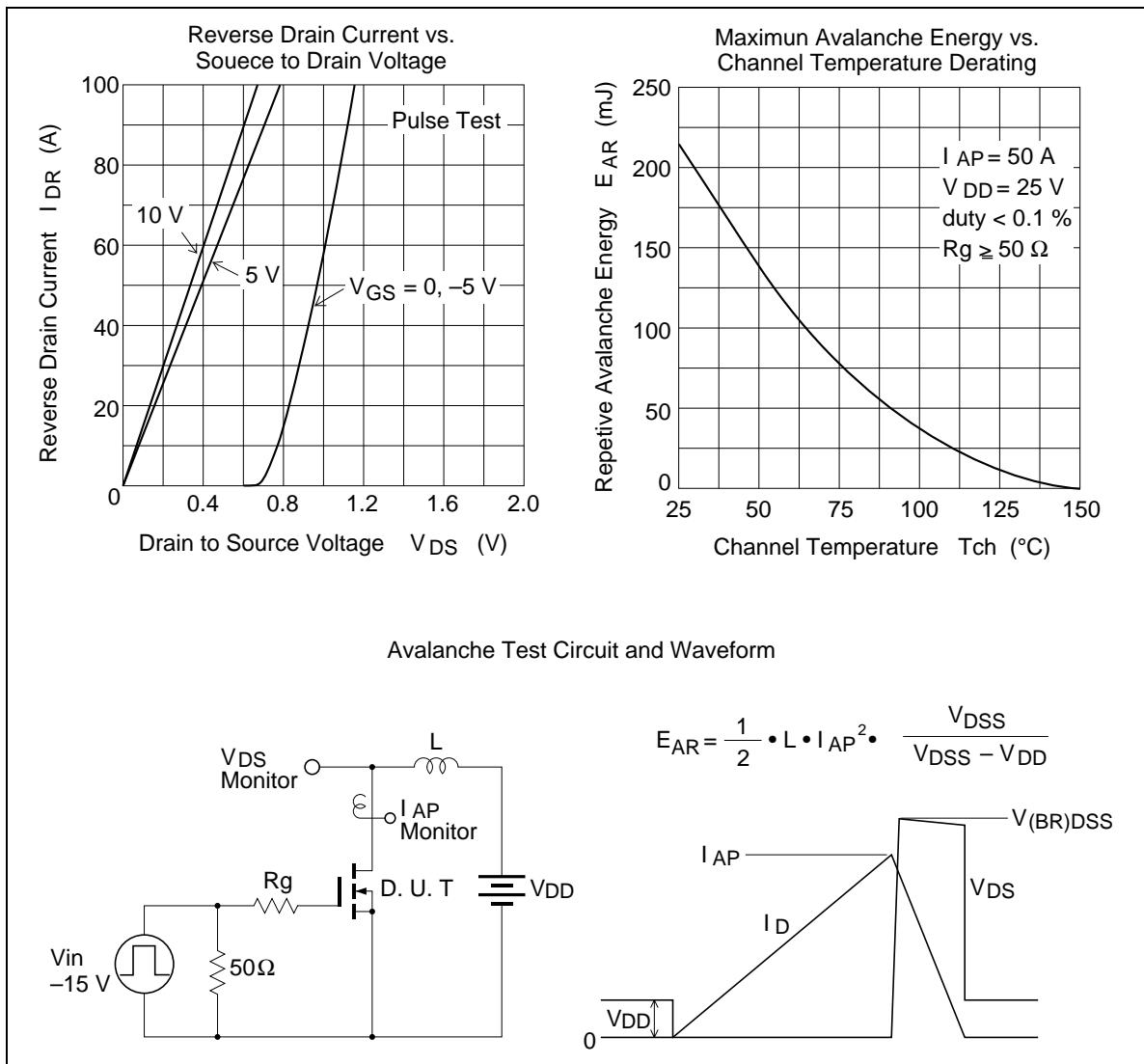
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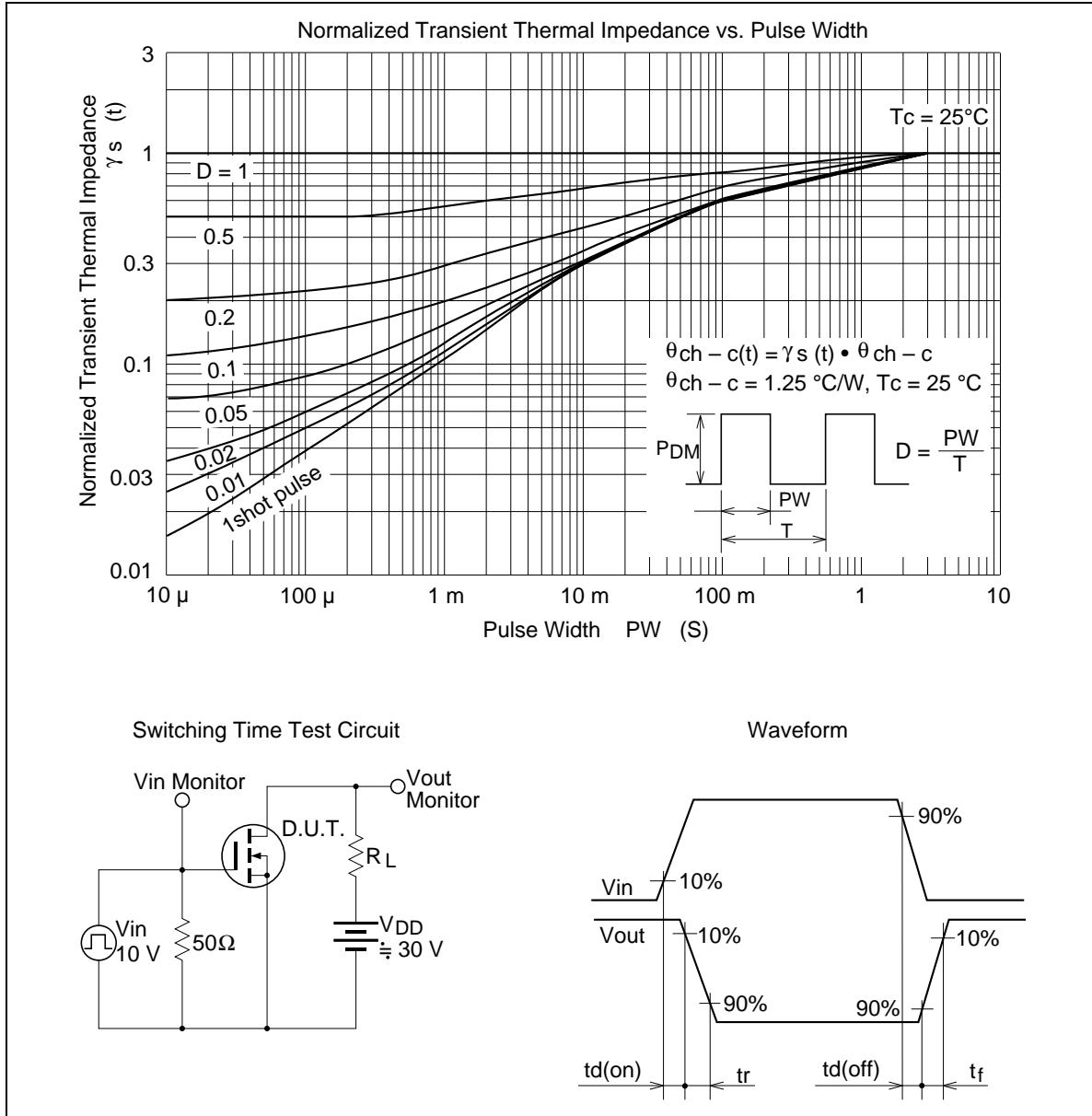
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