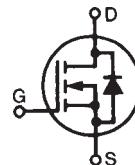


# PolarHT™ Power MOSFET

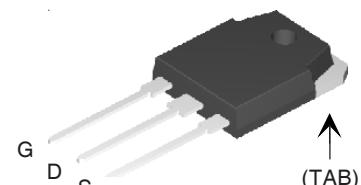
**IXTQ 42N25P**  
**IXTA 42N25P**  
**IXTP 42N25P**

**V<sub>DSS</sub>** = 250 V  
**I<sub>D25</sub>** = 42 A  
**R<sub>DS(on)</sub>** = 84 mΩ

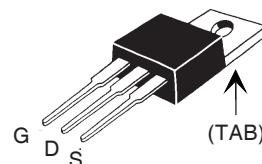
## N-Channel Enhancement Mode



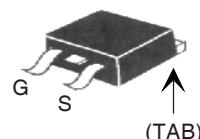
TO-3P (IXTQ)



TO-220 (IXTP)



TO-263 (IXTA)



G = Gate  
 S = Source

D = Drain  
 TAB = Drain

## Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

**Symbol**      **Test Conditions**  
 ( $T_j = 25^\circ\text{C}$ , unless otherwise specified)

**Characteristic Values**  
 Min.    Typ.    Max.

<b>V<sub>DSS</sub></b>	$V_{GS} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$	250		V
<b>V<sub>GS(th)</sub></b>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	2.5		5.0 V
<b>I<sub>GSS</sub></b>	$V_{GS} = \pm 20 \text{ V}_{DC}$ , $V_{DS} = 0$		$\pm 100$	nA
<b>I<sub>DSS</sub></b>	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$	$T_j = 125^\circ\text{C}$	25 250	$\mu\text{A}$
<b>R<sub>DS(on)</sub></b>	$V_{GS} = 10 \text{ V}$ , $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$ , duty cycle d $\leq 2 \%$	70	84	mΩ

## Advantages

- Easy to mount
- Space savings
- High power density

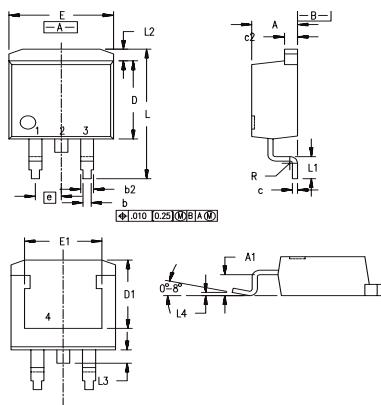
**PolarHT™ DMOS transistors utilize proprietary designs and process. US patent is pending.**

**Symbol**      **Test Conditions**
**Characteristic Values**
 $(T_J = 25^\circ\text{C}, \text{unless otherwise specified})$ 
**Min.**    **Typ.**    **Max.**

$g_{fs}$	$V_{DS} = 10 \text{ V}; I_D = 0.5 I_{D25}$ , pulse test	12	20	S
$C_{iss}$ $C_{oss}$ $C_{rss}$	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	2300	pF	
		430	pF	
		115	pF	
$t_{d(on)}$ $t_r$ $t_{d(off)}$ $t_f$	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = I_{D25}$ $R_G = 10 \Omega$ (External)	24	ns	
		28	ns	
		81	ns	
		30	ns	
$Q_{g(on)}$ $Q_{gs}$ $Q_{gd}$	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$	70	nC	
		17	nC	
		37	nC	
$R_{thJC}$			0.42 K/W	
$R_{thCK}$	(TO-3P)	0.21	K/W	
	(TO-220)	0.25	K/W	

**Source-Drain Diode**
**Characteristic Values**
 $(T_J = 25^\circ\text{C}, \text{unless otherwise specified})$ 

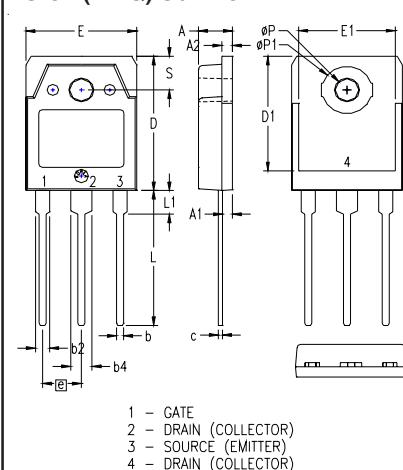
Symbol	Test Conditions	Min.	typ.	Max.
$I_s$	$V_{GS} = 0 \text{ V}$			42 A
$I_{SM}$	Repetitive			110 A
$V_{SD}$	$I_F = I_S, V_{GS} = 0 \text{ V}$ , Pulse test, $t \leq 300 \mu\text{s}$ , duty cycle $d \leq 2 \%$			1.5 V
$t_{rr}$	$I_F = 25 \text{ A}$ $-di/dt = 100 \text{ A}/\mu\text{s}$	200		ns
$Q_{RM}$	$V_R = 100 \text{ V}$		2.0	$\mu\text{C}$

**TO-263 (IXTA) Outline**


Dim.	Millimeter	Inches	Dim.	Millimeter	Inches
	Min.	Max.		Min.	Max.
A	4.06	4.83	.160	.190	
A1	2.03	2.79	.080	.110	
b	0.51	0.99	.020	.039	
b2	1.14	1.40	.045	.055	
c	0.46	0.74	.018	.029	
c2	1.14	1.40	.045	.055	
D	8.64	9.65	.340	.380	
D1	7.11	8.13	.280	.320	
E	9.65	10.29	.380	.405	
E1	6.86	8.13	.270	.320	
e	2.54	BSC	.100	BSC	
L	14.61	15.88	.575	.625	
L1	2.29	2.79	.090	.110	
L2	1.02	1.40	.040	.055	
L3	1.27	1.78	.050	.070	
L4	0	0.38	0	.015	
R	0.46	0.74	.018	.029	

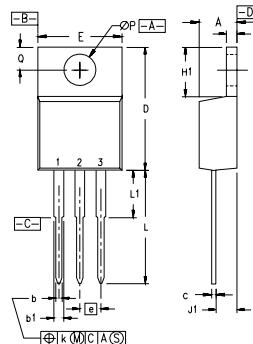
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,850,072, 4,931,844, 5,034,796, 5,063,307, 5,237,481, 5,381,025, 6,404,065B1, 6,162,665, 6,534,343, 6,583,505, 6,306,728B1, 6,259,123B1, 6,306,728B1, 6,683,344.

**TO-3P (IXTQ) Outline**


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.193	4.70	4.90
A1	.051	.059	1.30	1.50
A2	.057	.065	1.45	1.65
b	.035	.045	0.90	1.15
b2	.075	.087	1.90	2.20
b4	.114	.126	2.90	3.20
c	.022	.031	0.55	0.80
D	.780	.791	19.80	20.10
D1	.665	.677	16.90	17.20
E	.610	.622	15.50	15.80
E1	.531	.539	13.50	13.70
e	.215	BSC	5.45	BSC
L	.779	.795	19.80	20.20
L1	.134	.142	3.40	3.60
$\varnothing P$	.126	.134	3.20	3.40
$\varnothing P1$	.272	.280	6.90	7.10
S	.193	.201	4.90	5.10

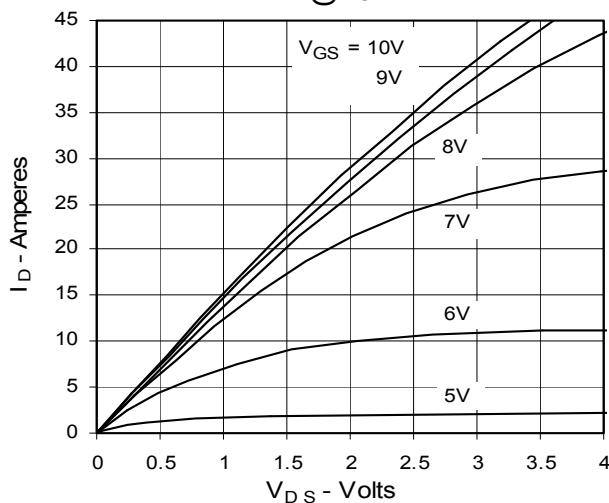
All metal areas are tin plated.

**TO-220 (IXTP) Outline**


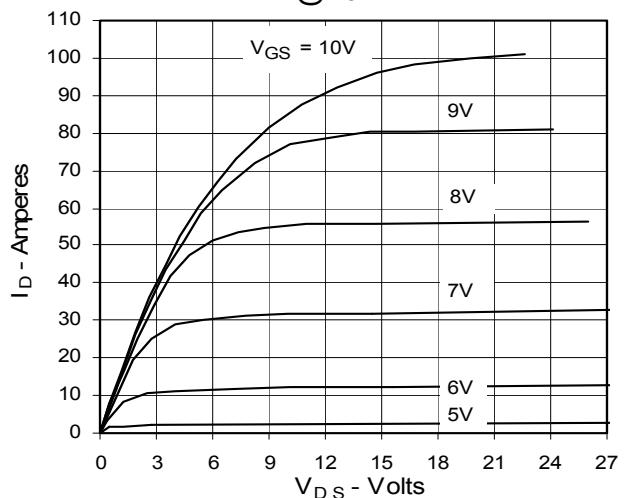
Pins: 1 - Gate      2 - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100	BSC	2.54	BSC
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	.038
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
$\varnothing P$	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

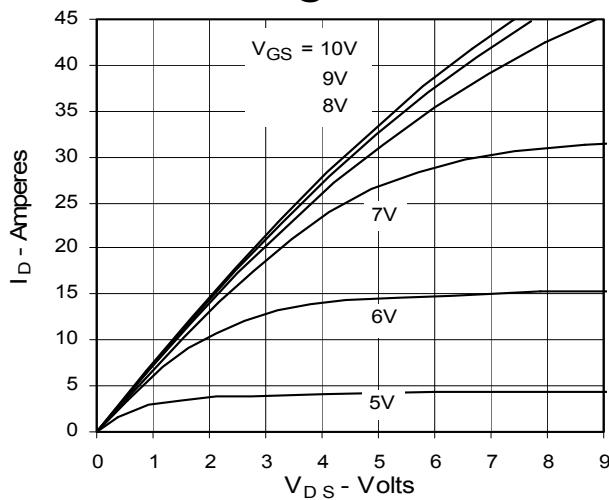
**Fig. 1. Output Characteristics  
@ 25°C**



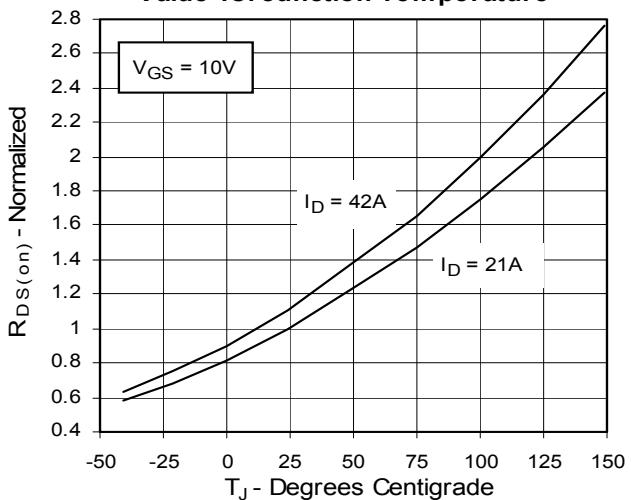
**Fig. 2. Extended Output Characteristics  
@ 25°C**



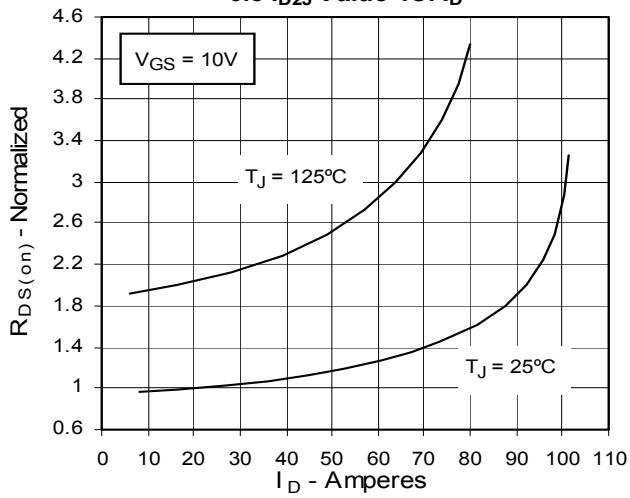
**Fig. 3. Output Characteristics  
@ 125°C**



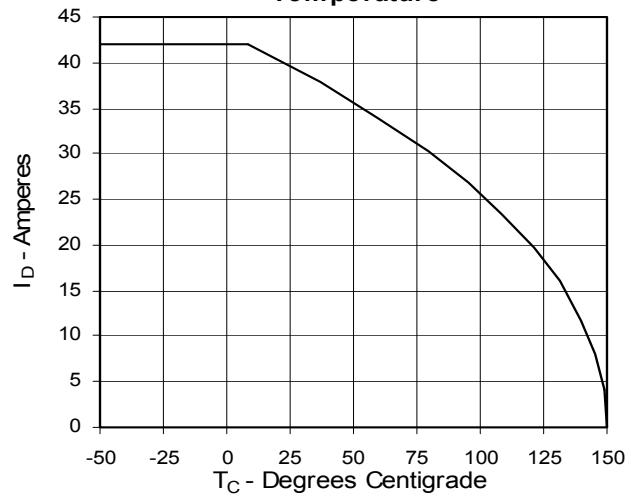
**Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature**

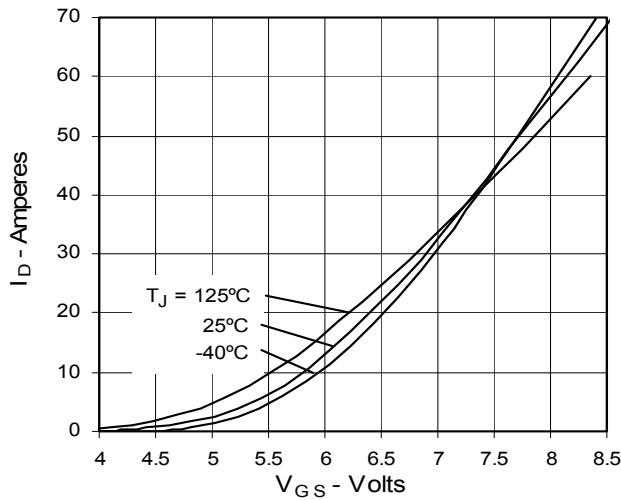
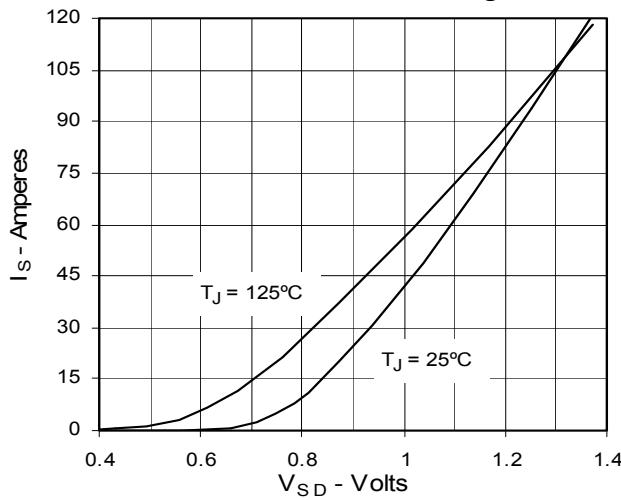
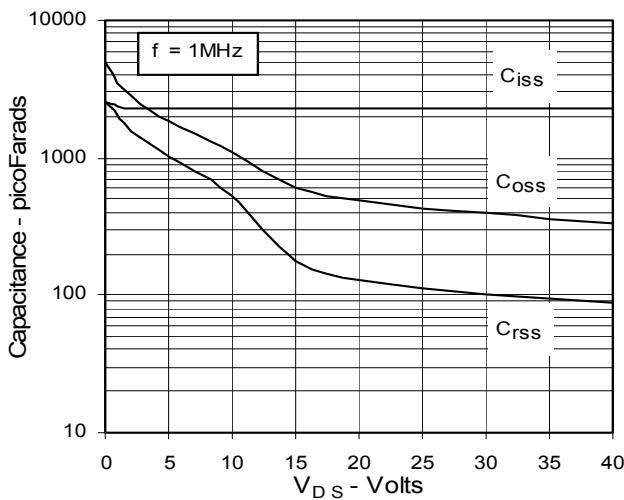
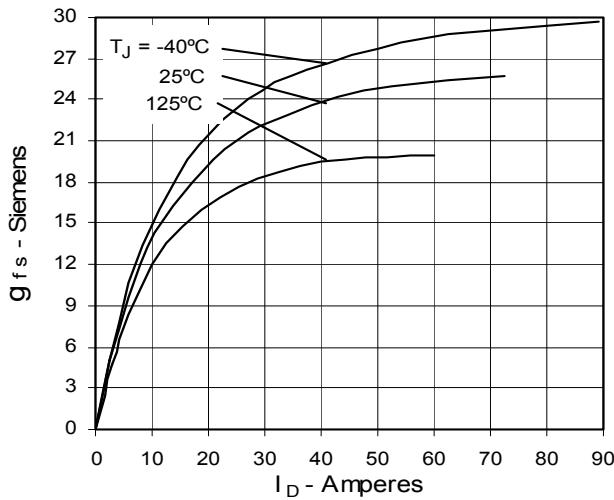
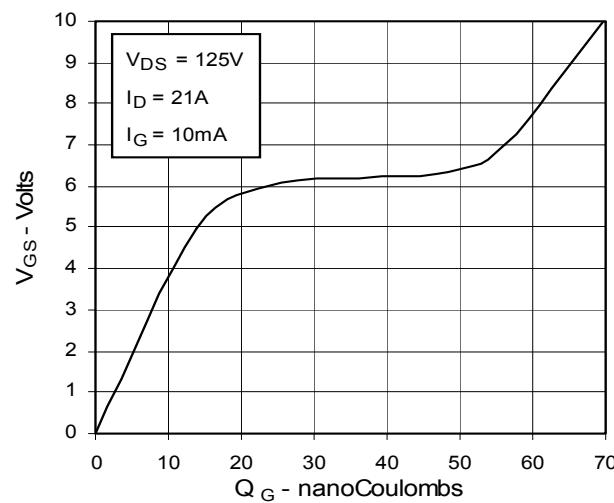
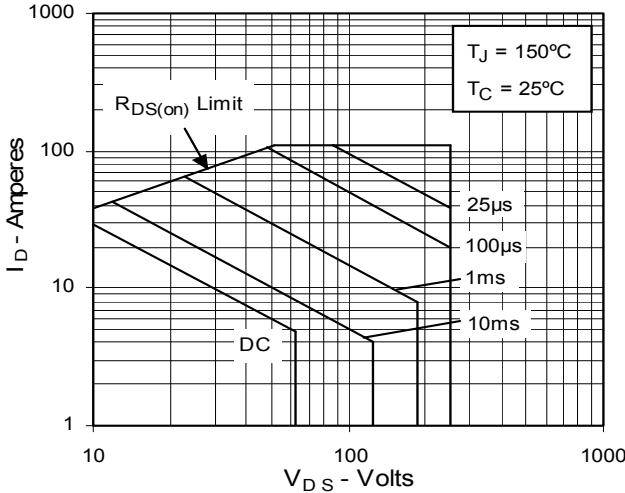


**Fig. 5.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs.  $I_D$**



**Fig. 6. Drain Current vs. Case Temperature**



**Fig. 7. Input Admittance**

**Fig. 9. Source Current vs.  
Source-To-Drain Voltage**

**Fig. 11. Capacitance**

**Fig. 8. Transconductance**

**Fig. 10. Gate Charge**

**Fig. 12. Forward-Bias  
Safe Operating Area**


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**Fig. 13. Maximum Transient Thermal Resistance**