

General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for DC/DC Converter.

FEATURES

- $V_{DSS}=30V$, $I_D=52A$.
- Low Drain to Source On-state Resistance.
 - : $R_{DS(ON)}=11m$ (Max.) @ $V_{GS}=10V$
 - : $R_{DS(ON)}=15m$ (Max.) @ $V_{GS}=4.5V$

MAXIMUM RATING (Ta=25 °C Unless otherwise Noted)

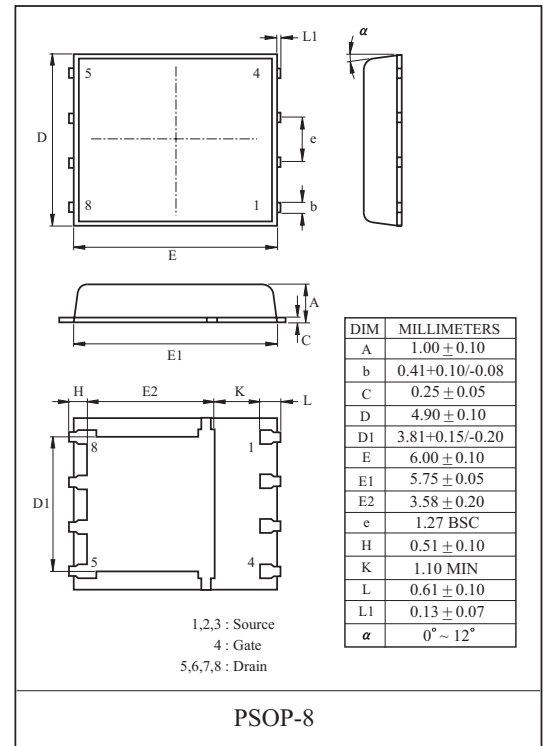
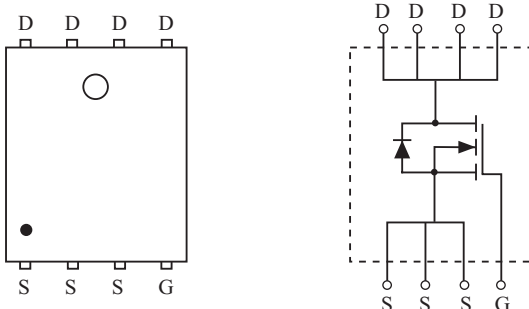
CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain to Source Voltage		V_{DSS}	30	V
Gate to Source Voltage		V_{GSS}	± 20	V
Drain Current	DC @ $T_C=25^\circ C$ (Note1)	I_D	52	A
	Pulsed (Note2)	I_{DP}	208	
Single Pulsed Avalanche Energy (Note3)		E_{AS}	170	mJ
Drain Power Dissipation	@ $T_C=25^\circ C$ (Note1)	P_D	54	W
	@ $T_a=25^\circ C$ (Note2)		2.5	
Maximum Junction Temperature		T_j	150	$^\circ C$
Storage Temperature Range		T_{stg}	-55 ~ 150	$^\circ C$
Thermal Resistance, Junction to Case (Note1)		R_{thJC}	2.3	$^\circ C/W$
Thermal Resistance, Junction to Ambient (Note2)		R_{thJA}	50	$^\circ C/W$

Note 1) R_{thJC} means that the infinite heat sink is mounted.

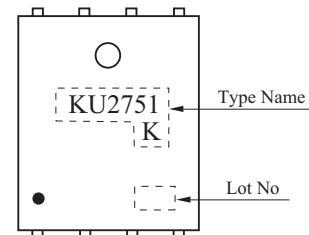
Note 2) Surface Mounted on 1×1 Pad of 2 oz copper.

Note 3) $L=78.5/\sqrt{f}$, $I_{AS}=52A$, $V_{DD}=15V$, $V_{GS}=10V$, Starting $T_j=25^\circ C$

PIN CONNECTION (TOP VIEW)



MARKING



KU2751K

ELECTRICAL CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static							
Drain to Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	30	-	-	V
Drain Cut-off Current		I _{DSS}	V _{GS} =0V, V _{DS} =30V	-	-	1	μA
Gate to Source Leakage Current		I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate to Source Threshold Voltage		V _{th}	V _{DS} =V _{GS} , I _D =250μA	1.0	-	3.0	V
Drain to Source On Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =20A (Note4)	-	9.2	11.0	m
			V _{GS} =4.5V, I _D =20A (Note4)	-	12.5	15.0	
Forward Transconductance		g _{fs}	V _{DS} =5V, I _D =20A (Note4)	-	43	-	S
Dynamic							
Input Capacitance		C _{iss}	V _{DS} =15V, f=1MHz, V _{GS} =0V	-	777	-	pF
Output Capacitance		C _{oss}		-	271	-	
Reverse Transfer Capacitance		C _{rss}		-	74	-	
Gate Resistance		R _g	f=1MHz	-	1.0	-	
Total Gate Charge	V _{GS} =10V	Q _g	V _{DS} =15V, V _{GS} =10V, I _D =20A (Note4)	-	12.5	-	nC
	V _{GS} =4.5V	Q _g		-	6.8	-	
Gate to Source Charge		Q _{gs}		-	2.4	-	
Gate to Drain Charge		Q _{gd}		-	2.2	-	
Turn-On Delay Time		t _{d(on)}		V _{DD} =15V, V _{GS} =10V I _D =20A, R _G =1.6 Ω (Note4)	-	7.0	
Turn-On Rise Time		t _r	-		2.9	-	
Turn-Off Delay Time		t _{d(off)}	-		21.0	-	
Turn-Off Fall Time		t _f	-		2.6	-	
Source to Drain Diode Ratings							
Source to Drain Forward Voltage		V _{SD}	V _{GS} =0V, I _S =20A (Note4)	-	0.8	1.2	V
Reverse Recovery Time		t _{rr}	I _S =20A, dI/dt=100A/μs (Note4)	-	18.0	-	ns
Reverse Recovered Charge		Q _{rr}	I _S =20A, dI/dt=100A/μs (Note4)	-	7.5	-	nC

Note 4) Pulse Test : Pulse width <300 μs, Duty cycle < 2%

Fig1. $I_D - V_{DS}$

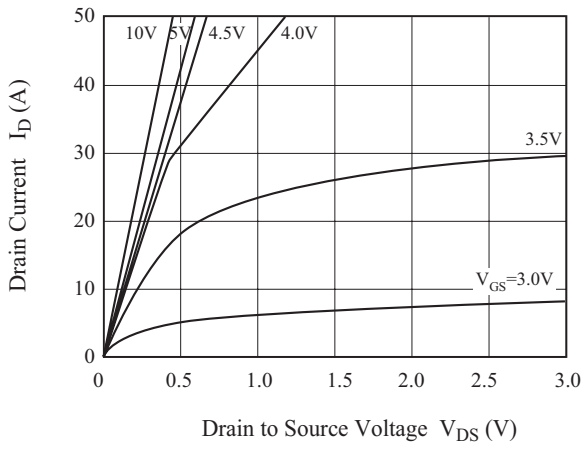


Fig2. $R_{DS(on)} - I_D$

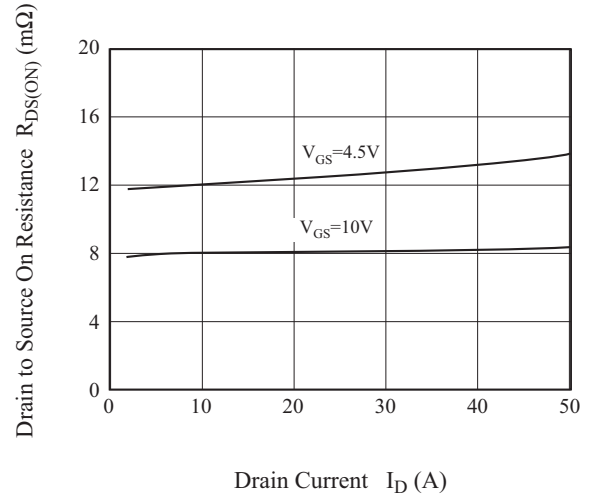


Fig3. $I_D - V_{GS}$

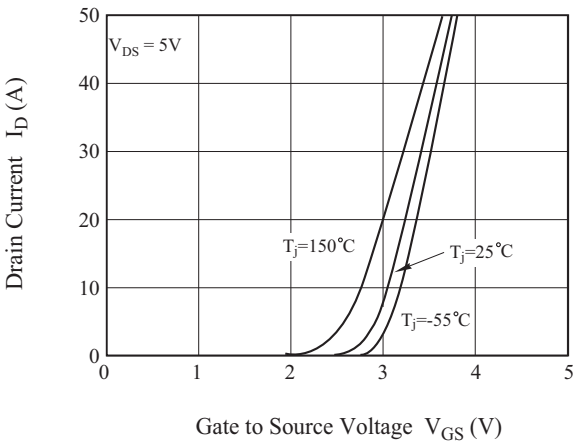


Fig4. $R_{DS(ON)} - T_j$

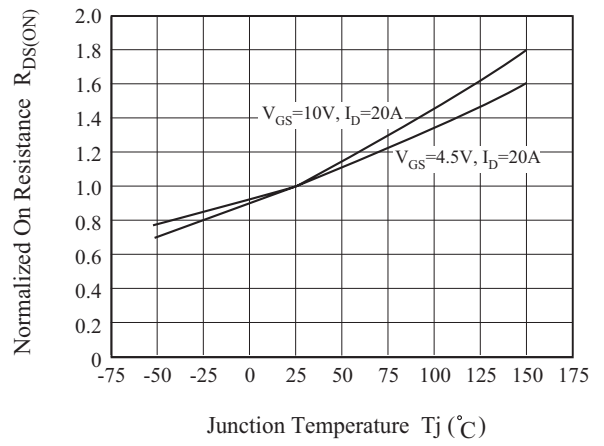


Fig5. $V_{th} - T_j$

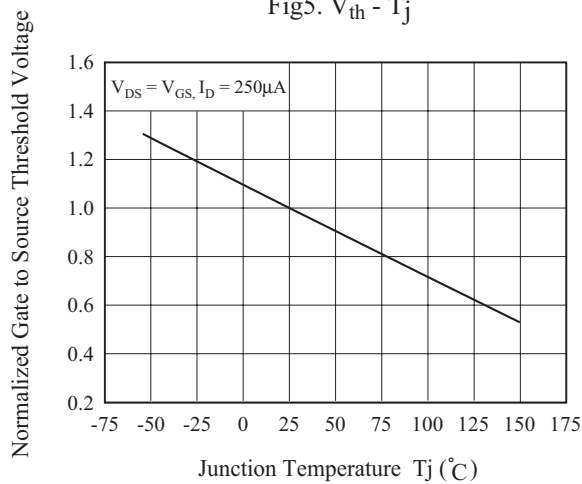


Fig6. $I_S - V_{SD}$

