

12 A SCR

Features

High current density per square mm

Applications

- Overvoltage crowbar protection
- Motor control circuits in power tools and kitchen aids
- Inrush current limiting circuits

Description

This device is mounted in DPAK and intended for use in applications such as voltage regulators circuits for motorbikes, overvoltage crowbar protection, motor control circuits in power tools and capacitive discharge ignition.

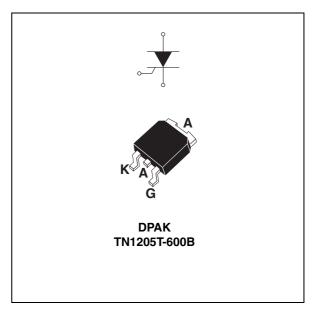


Table 1.Device summary

I _{T(rms)}	12 A
V _{DRM} /V _{RRM}	600 V
I _{GT}	2 to 5 mA

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1 Characteristics

Table 2.Absolute ratings⁽¹⁾

Parameter			Value	Unit	
On-state rms current (180 °C conduction angle) $T_c = 103$ °C				А	
Average on-state current(180 °C conduction angle) $T_c = 103 °C$				А	
Non repetitive surge peak on-state current $t_p = 8.3 \text{ ms}$ $t_p = 10 \text{ ms}$				А	
I^2 T value for fusing $t_p = 10 \text{ ms}$				A ² s	
$\label{eq:F} \begin{array}{l} \mbox{Critical rate of rise of on-state current} \\ I_G = 2 \ x \ I_{GT}, \ tr \leq 100 \ ns \end{array} \qquad $		T _j = 125 °C	50	A/µs	
Peak gate current $t_p = 20 \ \mu s$ $T_c = 125$		T _c = 125 °C	4	А	
Average gate power dissipation $T_j = 125 \text{ °C}$			1	W	
Storage junction temperature range			-40 to + 150	°C	
Operating junction temperature range					
	On-state rms current (180 °C conduction angle) Average on-state current(180 °C conduction angle) Non repetitive surge peak on-state current l^2T value for fusing Critical rate of rise of on-state current $l_G = 2 \times l_{GT}$, tr ≤ 100 ns Peak gate current Average gate power dissipation Storage junction temperature range	On-state rms current (180 °C conduction angle)Average on-state current(180 °C conduction angle)Non repetitive surge peak on-state current l^2T value for fusingCritical rate of rise of on-state current $I_G = 2 \times I_{GT}, tr \le 100 \text{ ns}$ Peak gate currentAverage gate power dissipationStorage junction temperature range	$\begin{array}{ll} \text{On-state rms current (180 °C conduction angle)}} & T_c = 103 °C \\ \text{Average on-state current(180 °C conduction angle)}} & T_c = 103 °C \\ \text{Non repetitive surge peak on-state current}} & t_p = 8.3 \text{ ms} \\ t_p = 10 \text{ ms} \\ t_p = 10 \text{ ms} \\ t_p = 10 \text{ ms} \\ \text{Critical rate of rise of on-state current}} & t_p = 10 \text{ ms} \\ \text{Critical rate of rise of on-state current}} & F = 60 \text{ Hz} & T_j = 125 °C \\ \text{Peak gate current}} & t_p = 20 \ \mu\text{s} & T_c = 125 °C \\ \text{Average gate power dissipation}} & T_j = 125 °C \\ \text{Storage junction temperature range} & \text{Storage junction temperature range} \\ \end{array}$	$\begin{array}{c c c c c c c c c } \hline \label{eq:conduction angle} & T_c = 103 \ ^\circ C & 12 \\ \hline \mbox{Average on-state current(180 \ ^\circ C \ conduction \ angle)} & T_c = 103 \ ^\circ C & 8 \\ \hline \mbox{Average on-state current(180 \ ^\circ C \ conduction \ angle)} & t_p = 8.3 \ ^ms & 120 \\ \mbox{t}_p = 10 \ ^ms & 115 \\ \hline \mbox{I}^2 \ value \ for \ fusing & t_p = 10 \ ms & 66 \\ \hline \mbox{Critical rate of rise of on-state current} & t_p = 10 \ ms & 66 \\ \hline \mbox{Critical rate of rise of on-state current} & F = 60 \ Hz & T_j = 125 \ ^\circ C & 50 \\ \hline \mbox{Peak gate current} & t_p = 20 \ \mu s & T_c = 125 \ ^\circ C & 4 \\ \hline \mbox{Average gate power dissipation} & & T_j = 125 \ ^\circ C & 1 \\ \hline \mbox{Storage junction temperature range} & & & -40 \ to + 150 \\ \hline \end{tabular}$	

1. $T_j = 25 \ ^{\circ}C$, unless otherwise specified

Table 3. Electrical characteristics⁽¹⁾

	Symbol	Test conditions		Min.	Тур.	Max.	Unit
	I_{GT} $V_{D} = 12 \text{ V}, \text{ R}_{L} = 33 \Omega$			2		5	mA
	V _{GT}	V_D = 12 V, R _L = 33 Ω				1.3	V
	V_{GD} $V_D = V_{DRM}$, $R_L = 3.3 \text{ k}\Omega$ $T_j = 125 \text{ °C}$		0.2			V	
	Ι _Η	I _T = 500 mA gate open	-			15	mA
	١ _L	$I_{G} = 1.2 I_{GT}$				30	mA
	dV/dt	$V_D = 67\% V_{DRM}$ gate open $T_j = 125 \text{ °C}$		100			V/µs
www.Data	$\begin{array}{c} \text{W.Data} \overset{\text{Sheet4U.co}}{\text{H}} \overset{\text{Gate controlled turn on time}}{I_{TM} = 40 \text{ A}, \text{ V}_{D} = \text{V}_{DRM(MAX)}, \text{ I}_{GT} = 100 \text{ mA}}\\ & \text{dI}_{G}/\text{dt} = 5 \text{ A/}\mu\text{s}, \text{ R}_{G} = 68 \Omega \end{array}$ $\begin{array}{c} \text{Circuit commutated turn off time}}\\ \text{V}_{D} = 67\% \text{ V}_{DRM(MAX)}, \text{ T}_{j} = 125 \text{ °C}, \text{ I}_{TM} = 20 \text{ A}, \text{ V}_{R} = 25 \text{ V}}\\ & \text{dI}_{T}/\text{dt} = 30 \text{ A/}\mu\text{S}, \text{ dV}_{D}/\text{dt} = 50 \text{ V/}\mu\text{s}, \text{ R}_{GK} = 100 \Omega \end{array}$			1.2		μs	
				55		μs	
	V _{TM}	r				1.6	V
	V _{T0}					0.85	V
	R _d	Dynamyc restistance	T _j = 125 °C			30	mΩ
	I_{DRM} $Y_{j} = 25 ^{\circ}\text{C}$		T _j = 25 °C			5	μA
	$V_{\text{DRM}} = V_{\text{RRM}}$	T _j = 125 °C			2	mA	

1. $T_j = 25$ °C, unless otherwise specified



Characteristics

Table 4.Thermal resistance

Symbol	Parameter			Unit
R _{th(j-c)}	Junction to case (DC)		1.8	°C/W
R _{th(j-a)}	Junction to ambient (DC)	$S^{(1)} = 0.5 \text{ cm}^2$	70	°C/W

1. S = Copper surface under tab.

Figure 1. Maximum average power dissipation versus average on-state current

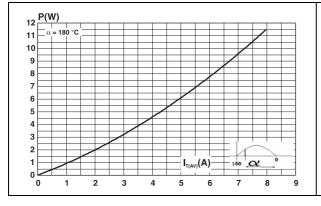
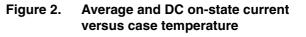


Figure 3. Average DC on-state current versus ambient temperature



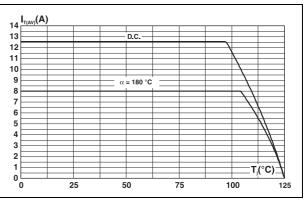
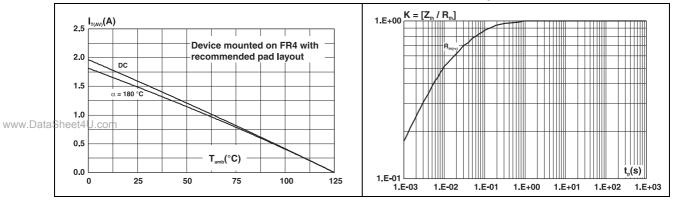


Figure 4. Relative variation of thermal impedance junction to case versus pulse duration

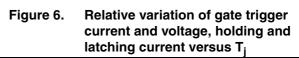




T_i(°C)

Typical values

Figure 5. **Relative variation of thermal** impedance junction to ambient versus pulse duration



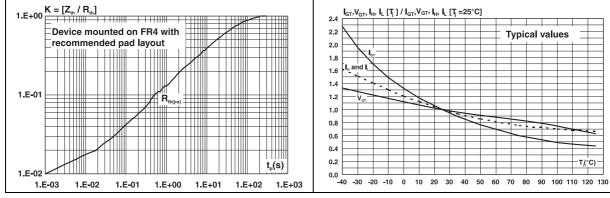


Figure 7. Surge peak on-state current versus number of cycles

Figure 8. Non-repetitive surge peak on-state current for a sinusoidal pulse, and corresponding values of I²t

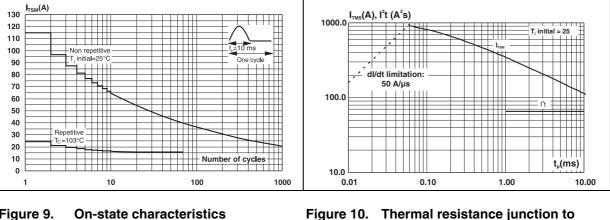
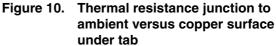
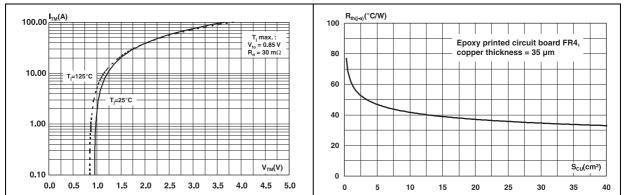


Figure 9.

(maximum values) www.DataSheet4U.com







2 Ordering information scheme

Standard SCR series Current 12 = 12 A Sensitivity	TN 12 05 T - 600 B (-TR)
05 = 5 mA Application specific	
Voltage 600 = 600 V Package B = DPAK	
Packing mode Blank = Tube -TR = Tape and reel	

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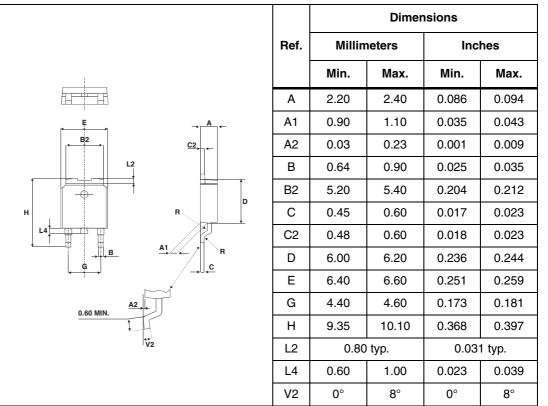


3 Package information

- Epoxy meets UL94, V0
- Lead-free package

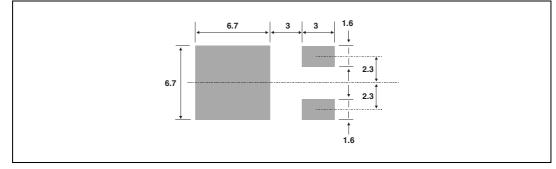
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

Table 5. DPAK dimensions



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Figure 12. Footprint (dimensions in mm)





4 Ordering information

Table 6. Ordering information

Order code	er code Marking Package Weight		Weight	Base qty	Delivery mode
TN1205T-600B	TN12 05T6	DPAK 0.3g		75	Tube
TN1205T-600B-TR	TN12 05T6	DFAR	DPAK 0.3g		Tape and reel

5 Revision history

Table 7.Document revision history

Date	Revision	Changes
01-Oct-2009	1	Initial release.

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