

SIGC05T60SNC

IGBT Chip in NPT-technology

FEATURES:

- 600V NPT technology
- 100µm chip
- short circuit prove
- positive temperature coefficient
- · easy paralleling

This chip is used for:

DuoPack SGP04N60



Applications:

• drives

Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC05T60SNC	600V	4A	2.29 x 2.29 mm ² sawn on foi	sawn on foil	Q67050-A4149-
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MECHANICAL PARAMETER:

Raster size	2.29 x 2.29			
Area total / active	5.2 / 3.2			
Emitter pad size	1.38 x 0.93			
Gate pad size	0.7 x 0.5			
Thickness	100	μm		
Wafer size	150	mm		
Flat position	180	deg		
Max.possible chips per wafer	2990			
Passivation frontside	Photoimide			
Emitter metallization	3200 nm Al Si 1%			
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solde	r		
Wire bond	Al,≤500μm			
Reject Ink Dot Size	Ø 0.65mm; max 1.2mm			
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C			



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MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T _j =25 °C	V _{CE}	600	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	12	А
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T_j , T_{stg}	-55 + 150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_i =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
T diameter		Conditions	min.	typ.	max.	0
Collector-emitter breakdown voltage	V _{(BR)CES}	V _{GE} =0V, I _C =500μA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =4A	1.6	2	2.5	V
Gate-emitter threshold voltage	V _{GE(th)}	$I_C=200\mu A,\ V_{GE}=V_{CE}$	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V			0.45	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =20V			120	nA

DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
raiailletei			min.	typ.	max.	Joint
Input capacitance	Ciss	V _{CE} =25V	-	264	317	pF
Output capacitance	Coss	V _{GE} =0V	-	29	35	
Reverse transfer capacitance	Crss	f=1MHz	-	17	21	

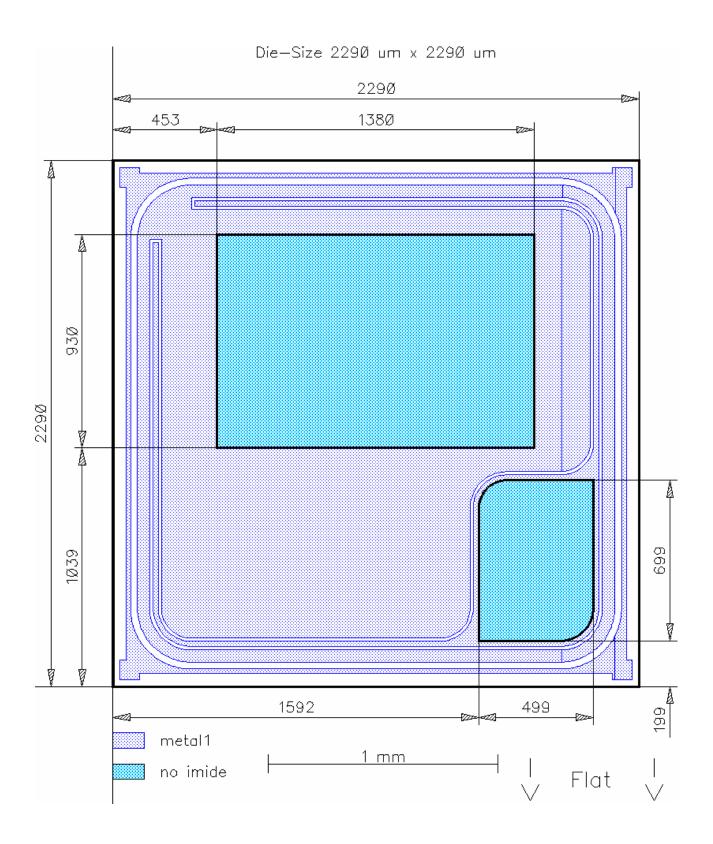
SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions 2)	Value			Unit
			min.	typ.	max.	Uiiit
Turn-on delay time	$t_{d(on)}$	T _j =150°C V _{CC} =400V	-	22	26	ns
Rise time	t_{r}	I _C =4A	-	16	19	
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE}$ =+15/0V $R_{\rm G}$ =67 Ω	-	264	317	
Fall time	t _f	716-0722	-	104	125	

²⁾ Values also influenced by parasitic L- and C- in measurement and package.



CHIP DRAWING:





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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet SGP04N60 Package:TO220

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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