

High Speed, Single Channel, Power MOSFET Drivers

Features

- · Industry-standard driver replacement
- Improved response times
- · Matched rise and fall times
- · Reduced clock skew
- Low output impedance
- · Low input capacitance
- · High noise immunity
- · Improved clocking rate
- · Low supply current
- Wide operating range
- · Separate drain connections

Applications

- · Clock/line drivers
- · CCD drivers
- · Ultrasound transducer drivers
- · Power MOSFET drivers
- Switch mode power supplies
- · Resonant charging
- · Cascoded drivers

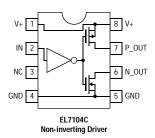
Ordering Information

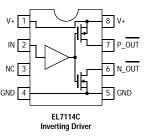
Part No.	Package	Tape & Reel	Outline #
EL7154CN	8-Pin PDIP		MDP0031
EL7154CS	8-Pin SO		MDP0027

General Description

The EL7104C and EL7114C ICs are matched driver ICs that improve the operation of the industry-standard TC-4420/29 clock drivers. The Elantec versions are very high speed drivers capable of delivering peak currents of 1A into highly capacitive loads. The high speed performance is achieved by means of a proprietary "Turbo-Driver" circuit that speeds up input stages by tapping the wider voltage swing at the output. Improved speed and drive capability are enhanced by matched rise and fall delay times. These matched delays maintain the integrity of input-to-output pulse-widths to reduce timing errors and clock skew problems. This improved performance is accompanied by a 10-fold reduction in supply currents over bipolar drivers, yet without the delay time problems commonly associated with CMOS drivers.

Connection Diagrams





August 20, 2001

Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.

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Absolute Maximum Ratings (TA = 25°C)

Supply (V+ to GND) 16.5V Operating Junction Temperature

Input Pins -0.3 V to +0.3 V above V+ Power Dissipation:

Peak Output Current 4A SO

Storage Temperature Range -65°C to +150°C PDIP 1050mW
Ambient Operating Temperature -40°C to +85°C

Important Note:

All parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$.

DC Electrical Characteristics

 $T_A = 25$ °C, V+ = 15V unless otherwise specified.

Parameter	Description	Test Conditions	Min	Тур	Max	Unit
Input	<u> </u>		•	•	•	
V_{IH}	Logic "1" Input Voltage		2.4			V
I_{IH}	Logic "1" Input Current	@V+		0.1	10	μΑ
$V_{\rm IL}$	Logic "0" Input Voltage				0.8	V
I_{IL}	Logic "0" Input Current	@0V		0.1	10	μΑ
V _{HVS}	Input Hysteresis			0.3		V
Output	·	<u>.</u>	•	•	•	
R _{OH}	Pull-Up Resistance	I _{OUT} = -100 mA		1.5	4	Ω
R _{OL}	Pull-Down Resistance	$I_{OUT} = +100 \text{ mA}$		2	4	Ω
I _{OUT}	Output Leakage Current	V+/GND		0.2	10	μΑ
I_{PK}	Peak Output Current	Source		4.0		A
		Sink		4.0		
I_{DC}	Continuous Output Current	Source/Sink	200			mA
Power Supply	•					
Is	Power Supply Current	Input = V+ EL7104C		4.5	7.5	mA
		EL7114C		1	2.5	
Vs	Operating Voltage		4.5		16	V

AC Electrical Characteristics

 $T_A = 25$ °C, V = 15V unless otherwise specified.

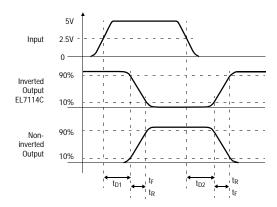
Parameter	Description	Test Conditions	Min	Тур	Max	Unit			
Switching Characteristics ($V_{DD} = V_H = 12V$; $V_L = -3V$)									
t _R	Rise Time	$C_{\rm L} = 1000 \; \rm pF$		7.5		ns			
		$C_{L} = 2000 \text{ pF}$		10	20	ns			
t _F	Fall Time	$C_{\rm L} = 1000 \; \rm pF$		10		ns			
		$C_{L} = 2000 \text{ pF}$		15	20	ns			
t _{D-ON}	Turn-On Delay Time	See Timing Table		18	25	ns			
t _{D-OFF}	Turn-Off Delay Time	See Timing Table		18	25	ns			

+125°C

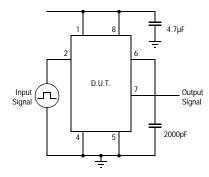
570mW

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Timing Table

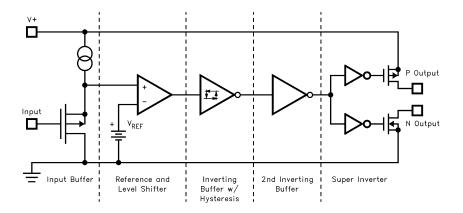


Standard Test Configuration

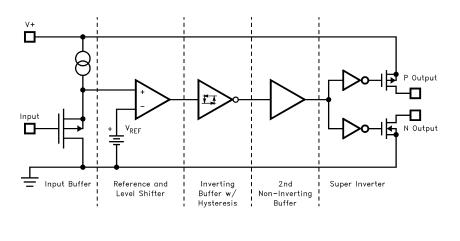


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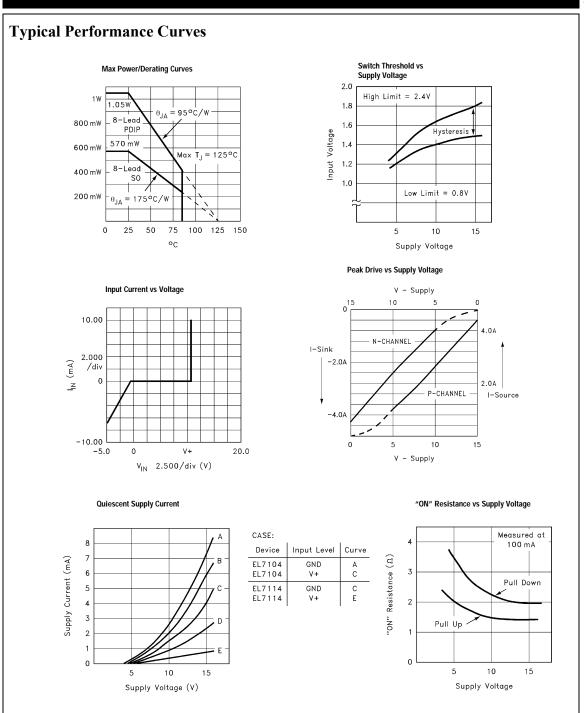
EL7104C Simplified Schematic



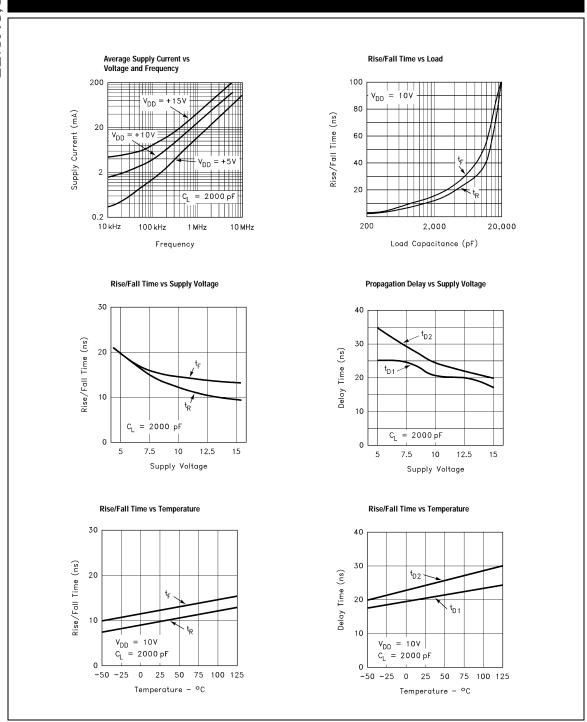
EL7114C Simplified Schematic



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