Praetorian III® EMI Filter for T-Flash/MicroSD Interfaces

CM1624

Features

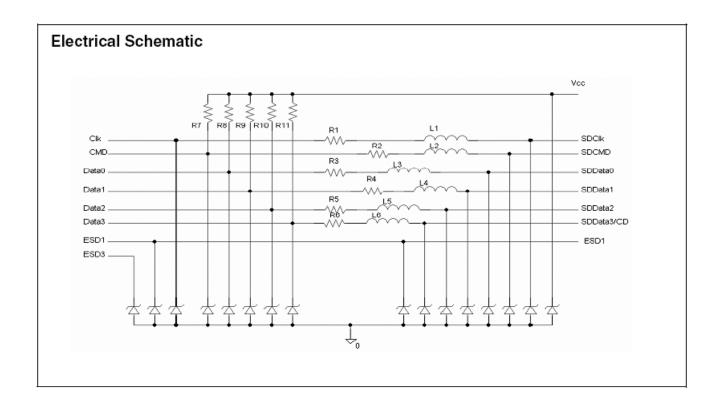
- Bidirectional EMI/RFI filtering and line termination with integrated ESD protection
- Provides ESD protection to IEC61000-4-2:
 - ±15kV contact discharge
- TVS working voltage: 5V
- Termination resistors: 40Ω
- Pull-up resistors: 25kΩ
- Typical capacitance per line: 12pF $(V_{IN} = 2.5V)$
- Protection and termination for six lines + Vcc
- Solid-state technology

Applications

- T-Flash / MicroSD interfaces
- MMC interfaces
- CDMA, GSM, 3G cell phones

Mechanical Characteristics

- 0.40mm, uDFN 16-pin package
- RoHS/WEEE compliance, lead-free finish
- Nominal dimensions: 3.30 x 1.35 x 0.50mm
- Pitch: 0.4mm
- Pin-lead finish: NiPd



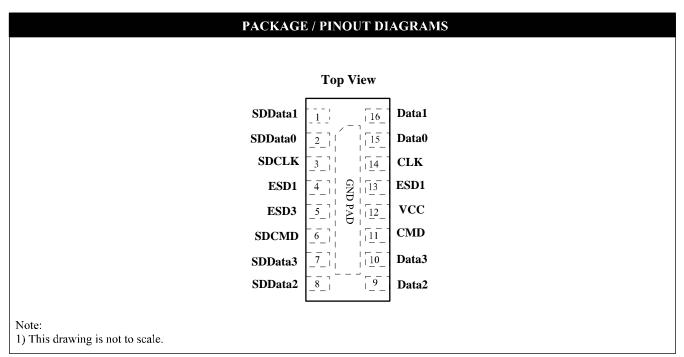
Description

The CM1624 is a combination EMI filter and line termination device with integrated TVS diodes for use on Multimedia Card interfaces. This state-of-the-art device utilizes solid-state, silicon-avalanche technology for superior clamping performance and DC electrical characteristics. The CM1624 has been optimized for protection of T-Flash/MicroSD interfaces in cellular phones and other portable electronics.

The CM1624 consists of six circuits that includes series impedance matching resistors and pull-up resistors as required by the SD specification. TVS diodes are included on each line for ESD protection. An additional TVS diode connection is included for protection of the voltage (Vcc) bus. Termination resistor value of 40Ω is provided on the SDData0, SDData1, SDData2, SDData3, CMD, and CLK lines.

Pull-up resistors of $25k\Omega$ are included on the SDData0, SDData1, SDData2, SDData3 and CMD lines, as well. These may be configured for devices operating in SD or SPI mode. The TVS diodes provide effective suppression of ESD voltages in excess of $\pm 15kV$ (contact discharge) per IEC 61000-4-2, level 4. The CM1624 is in a 16-pin, RoHS/WEEE compliant, UDFN 16-pin package. It measures 3.30 x 1.35 x 0.50mm. The leads are spaced at a pitch of 0.4mm and are finished with lead-free NiPd.

Package/Pin Information



PIN DESCRIPTIONS					
Pin	Name	Description			
1	SDData1	Data line #1 input/output with pull-up resistor			
2	SDData0	Data line #0 input/output with pull-up resistor			
3	SDCLK	Clock line Input/Output			
4	ESD1	Single ESD			
5	ESD3	Single ESD			
6	SDCMD Command Line Input/Out				
7	SDData3	Data line #3 input/output with pull-up resistor Data line #2 input/output with pull-up resistor			
8	SDData2				
9	Data2	Data line #2 input/output with pull-up resistor			
10	Data3	Data line #3 input/output with pull-up resistor			
11	CMD	Command Line Input/Output			
12	VCC	Power Supply ESD Protection			
13	ESD1	Single ESD			
14	CLK	Clock line Input/Output			
15	Data0	Data line #0 input/output with pull-up resistor			
16	Data1	Data line #1 input/output with pull-up resistor			
GND PAD GND		Ground return to shield			

Ordering Information

PART NUMBERING INFORMATION (Note 1)						
PIN PACKAGE		LEAD-FREE FINISH	Part Marking			
16	uDFN-16 (0.40mm)	CM1624 -08DE	1624(x) (Note 2)			

Note 1: Parts are shipped in Tape and Reel form unless otherwise specified.

Note 2: (x) = single character date code.

Specifications

ABSOLUTE MAXIMUM RATINGS*						
PARAMETER	RATING	UNITS				
Operating Temperature Range	-40 to +85	°C				
Storage Temperature Range	-55 to +150	°C				

^{*}Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

	ELECTRICAL OPERATING CHARACTERISTICS (SEE NOTE 1)								
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS			
R _{cH}	Channel Resistance (R1 to R6)		34	40	46	Ω			
L _{CH}	Channel Inductance			20		nH			
С	Capacitance per Channel	V _{IN} = 0V; 1MHz; 30mV _{RMS}	16	20	24	pF			
		V _{IN} = 2.5V; 1MHz; 30mV _{RMS;}		12		pF			
R _{UP}	Pull-up Resistance (R7 to R11)		21	25	29	kΩ			
l _{LEAK}	Diode Leakage Current per Channel	V _{IN} = 3V		0.1	0.5	μА			
V _{SIG}	Signal Clamp Voltage Positive Clamp Negative Clamp	$I_{LOAD} = 10mA$ $I_{LOAD} = -10mA$	5.6 -1.5	6.8 -0.8	9.0 -0.4	V V			
V _{ESD}	ESD Protection - Peak Discharge Voltage at any channel input, in system: a) Contact discharge per IEC 61000-4-2 Standard and b) Air discharge per IEC 61000-4-2 Standard		±15			kV kV			
f _c	Cut-off frequency $Z_{\text{SOURCE}} = 50\Omega$, $Z_{\text{LOAD}} = 50\Omega$			300		MHz			

Note 1: All parameters specified at $T_A = 25$ °C unless otherwise noted.

www.DataSheet4U.com

Performance Information

Typical Filter Performance (nominal conditions unless specified otherwise, 50Ω Environment)

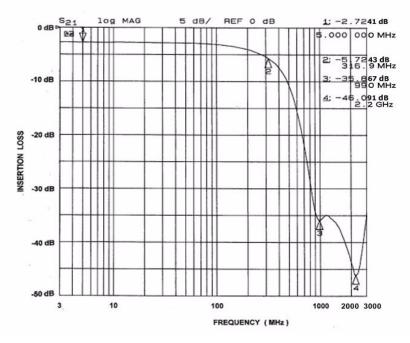


Figure 1. Insertion Loss VS. Frequency (Pins 1 and 16)

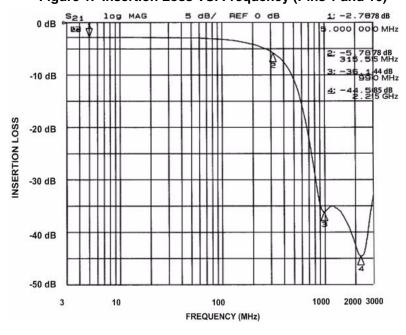


Figure 2. Insertion Loss VS. Frequency (Pins 2 and 15)

Performance Information (cont'd)

Typical Filter Performance (nominal conditions unless specified otherwise, 50Ω Environment)

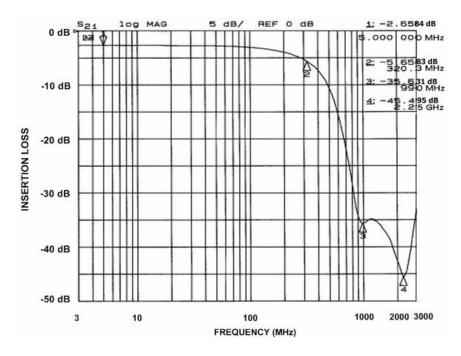


Figure 3. Insertion Loss VS. Frequency (Pins 3 and 14)

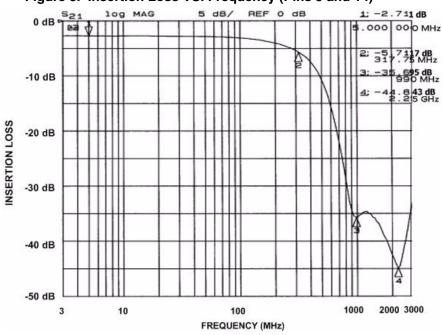


Figure 4. Insertion Loss VS. Frequency (Pins 6 and 11)

www.DataSheet4U.com

Performance Information (cont'd)

Typical Filter Performance (nominal conditions unless specified otherwise, 50Ω Environment)

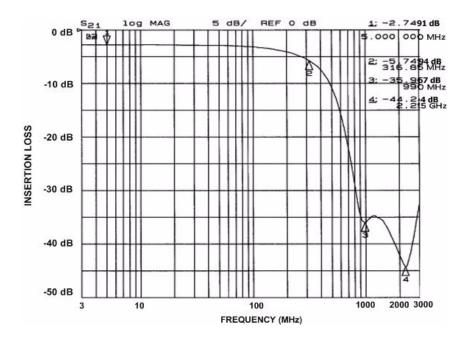


Figure 5. Insertion Loss VS. Frequency (Pins 7 and 10)

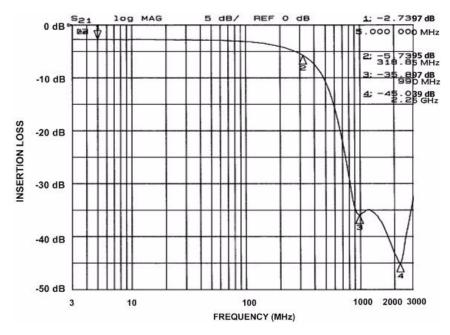


Figure 6. Insertion Loss VS. Frequency (Pins 8 and 9)

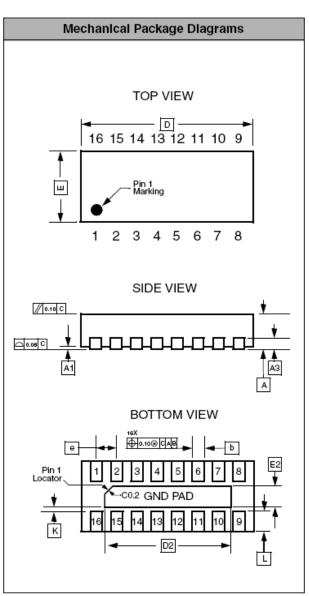
Mechanical Details

uDFN-16 Mechanical Specifications, 0.4mm

The 16-lead, 0.4mm pitch uDFN package dimensions are presented below.

	PAC	KAGE	DIME	NSIO	NS		
Package	uDFN MO-229C [*]						
JEDEC No.							
Leads			1	16			
Dim.	Millimeters			Inches			
Diiii.	Min	Nom	Max	Min	Nom	Max	
Α	0.45	0.50	0.55	0.018	0.020	0.022	
A1	0.00	0.02	0.05	0.000	0.001	0.002	
А3	0.127 REF			0.005 REF			
b	0.15	0.20	0.25	0.006	0.008 0.01		
D	3.20	3.30	3.40	0.126	0.130	0.134	
D2	2.70	2.80	2.90	0.106	0.110	0.114	
E	1.25	1.35	1.45	0.049			
E2	0.30	0.40	0.50	0.012			
е	0.40 BSC			0.016 BSC			
К	0.22 REF			0.009 REF			
L	0.15	0.25	0.35	0.006	0.010	0.014	
# per tape and reel	3000 pieces						
	Contro	Controlling dimension: millimeters					

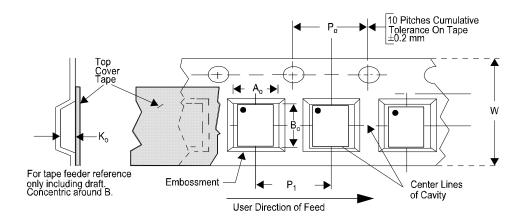
^{*}This package is compliant with JEDEC standard MO-229C with the exception of the D, D2, E, E2, K and L dimensions as called out in the table above.



Dimensions for 16-Lead, 0.4mm pitch uDFN package

Tape and Reel Specifications

PACKAGE SIZE (mm)		POCKET SIZE (mm) B _o X A _o X K _o	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	$P_{\scriptscriptstyle{0}}$	P ₁	
	CM1624 -08DE	3.30 X 1.35 X 0.50	3.50 X 1.55 X 0.70	12mm	178mm (7")	3000	4mm	4mm





ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local

Sales Representative