2SJ194



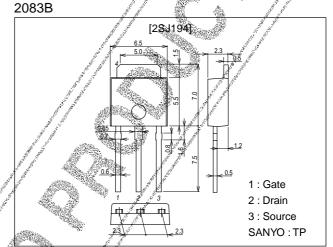
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

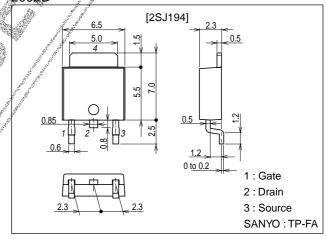
Package Dimensions

unit:mm



unit:mm

2092B



- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges,or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

Specifications

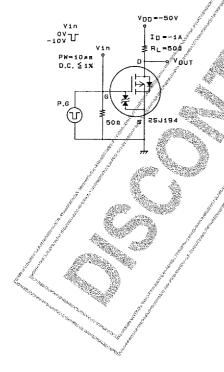
Absolute Maximum Ratings at Ta = 25°C

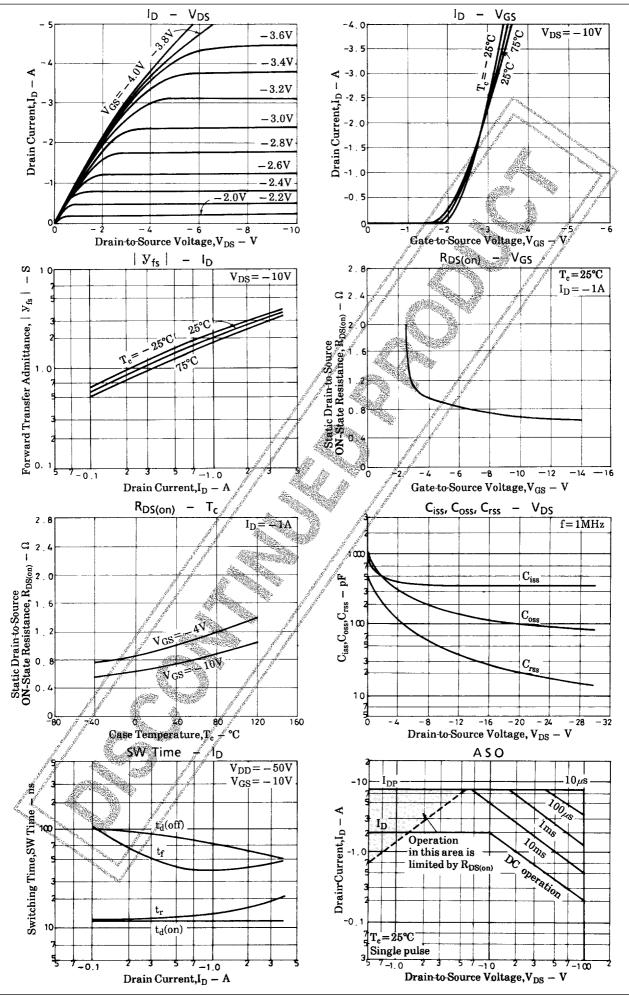
Parameter	Symbol	Conditions	Ratings	Unit	
Drain-to-Source Voltage	V _{DSS}			-100	V
Gate-to-Source Voltage	V _{GSS}		,	±15	V
Drain Current (DC)	I _D		S. Paris	<i>−</i> 2	Α
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	port of	-8	Α
Allowable Power Dissipation	PD	Tc=25°C	And And	20	W
Channel Temperature	Tch		Age Care Care Care Care Care Care Care Car	150	*C
Storage Temperature	Tstg	1	j	÷55 to +150	/ [®] C

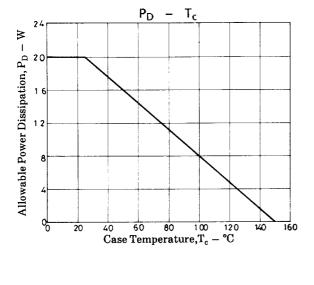
Electrical Characteristics at Ta = 25°C

			250.0	23.52	N. C.	
Parameter	Symbol	Conditions	min	Ratings typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0	-100		<i>*</i>	V
Gate-to-Source Breakdown Voltage	V _(BR) GSS	I _G =±100μA, V _{DS} =0	±15	and and		V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-100V, V _{GS} =0		A STATE OF THE STA	-100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±12V, V _{DS} =0	a de la companya de l	R. Carlotte	±10	μA
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	₃ -1.0		-2.0	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-1A	<i>∮</i> 1.2	2		S
Static Drain-to-Source ON-State Resistance	R _{DS(on)}	I _D =-1A, V _{GS} =-10W	er ja	0.7	0.95	Ω
Static Diam-to-Source ON-State Nesistance	R _{DS(on)}	I _D =-1A, V _{GS} =-4V	T.	0.95	1.3	Ω
Input Capacitance	Ciss	V _{DS} =–20V, f⊋1MHz		380		pF
Output Capacitance	Coss	V _{DS} =-20V ₂ f=1MHz		100		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V _J f=1MHz		20		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		12		ns
Rise Time	t _r	See specified Fest Circuit		14		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		75		ns
Fall Time	t _f	See specified Test Qircuit		40		ns
Diode Forward Voltage	V _{SD}	1 _S =-2A, V _{GS} =0		-1.0	-1.5	V

Switching Time Test Circuit







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