

Features

- Current Bias/Current Sense Architecture
- 3.3 or 5V Supply
- 3.3V CMOS Compatible Logic Interface
- Power Management
- Integrated Reference Resistor
- Single-ended Input to Reader with One Side Grounded Externally
- Differential Read Output
- GMR Resistor Range: 25Ω to 65Ω
- Programmable 5-bit GMR Head Bias Current: $I_b = 2\text{ mA} - 9.75\text{ mA}$
- Programmable Gain Control: 130V/V & 200V/V @ $R_{mr} = 35\Omega$, $I_b = 6\text{ mA}$
- Wide Bandwidth: $\text{BW} = 150\text{ MHz}$ at -3dB @ $R_{mr} = 35\Omega$
- Programmable Booster for Wide Bandwidth
- Equivalent Input Noise: $V_n = 0.8\text{ nV}/\sqrt{\text{Hz}}$ @ $R_{mr} = 35\Omega$
- Impedance Matched Differential Inputs for WDX and WDY
- Programmable 5-Bit Write Head Current: $I_w = 15\text{ mA}$ to 60 mA
- Servo Bank Write Mode
- Rise/Fall Time: 1 ns @ 5V and 2 ns @ 3.3V Typical
($I_w = 40\text{ mA}$ (0-p), $L_{ff} = 50\text{ nH}$, $R_{ff} = 15\Omega$)
- Fast Recovery Times: $\text{W/R} = 200\text{ ns}$ @ 5V Supply and $\text{W/R} = 400\text{ ns}$ @ 3.3V Supply
- Programmable Write Current Overshoot Control
- Read Fault and Write Fault Detection
- R_{mr} Value Measurement Mode
- GMR Head Open/Short and TF Head Protection
- Programmable (7 bits) Thermal Asperity Detection Threshold Control and Programmable Thermal Asperity Compensation
- Serial Port Read Back Capability
- GMR ESD Protection Diodes on Reader
- Internal Capacitor
- Available in a Variety of Packages Depending on Customer Requirements

Description

The AT78C6001 is a BiCMOS 1-channel GMR Preamplifier device (also available as a 2-channel device, AT78C6002). They are designed for use with 4-terminal magneto-resistive recording heads, providing a low noise GMR head amplifier, GMR bias current control, thin film write driver, write current control, thermal asperity detection and correction. The fast recovery mode can be also programmed to put the chip in read mode faster than any other existing modes. The device provides programmable read gain, GMR resistance measurement, and thermal asperity threshold level. The device allows multiple channel write functions for servo writing. Half or all of the heads can be simultaneously selected in the servo write mode. Features and thresholds are controlled through a serial port interface. This product requires +3.3V or +5V supply voltage. It is available in Flip-chip or TSSOP packages.



1-Channel 3.3V or 5V GMR Head Preamplifier

AT78C6001

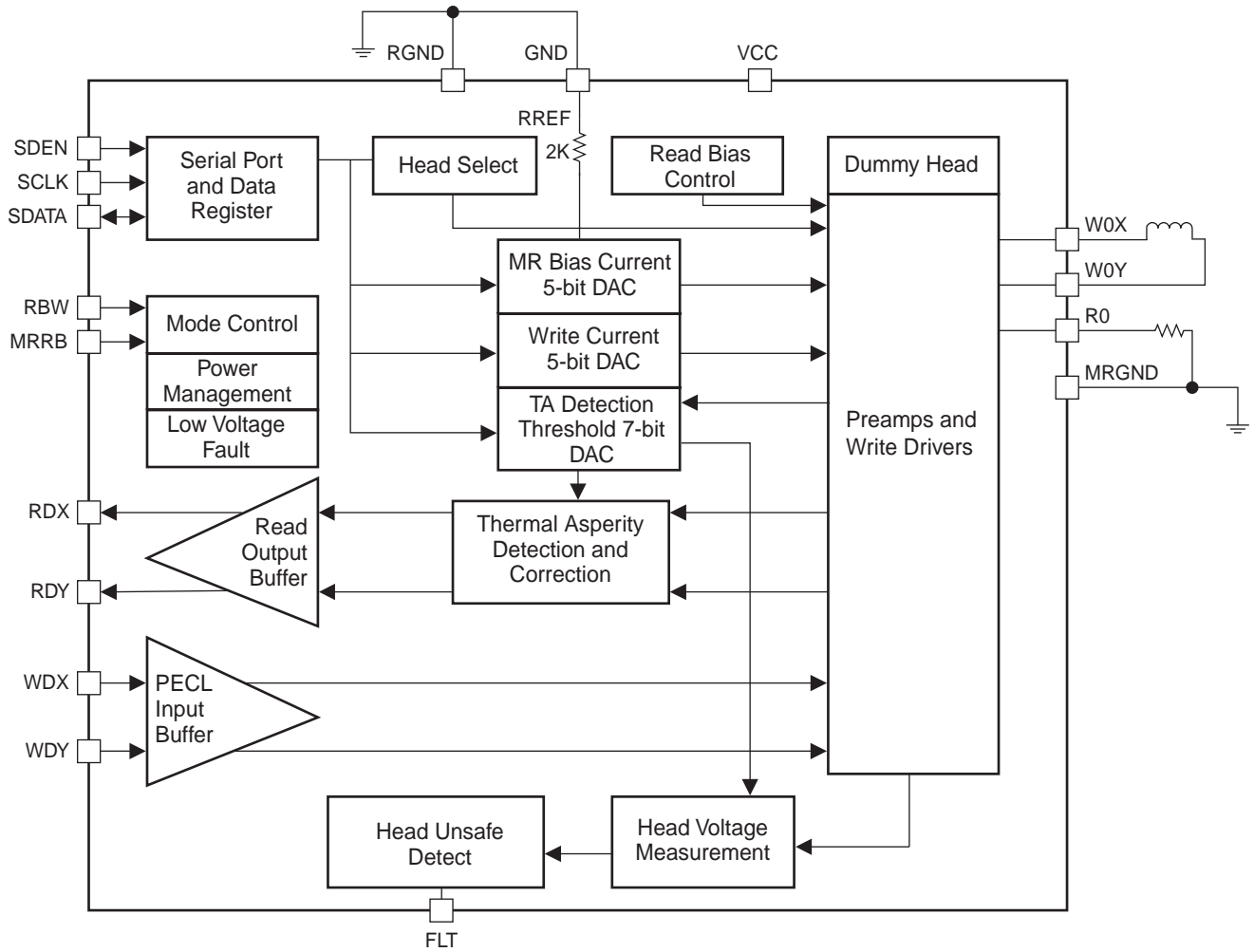
Summary

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Note: This is a summary document. A complete document is available under NDA. For more information, please contact your local Atmel sales office.

Figure 1. Block Diagram





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