

- Triple In★Out Delay
- TTL Compatible
- 14 Pin DIL Package



**Specifications:**

- Delay tolerance :  $\pm 5\%$  or 2ns whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/°C
- Operating Temp. Range:  $-0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$
- Minimum Pulse Width : 40% of total delay
- Supply Voltage :  $5\text{V}_{\text{DC}} \pm 5\%$
- Logic 1 Input current : 50  $\mu\text{A}$  max.
- Logic 0 Input current :  $-2 \text{ mA}$  max.
- Logic 1 Output : 2.75V min
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

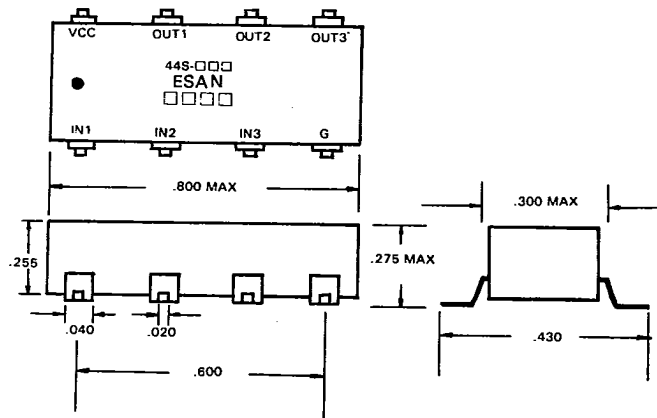
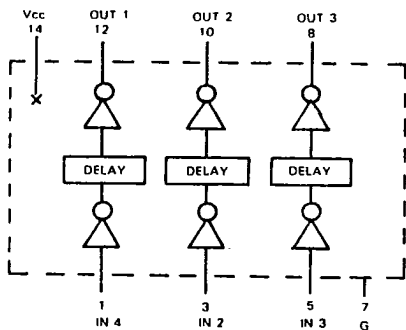
**Features:**

- Schottky TTL interface
- Low power schottky available named "44SL- xxx" series
- Both Leading and Trailing edge available under request named "44SB- xxx" series

**Test Conditions:**

- Input pulse voltage : 3.2V
- Input pulse width : 1.2 \* total delay
- Input pulse spacing : 2 \* total delay
- Input pulse rise time: 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @  $V_{\text{CC}} = 5.0\text{V}$ ,  $T_{\text{A}} = 25^{\circ}\text{C}$

PART NO.	DELAY TIME (ns)
44S-025	25
44S-050	50
44S-075	75
44S-100	100
44S-125	125
44S-150	150
44S-200	200
44S-250	250
44S-300	300
44S-400	400
44S-500	500



Unit: Inch