

#### **Vishay Semiconductors**

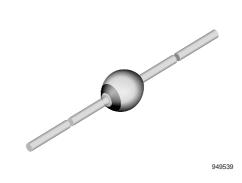
# **Ultra Fast Avalanche Sinterglass Diode**

#### Features

- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- Soft recovery characteristics

#### Applications

Very fast rectification and switches Switched mode power supplies High-frequency inverter circuits



Polarity: Color band denotes cathode end

Mounting Position: Any

Weight: 370 mg, (max. 500 mg)

**Mechanical Data** 

**Case:** Sintered glass case, SOD 57 **Terminals:** Plated axial leads, solderable per MIL-STD-750, Method 2026

#### Parts Table

Part	Type differentiation	Package
BYT53A	V <sub>R</sub> = 50 V; I <sub>FAV</sub> = 1.9 A	SOD57
BYT53B	V <sub>R</sub> = 100 V; I <sub>FAV</sub> = 1.9 A	SOD57
BYT53C	V <sub>R</sub> = 150 V; I <sub>FAV</sub> = 1.9 A	SOD57
BYT53D	V <sub>R</sub> = 200 V; I <sub>FAV</sub> = 1.9 A	SOD57
BYT53F	V <sub>R</sub> = 300 V; I <sub>FAV</sub> = 1.9 A	SOD57
BYT53G	V <sub>R</sub> = 400 V; I <sub>FAV</sub> = 1.9 A	SOD57

#### **Absolute Maximum Ratings**

 $T_{amb}$  = 25 °C, unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Reverse voltage = Repetitive peak reverse	see electrical characteristics	BYT53A	V <sub>R</sub> =	50	V
voltage			V <sub>RRM</sub>		
	see electrical characteristics	BYT53B	V <sub>R</sub> =	100	V
			V <sub>RRM</sub>		
	see electrical characteristics	BYT53C	V <sub>R</sub> =	150	V
			V <sub>RRM</sub>		
	see electrical characteristics	BYT53D	V <sub>R</sub> =	200	V
			V <sub>RRM</sub>		
	see electrical characteristics	BYT53F	V <sub>R</sub> =	300	V
			V <sub>RRM</sub>		
	see electrical characteristics	BYT53G	V <sub>R</sub> =	400	V
			V <sub>RRM</sub>		
Peak forward surge current	t <sub>p</sub> = 10 ms, half sinewave		I <sub>FSM</sub>	50	A

# **BYT53.**





Parameter	Test condition	Sub type	Symbol	Value	Unit
Average forward current	l = 10 mm, T <sub>L</sub> = 25 °C		I <sub>FAV</sub>	1.9	А
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C
Non repetitive reverse avalanche energy	I <sub>(BR)R</sub> = 1 A		E <sub>R</sub>	20	mJ

#### **Maximum Thermal Resistance**

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Value	Unit
Junction ambient	I = 10 mm, T <sub>L</sub> = constant		R <sub>thJA</sub>	45	K/W
	on PC board with spacing 25 mm		R <sub>thJA</sub>	100	K/W

#### **Electrical Characteristics**

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

Parameter	Test condition	Sub type	Symbol	Min	Тур.	Max	Unit
Forward voltage	I <sub>F</sub> = 1 A		V <sub>F</sub>			1.1	V
	I <sub>F</sub> = 1 A, T <sub>j</sub> = 175 °C		V <sub>F</sub>			0.9	V
Reverse current	$V_{\rm R} = V_{\rm RRM}$		I <sub>R</sub>			5	μA
	$V_R = V_{RRM}, T_j = 150 \ ^\circ C$		I <sub>R</sub>			200	μA
Reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, i <sub>R</sub> = 0.25 A		t <sub>rr</sub>			50	ns

**Typical Characteristics** ( $T_{amb}$  = 25 °C unless otherwise specified)

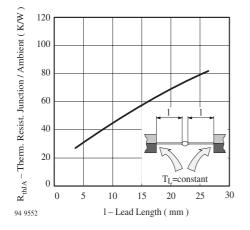


Figure 1. Max. Thermal Resistance vs. Lead Length

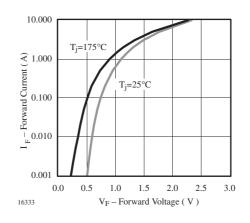


Figure 2. Forward Current vs. Forward Voltage



## BYT53. Vishay Semiconductors

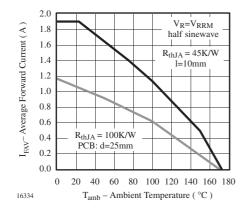


Figure 3. Max. Average Forward Current vs. Ambient Temperature

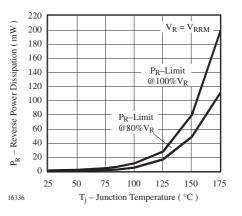


Figure 5. Max. Reverse Power Dissipation vs. Junction Temperature

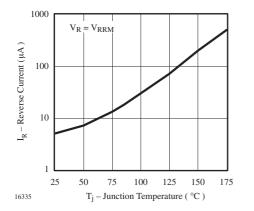


Figure 4. Reverse Current vs. Junction Temperature

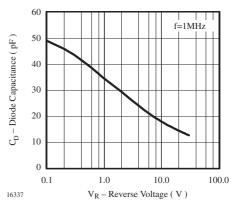
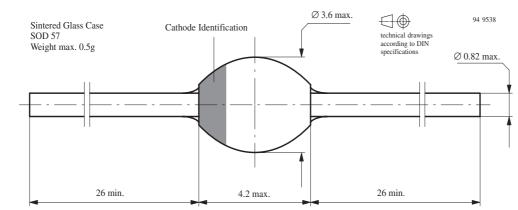


Figure 6. Diode Capacitance vs. Reverse Voltage



## Package Dimensions in mm

### **Vishay Semiconductors**



### **Ozone Depleting Substances Policy Statement**

#### It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

#### We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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