

# PC923X

## High Speed \*OPIC Photocoupler for MOS-FET / IGBT Drive

- \* Lead forming type (I type) and taping reel type (P type) are also available. (PC923XI/PC923XP)
- \*\* TÜV (VDE0884) approved type is also available as an option.

### ■ Features

1. Built-in direct drive circuit for MOS-FET/IGBT drive  
( $I_{O1P}, I_{O2P}: 0.4A$ )
2. High speed response ( $t_{PLH}, t_{PHL}: \text{MAX.} 0.5\mu s$ )
3. Wide operating supply voltage range  
( $V_{CC}: 15 \text{ to } 30V, T_a: -10 \text{ to } 60^\circ C$ )
4. High noise reduction type  
( $CM_H = \text{MIN.} -1.5kV/\mu s$ )  
( $CM_L = \text{MIN.} 1.5kV/\mu s$ )
5. Recognized by UL, file No. E64380 (Model No. **PC923**)
6. High isolation voltage between input and output  
( $V_{ISO(rms)} = 5.0kV$ )

### ■ Applications

1. Inverter controlled air conditioners

### ■ Absolute Maximum Ratings

( $T_a = T_{opr}$  unless otherwise specified)

	Parameter	Symbol	Rating	Unit
Input	Forward current	$I_F$	20	mA
	*1 Reverse voltage	$V_R$	6	V
	Supply voltage	$V_{CC}$	35	V
Output	$O_1$ output current	$I_{O1}$	0.1	A
	*2 $O_1$ peak output current	$I_{O1P}$	0.4	A
	$O_2$ output current	$I_{O2}$	0.1	A
	*2 $O_2$ peak output current	$I_{O2P}$	0.4	A
	$O_1$ output voltage	$V_{O1}$	35	V
	Power dissipation	$P_O$	500	mW
	Total power dissipation	$P_{tot}$	550	mW
*3 Isolation voltage	$V_{iso(rms)}$	5.0	kV	
Operating temperature	$T_{opr}$	-25 to +80	$^\circ C$	
Storage temperature	$T_{stg}$	-55 to +125	$^\circ C$	
*4 Soldering temperature	$T_{sol}$	260	$^\circ C$	

\*1  $T_a = 25^\circ C$

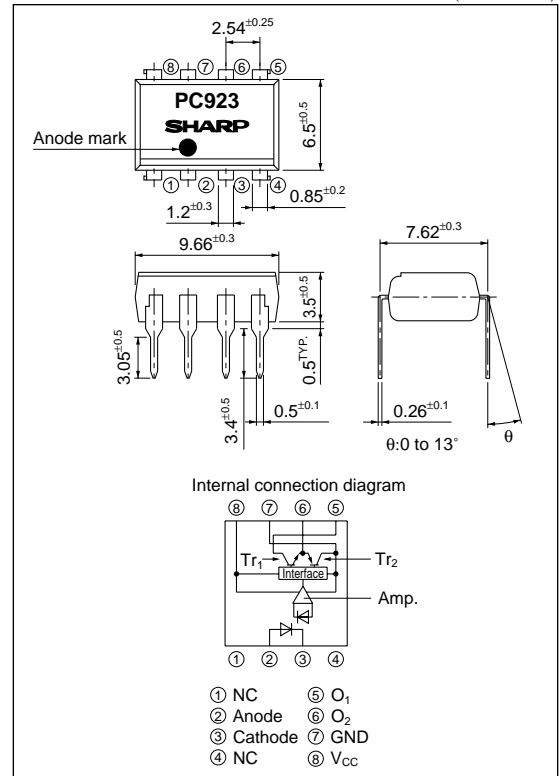
\*2 Pulse width  $\leq 0.15\mu s$ , Duty ratio: 0.01

\*3 40 to 60% RH, AC for 1 minute,  $T_a = 25^\circ C$

\*4 For 10s

### ■ Outline Dimensions

(Unit : mm)



\* "OPIC" (Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and signal-processing circuit integrated onto a single chip.

## ■ Electro-optical Characteristics

(T<sub>a</sub>=T<sub>opr</sub> unless otherwise specified)

Parameter		Symbol	*5 Conditions	MIN.	TYP.	MAX.	Unit	
Input	Forward voltage	V <sub>F1</sub>	T <sub>a</sub> =25°C, I <sub>F</sub> =10mA	–	1.6	1.75	V	
		V <sub>F2</sub>	T <sub>a</sub> =25°C, I <sub>F</sub> =0.2mA	1.2	1.5	–	V	
	Reverse current	I <sub>R</sub>	T <sub>a</sub> =25°C, V <sub>R</sub> =5V	–	–	10	μA	
	Terminal capacitance	C <sub>t</sub>	T <sub>a</sub> =25°C, V=0, f=1kHz	–	30	250	pF	
Output	Operating supply voltage	V <sub>CC</sub>	T <sub>a</sub> =–10 to 60°C	15	–	30	V	
			–	15	–	24	V	
	*6 O <sub>1</sub> low level output voltage	V <sub>O1L</sub>	V <sub>CC1</sub> =12V, V <sub>CC2</sub> =–12V I <sub>O1</sub> =0.1A, I <sub>F</sub> =5mA	–	0.2	0.4	V	
	*7 O <sub>2</sub> high level output voltage	V <sub>O2H</sub>	V <sub>CC</sub> =V <sub>O1</sub> =24V, I <sub>O2</sub> =–0.1A, I <sub>F</sub> =5mA	18	21	–	V	
	*8 O <sub>2</sub> low level output voltage	V <sub>O2L</sub>	V <sub>CC</sub> =24V, I <sub>O2</sub> =0.1A, I <sub>F</sub> =0	–	1.2	2.0	V	
	*9 O <sub>1</sub> leak current	I <sub>O1L</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =V <sub>O1</sub> =35V, I <sub>F</sub> =0	–	–	500	μA	
	*10 O <sub>2</sub> leak current	I <sub>O2L</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =V <sub>O2</sub> =35V, I <sub>F</sub> =5mA	–	–	500	μA	
	*11 High level supply current	I <sub>CCH</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =24V, I <sub>F</sub> =5mA	–	6	10	mA	
			V <sub>CC</sub> =24V, I <sub>F</sub> =5mA	–	–	14	mA	
	*11 Low level supply current	I <sub>CCL</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =24V, I <sub>F</sub> =0	–	8	13	mA	
			V <sub>CC</sub> =24V, I <sub>F</sub> =0	–	–	17	mA	
Transfer characteristics	*12 "Low→High" threshold input current	I <sub>FLH</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =24V	0.3	1.5	3.0	mA	
			V <sub>CC</sub> =24V	0.2	–	5.0	mA	
	Isolation resistance	R <sub>ISO</sub>	T <sub>a</sub> =25°C, DC=500V, 40 to 60%RH	5×10 <sup>10</sup>	10 <sup>11</sup>	–	Ω	
	Response time	*13 "Low→High" propagation delay time	t <sub>PLH</sub>	T <sub>a</sub> =25°C, V <sub>CC</sub> =24V, I <sub>F</sub> =5mA R <sub>C</sub> =47Ω, C <sub>G</sub> =3 000pF	–	0.3	0.5	μs
		*13 "High→Low" propagation delay time	t <sub>PHL</sub>		–	0.3	0.5	μs
		*13 Rise time	t <sub>r</sub>		–	0.2	0.5	μs
		*13 Fall time	t <sub>f</sub>		–	0.2	0.5	μs
*14 Instantaneous common mode rejection voltage "Output:High level"	CM <sub>H</sub>	T <sub>a</sub> =25°C, V <sub>CM</sub> =600V (peak) I <sub>F</sub> =5mA, V <sub>CC</sub> =24V, ΔV <sub>O2H</sub> =2.0V	–1.5	–	–	kV/μs		
*14 Instantaneous common mode rejection voltage "Output:Low level"	CM <sub>L</sub>	T <sub>a</sub> =25°C, V <sub>CM</sub> =600V (peak) I <sub>F</sub> =0, V <sub>CC</sub> =24V, ΔV <sub>O2L</sub> =2.0V	1.5	–	–	kV/μs		

\*5 When measuring output and transfer characteristics, connect a by-pass capacitor (0.01μF or more) between V<sub>CC</sub> and GND near the device

\*6 Refer to Fig.1

\*7 Refer to Fig.2

\*8 Refer to Fig.3

\*9 Refer to Fig.4

\*10 Refer to Fig.5

\*11 Refer to Fig.6

\*12 I<sub>FLH</sub> represents forward current when output goes from "Low" to "High", Refer to Fig.7

\*13 Refer to Fig.8

\*14 Refer to Fig.9

## ■ Truth Table

Input	O <sub>2</sub> Output	Tr.1	Tr.2
ON	High level	ON	OFF
OFF	Low level	OFF	ON

■ Test Circuit

Fig.1

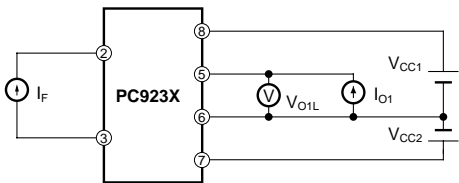


Fig.2

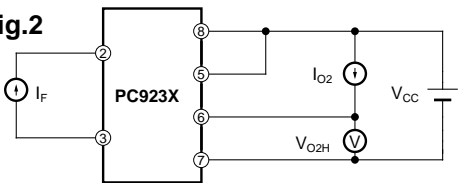


Fig.3

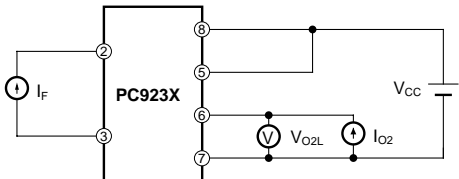


Fig.4

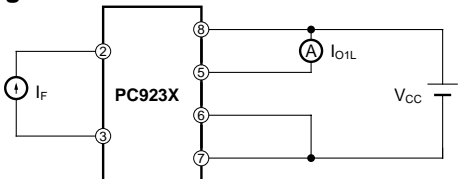


Fig.5

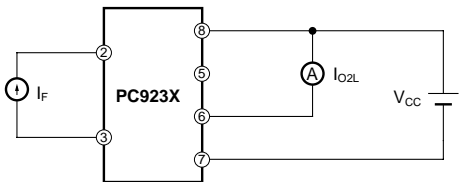


Fig.6

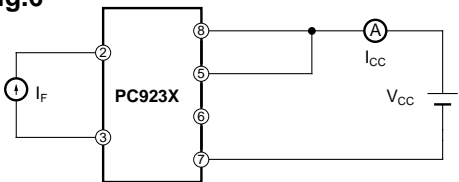


Fig.7

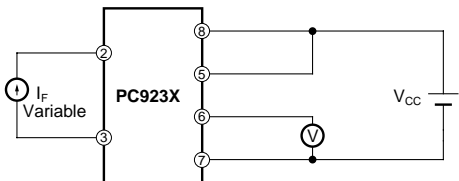


Fig.8

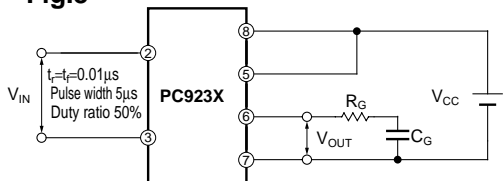
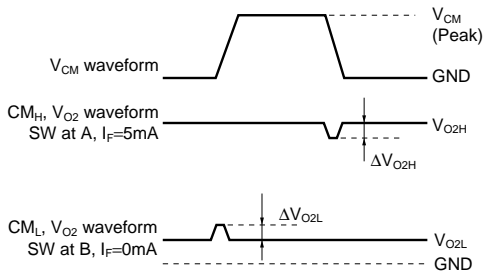
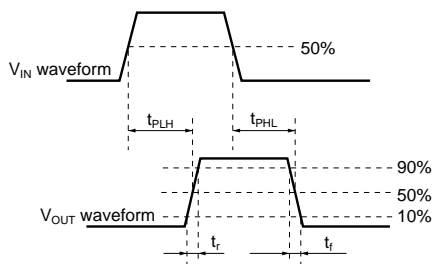
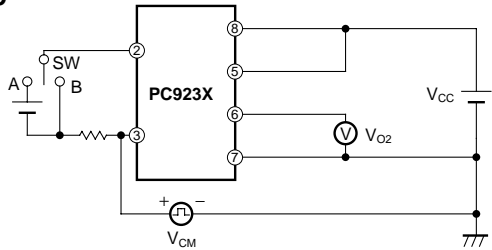
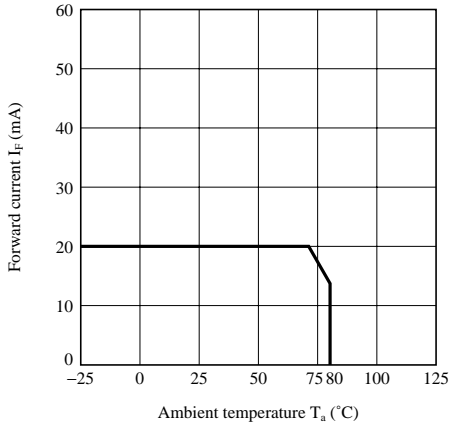


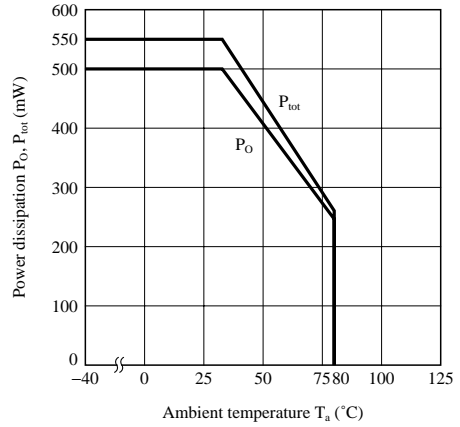
Fig.9



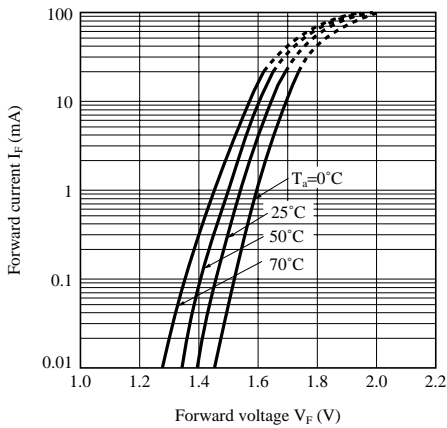
**Fig.10 Forward Current vs. Ambient Temperature**



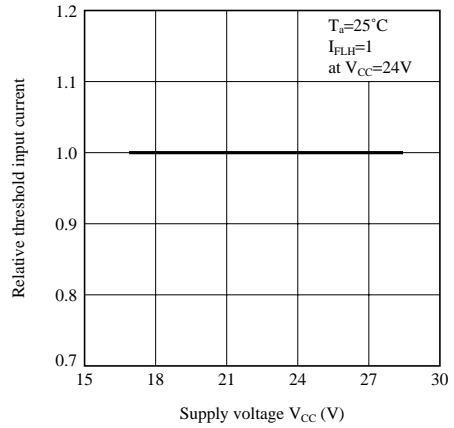
**Fig.11 Power Dissipation vs. Ambient Temperature**



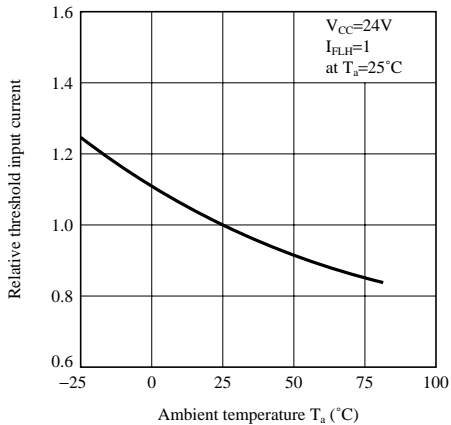
**Fig.12 Forward Current vs. Forward Voltage**



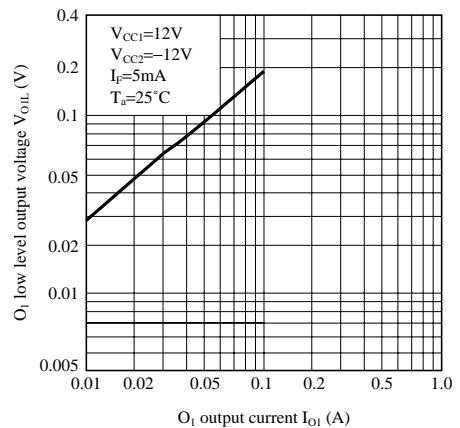
**Fig.13 "Low→High" Relative Threshold Input Current vs. Supply Voltage**



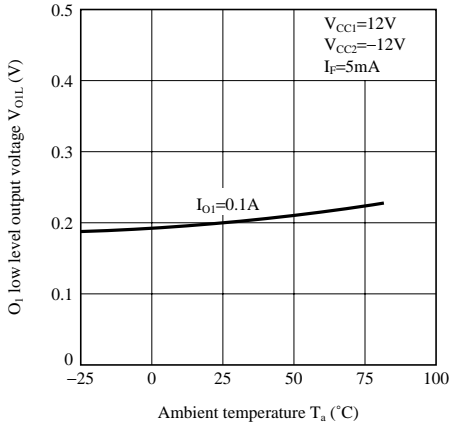
**Fig.14 "Low→High" Relative Threshold Input Current vs. Ambient Temperature**



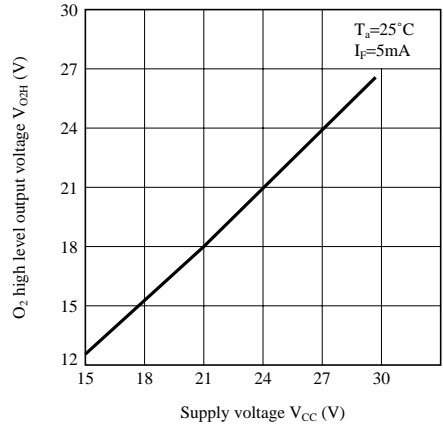
**Fig.15 O<sub>1</sub> Low Level Output Voltage vs. O<sub>1</sub> Output Current**



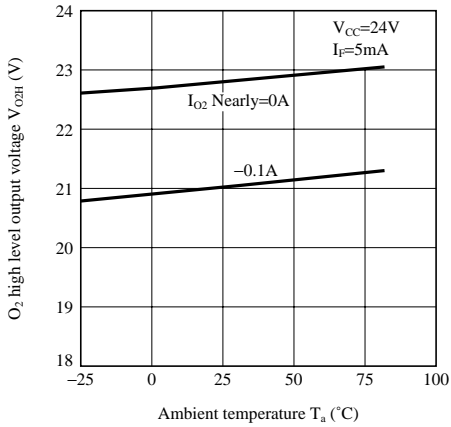
**Fig.16 O<sub>1</sub> Low Level Output Voltage vs. Ambient Temperature**



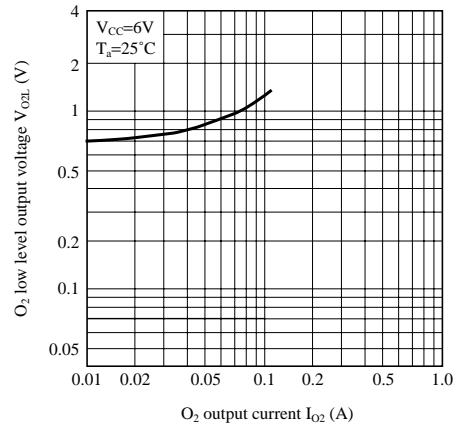
**Fig.17 O<sub>2</sub> High Level Output Voltage vs. Supply Voltage**



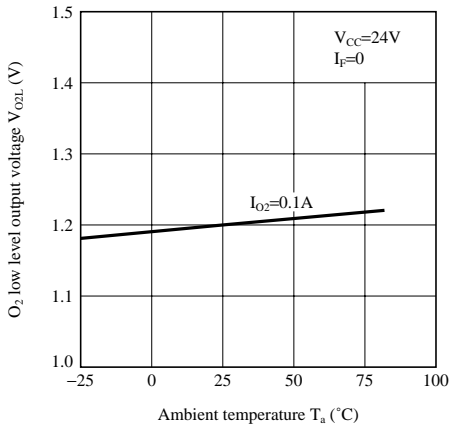
**Fig.18 O<sub>2</sub> High Level Output Voltage vs. Ambient Temperature**



**Fig.19 O<sub>2</sub> Low Level Output Voltage vs. O<sub>2</sub> Output Current**



**Fig.20 O<sub>2</sub> Low Level Output Voltage vs. Ambient Temperature**



**Fig.21 High Level Supply Current vs. Supply Voltage**

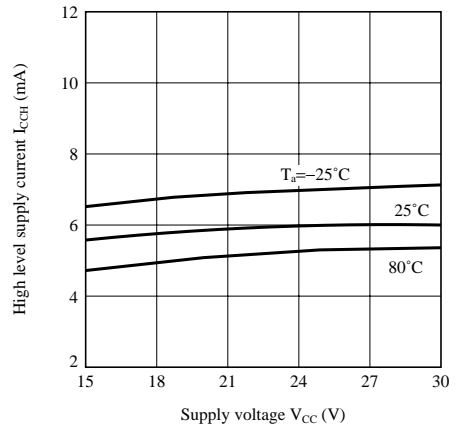


Fig.22 Low Level Supply Current vs. Supply Voltage

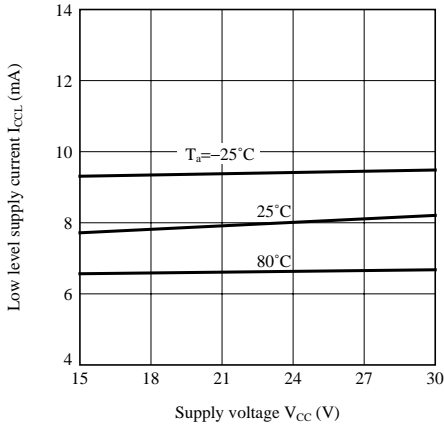


Fig.23 Propagation Delay Time vs. Forward Current

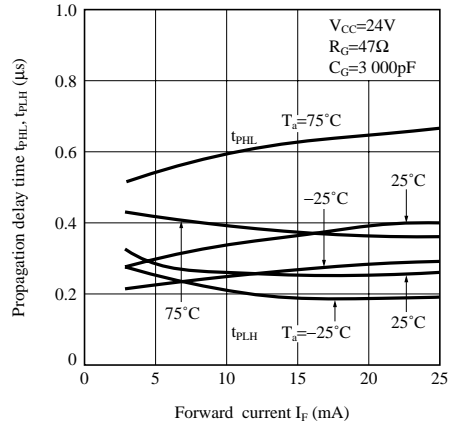
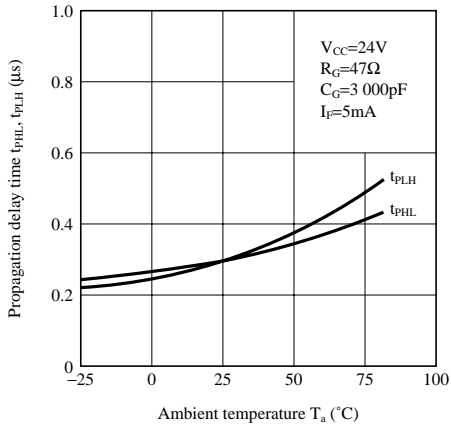
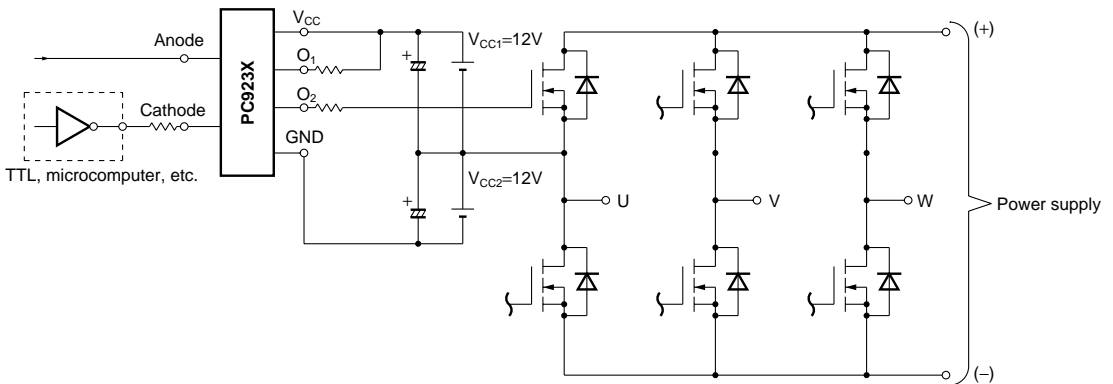


Fig.24 Propagation Delay Time vs. Ambient Temperature



■ Application Circuit (Foe Power MOS-FET Driving Inverter)



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