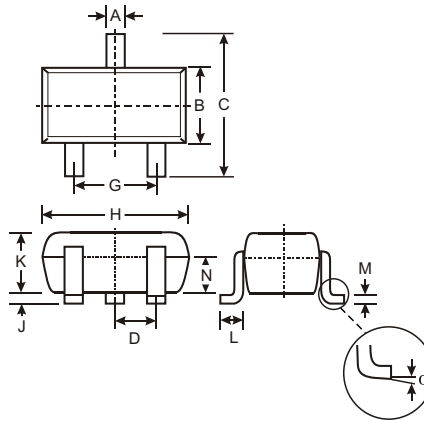


### Features

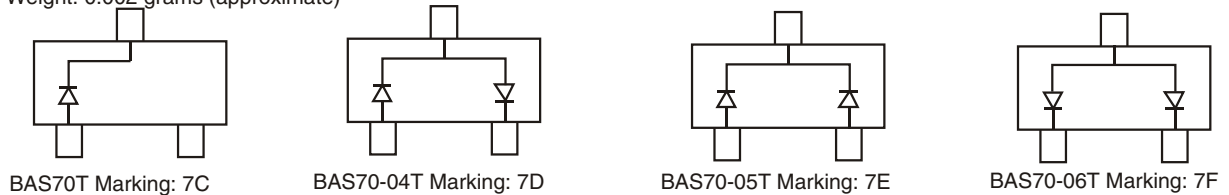
- Low Turn-on Voltage
- Fast Switching
- PN Junction Guard Ring for Transient and ESD Protection
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 3)**

### Mechanical Data

- Case: SOT-523
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminals: Finish - Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Polarity: See Diagrams Below
- Marking: See Diagrams Below & Page 2
- Ordering Information, See Page 2
- Weight: 0.002 grams (approximate)



SOT-523			
Dim	Min	Max	Typ
A	0.15	0.30	0.22
B	0.75	0.85	0.80
C	1.45	1.75	1.60
D			0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
J	0.00	0.10	0.05
K	0.60	0.80	0.75
L	0.10	0.30	0.22
M	0.10	0.20	0.12
N	0.45	0.65	0.50
	0	8	
All Dimensions in mm			



### Maximum Ratings and Electrical Characteristics, Single Diode @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	70	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	49	V
Forward Continuous Current (Note 1)	I <sub>FM</sub>	70	mA
Non-Repetitive Peak Forward Surge Current @ t <sub>p</sub> 1.0s	I <sub>FSM</sub>	100	mA
Power Dissipation (Note 1)	P <sub>d</sub>	150	mW
Thermal Resistance Junction to Ambient Air (Note 1)	R <sub>JA</sub>	833	°C/W
Operating Temperature Range	T <sub>j</sub>	-55 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-65 to +150	°C

### Electrical Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V <sub>(BR)R</sub>	70	—	—	I <sub>R</sub> = 10 A
Forward Voltage	V <sub>F</sub>	—	410 1000	mV	t <sub>p</sub> < 300μs, I <sub>F</sub> = 1.0mA t <sub>p</sub> < 300μs, I <sub>F</sub> = 15mA
Leakage Current (Note 2)	I <sub>R</sub>	—	100	nA	t <sub>p</sub> < 300μs, V <sub>R</sub> = 50V
Total Capacitance	C <sub>T</sub>	—	2.0	pF	V <sub>R</sub> = 0V, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	5.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA to I <sub>R</sub> = 1.0mA, I <sub>rr</sub> = 0.1 x I <sub>R</sub> , R <sub>L</sub> = 100

Notes: 1. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.  
2. Short duration test pulse used to minimize self-heating effect.  
3. No purposefully added lead.

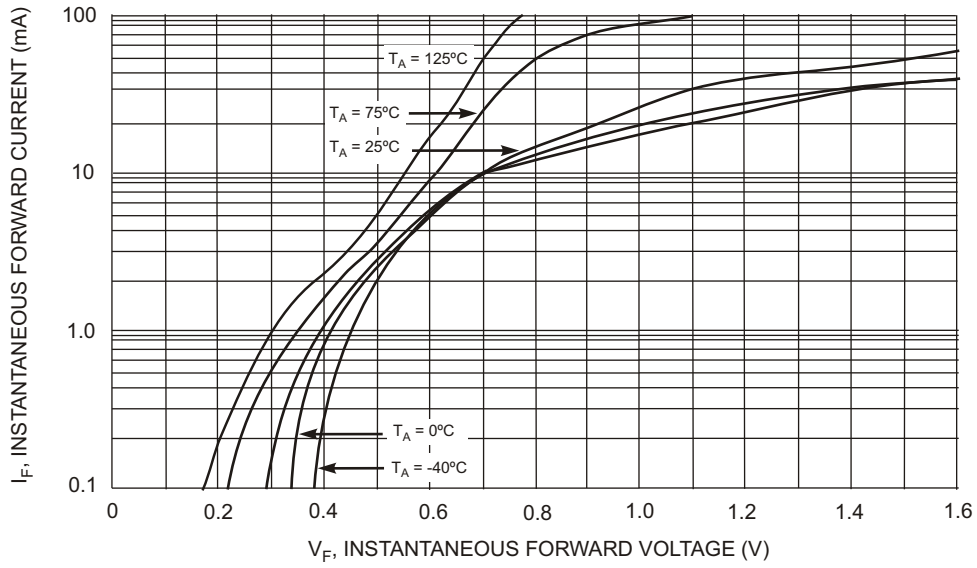


Fig. 1 Typical Forward Characteristics

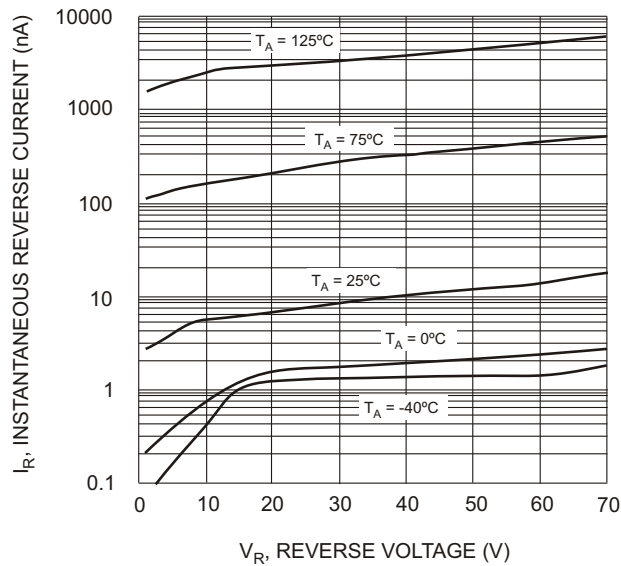


Fig. 2 Typical Reverse Characteristics

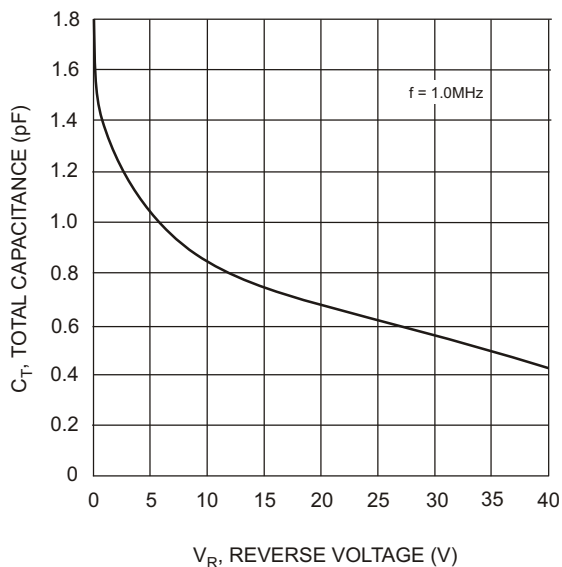


Fig. 3 Typical Capacitance

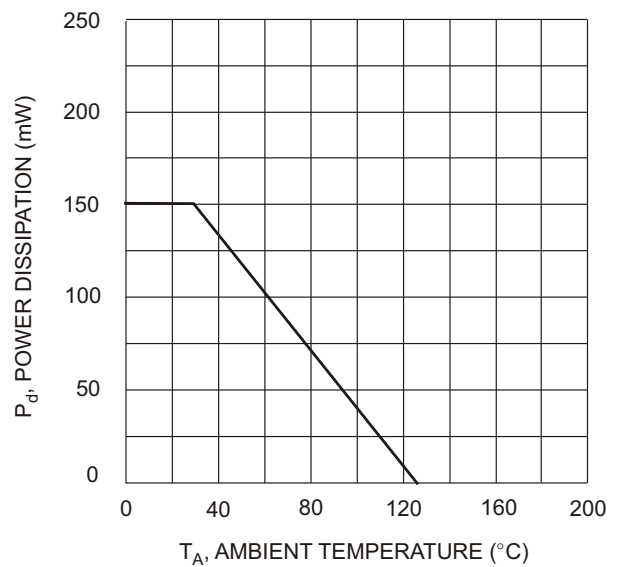


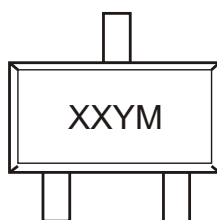
Fig. 4 Power Derating Curve, Total Package

## Ordering Information (Note 4)

Device	Packaging	Shipping
BAS70T-7-F	SOT-523	3000/Tape & Reel
BAS70-04T-7-F	SOT-523	3000/Tape & Reel
BAS70-05T-7-F	SOT-523	3000/Tape & Reel
BAS70-06T-7-F	SOT-523	3000/Tape & Reel

Notes: 4. For Packaging Details: go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



XX = Product Type Marking Code (See Page 1, e.g. 7C = BAS70T)  
 YM = Date Code Marking  
 Y = Year (ex: N = 2002)  
 M = Month (ex: 9 = September)

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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