

PowerMOS transistor Logic Level FET

BUK545-50A
BUK545-50B

T-39-09

GENERAL DESCRIPTION

N-channel enhancement mode logic level field-effect power transistor in a plastic full-pack envelope.

The device is intended for use in Switched Mode Power Supplies (SMPS), motor control, welding, DC/DC and AC/DC converters, and in general purpose switching applications.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
	BUK545	-50A	-50B	
V_{DS}	Drain-source voltage	50	50	V
I_D	Drain current (DC)	20	18	A
P_{tot}	Total power dissipation	30	30	W
T_J	Junction temperature	150	150	°C
$R_{DS(ON)}$	Drain-source on-state resistance $V_{GS} = 5\text{ V}$	0.042	0.055	Ω

MECHANICAL DATA

Dimensions in mm

Net Mass: 2g

Pinning:

1 = Gate

2 = Drain

3 = Source

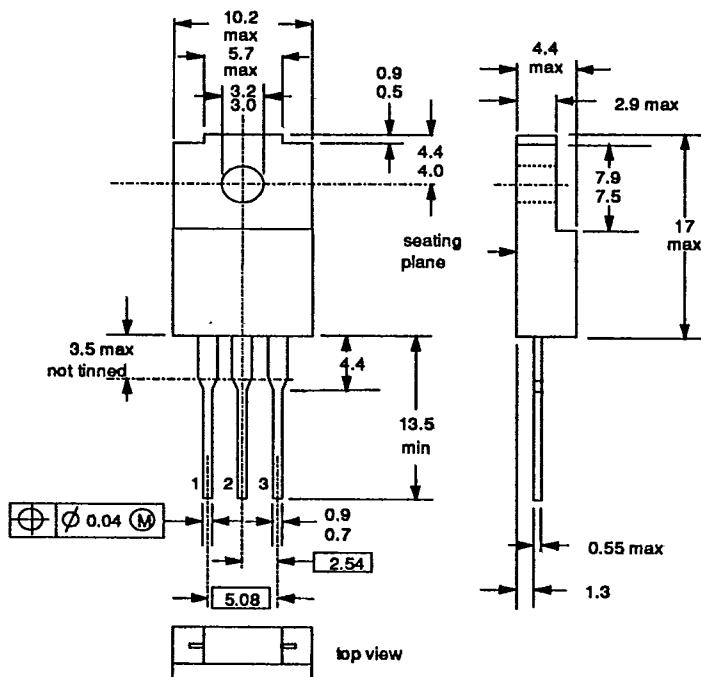
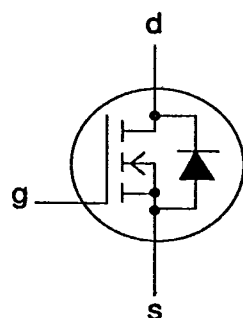


Fig.1 SOT-186; The seating plane is electrically isolated from all terminals.

Notes

1. Observe the general handling precautions for electrostatic-discharge sensitive devices (ESDs) to prevent damage to MOS gate oxide.
2. Accessories supplied on request: refer to Mounting instructions for F-pack envelopes.

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RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DS}	Drain-source voltage	-	-	50	V
V_{DGR}	Drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	50	V
$\pm V_{GS}$	Gate-source voltage	-	-	15	V
I_D	Drain current (DC)	$T_{hs} = 25^\circ\text{C}$	-	-50A 20	A
I_D	Drain current (DC)	$T_{hs} = 100^\circ\text{C}$	-	13	A
I_{DM}	Drain current (pulse peak value)	$T_{hs} = 25^\circ\text{C}$	-	80	A
P_{tot}	Total power dissipation	$T_{hs} = 25^\circ\text{C}$	-	30	W
T_{stg}	Storage temperature	-	-55	150	$^\circ\text{C}$
T_j	Junction Temperature	-	-	150	$^\circ\text{C}$

THERMAL RESISTANCES

From junction to heatsink	with heatsink compound	$R_{th\ j-hs} = 4.17 \text{ K/W}$
From junction to ambient		$R_{th\ j-a} = 55 \text{ K/W}$

STATIC CHARACTERISTICS

 $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.25 \text{ mA}$	50	-	-	V
$V_{GS(TO)}$	Gate threshold voltage	$V_{DS} = V_{GS}; I_D = 1 \text{ mA}$	1.0	1.5	2.0	V
I_{DSS}	Zero gate voltage drain current	$V_{DS} = 50 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25^\circ\text{C}$	-	1	10	μA
I_{DSS}	Zero gate voltage drain current	$V_{DS} = 50 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125^\circ\text{C}$	-	0.1	1.0	mA
I_{GSS}	Gate source leakage current	$V_{GS} = \pm 15 \text{ V}; V_{DS} = 0 \text{ V}$	-	10	100	nA
$R_{DS(ON)}$	Drain-source on-state resistance	$V_{GS} = 5 \text{ V}; I_D = 20 \text{ A}$	-	0.035	0.042	Ω
		BUK545-50A	-	0.045	0.055	Ω
		BUK545-50B	-			

DYNAMIC CHARACTERISTICS

 $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
g_{fs}	Forward transconductance	$V_{DS} = 25 \text{ V}; I_D = 20 \text{ A}$	15	20	-	S
C_{iss}	Input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz}$	-	1450	1750	pF
C_{oss}	Output capacitance		-	500	600	pF
C_{rss}	Feedback capacitance		-	220	275	pF
t_{don}	Turn-on delay time	$V_{DD} = 30 \text{ V}; I_D = 3 \text{ A};$	-	25	40	ns
t_r	Turn-on rise time	$V_{GS} = 5 \text{ V}; R_{GS} = 50 \Omega;$	-	120	150	ns
t_{doff}	Turn-off delay time	$R_{gen} = 50 \Omega$	-	160	220	ns
t_f	Turn-off fall time		-	110	145	ns
L_d	Internal drain inductance	Measured from drain lead 6 mm from package to centre of die	-	4.5	-	nH
L_s	Internal source inductance	Measured from source lead 6 mm from package to source bond pad	-	7.5	-	nH

ISOLATION

 $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq 65\%$; clean and dustfree	-	-	1500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1 \text{ MHz}$	-	12	-	pF

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Philips Components

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REVERSE DIODE RATINGS AND CHARACTERISTICS

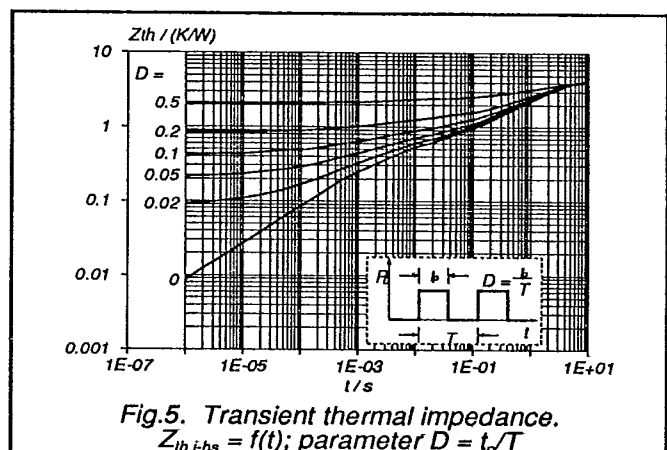
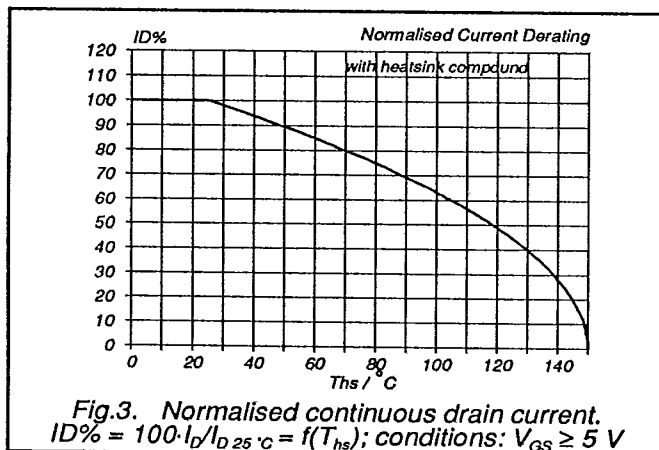
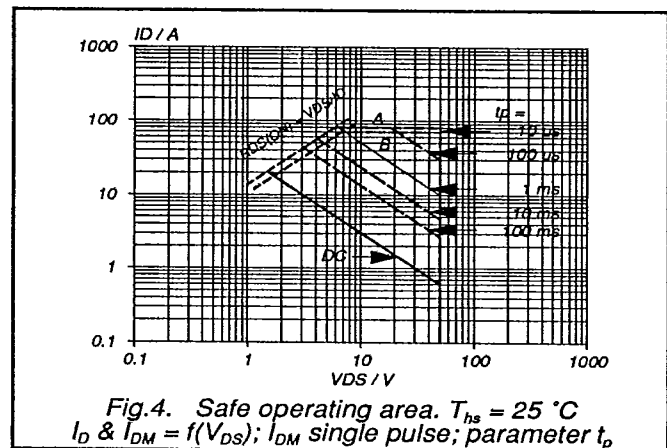
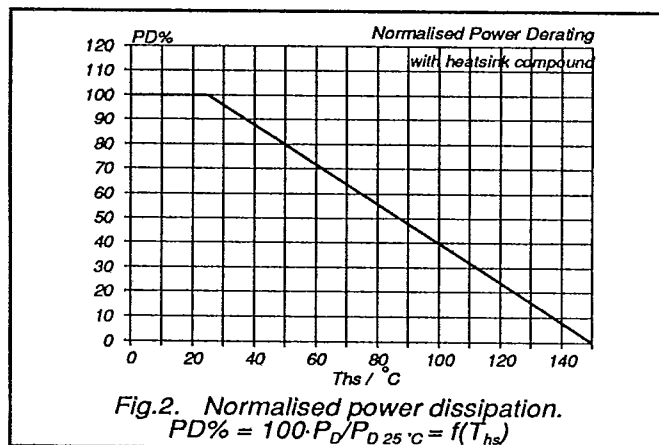
 $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{DR}	Continuous reverse drain current	-	-	-	20	A
I_{DRM}	Pulsed reverse drain current	-	-	-	80	A
V_{SD}	Diode forward voltage	$I_F = 20\text{ A}; V_{GS} = 0\text{ V}$	-	1.4	2.0	V
t_{rr}	Reverse recovery time	$I_F = 20\text{ A}; -di_F/dt = 100\text{ A}/\mu\text{s}; V_{GS} = 0\text{ V}; V_R = 30\text{ V}$	-	250	-	ns
Q_{rr}	Reverse recovery charge	$I_F = 20\text{ A}; -di_F/dt = 100\text{ A}/\mu\text{s}; V_{GS} = 0\text{ V}; V_R = 30\text{ V}$	-	0.30	-	μC

AVALANCHE RATING

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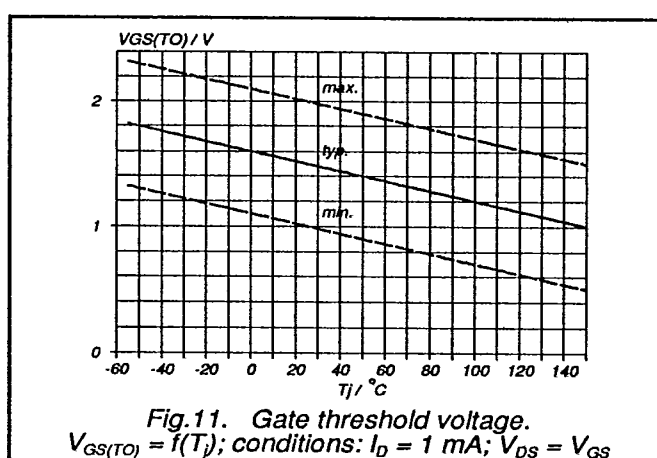
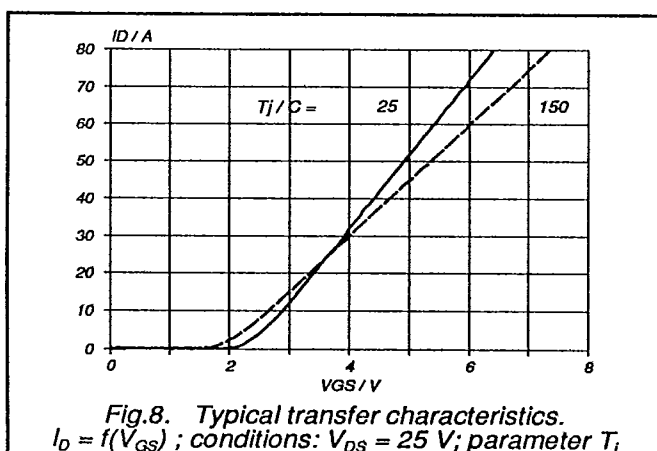
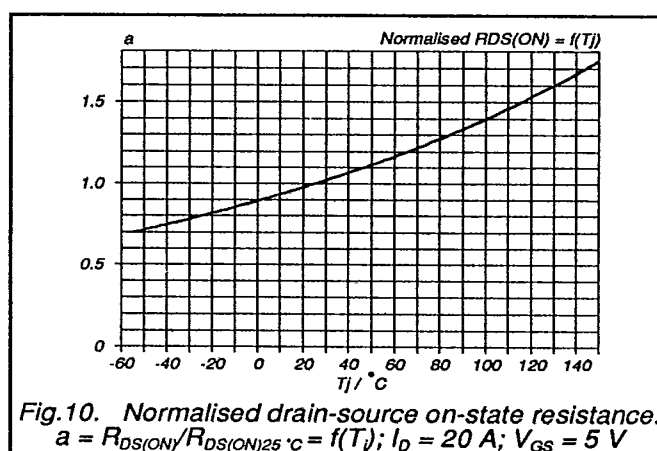
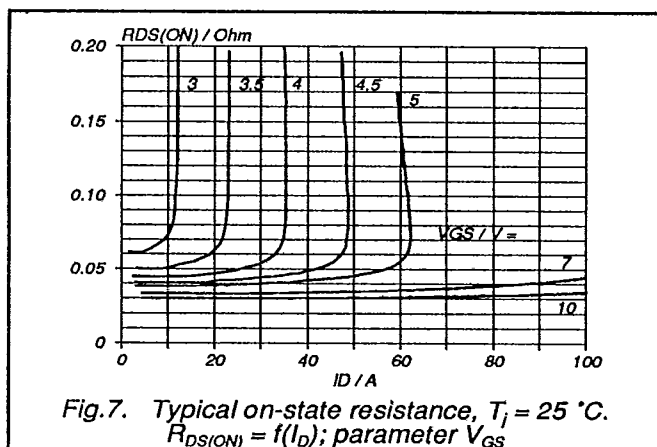
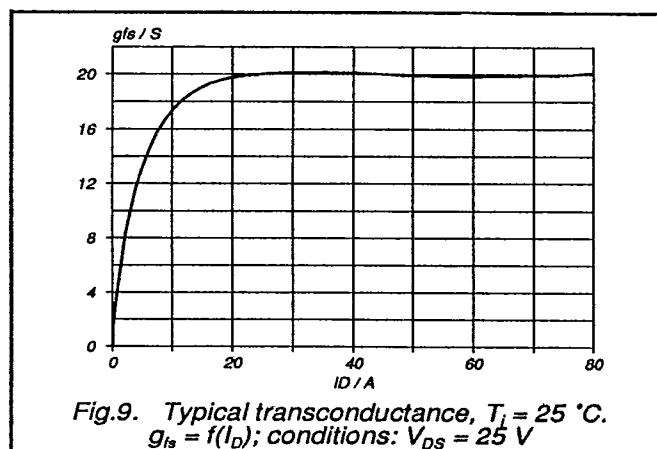
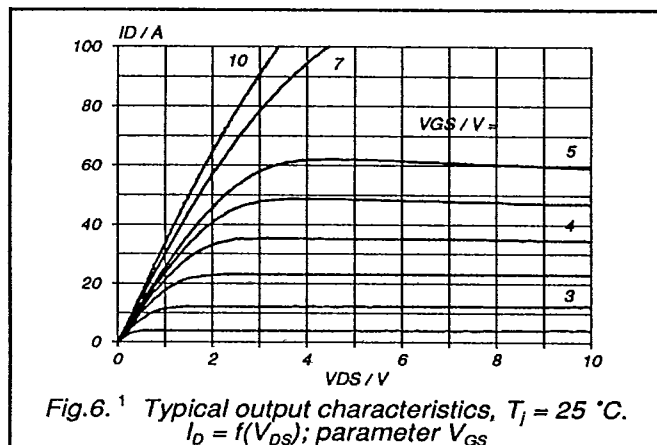
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
W_{DSS}	Drain-source non-repetitive unclamped inductive turn-off energy	$I_D = 39\text{ A}; V_{DD} \leq 25\text{ V}; V_{GS} = 5\text{ V}; R_{GS} = 50\ \Omega$	-	-	90	mJ



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