

- 14 Pin Chip Carrier • Designed For Surface Mounting • H-Cmos Logic
- 5 Equally Spaced Taps

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1 μ A Max
- Supply Current : ICCH = 5mA, ICCL = 20 μ A
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C

Part Number	Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
622-4*	4±1	1±0.5
622-5*	5±1	1.2±0.5
622-6*	6±1	1.5±0.5
622-8*	8±2	2±0.5
622-10*	10±2	2.5±0.5
622-12*	12±2	3±0.5
622-16*	16±2	4±0.6
622-25	25±2	5±0.8
622-30	30±2	6±1
622-35	35±2	7±1
622-40	40±2	8±1
622-45	45±2.2	9±1
622-50	50±2.5	10±2
622-60	60±3	12±2
622-75	75±3.5	15±2
622-100	100±5.0	20±2
622-125	125±5.5	25±2
622-150	150±6.0	30±3
622-175	175±8.7	35±3.5
622-200	200±10	40±4
622-250	250±12.5	50±5

*Time Delay measured with respect to 1st Tap.

