

5.0Vdc HCMOS/TTL COMPATIBLE SMD CRYSTAL CLOCK OSCILLATOR

ASLV SERIES

* RoHS COMPLIANT



7.0 x 5.0 x 1.8mm

FEATURES:

- Low profile (1.7) ceramic VCXO
- Enable/Disable functions
- 7.0mm x 5.0mm seam welded
- HCMOS/TTL output
- Suitable for reflow methods

APPLICATIONS:

- Phase locked loops (PLLs)
- Synthesizers
- Clock Recover, Digital Transmission device
- Digital set-top box

STANDARD SPECIFICATIONS:

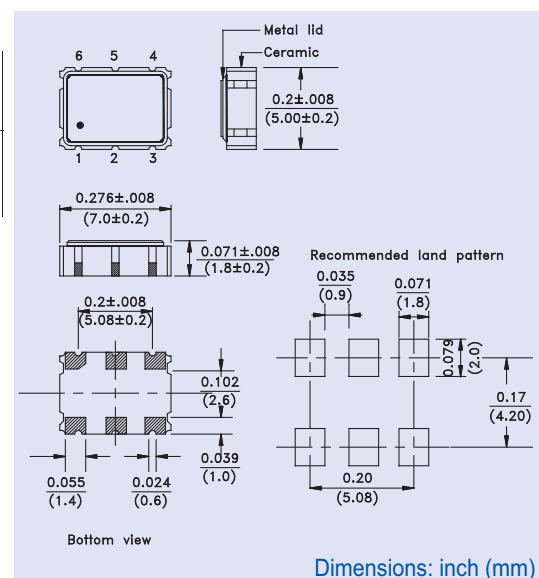
| PARAMETERS | |
|-----------------------------|--|
| Frequency Range | 1.00 MHz to 155.52 MHz |
| Operating Temperature | 0°C to + 70°C |
| Storage Temperature | - 55°C to + 125°C |
| Overall Frequency Stability | ± 50 ppm max. |
| Supply Voltage (Vdd) | 5.0 Vdc ± 5% |
| Input Current | 30mA max. |
| Symmetry | 40/60 % max. @ 1/2Vdd (see options) |
| Rise and Fall Time (Tr/Tf) | 8nS max |
| Output Load | 15pF (2TTL) |
| Output Disable/Enable time | 100ns max |
| Output Voltage | VOH = 0.9*Vdd min VOL = 0.4 Vdc max |
| Pulling | ± 100 ppm min. |
| Start-up Time | 10mS max |
| Tristate Function | "1" (VIH >= 2.2 Vdc) or open: Oscillation "0" (VIL < 0.8 Vdc) : Option A/Hi Z |
| Linearity | ± 10% max |

| PIN | FUNCTION |
|-----|--------------------|
| 1 | Voltage Control Vc |
| 2 | NC or Tri-state |
| 3 | GND/Case |
| 4 | Output |
| 5 | NC |
| 6 | Vdd |

MARKING:

- XX.X -A (Frequency); A - Tri-state option
- ASLV ZY (Z: month A to L; Y: year 0 for 2000, 1 for 2001)

OUTLINE DRAWING:



OPTIONS AND PART IDENTIFICATION (Left blank if standard):

ASLV - Frequency - Temperature - Duty cycle - Tri-state - Packaging

Temperature options:

- D for -10°C to +60°C
- E for -20°C to +70°C
- F for -30°C to +70°C

Symmetry option:

- S for 45/55% at 1/2Vdd

Tri-state mode option:

- A for tristate

Packaging option:

- T for Tape and Reel (1,000pcs/reel)