

DELAY LINES

T-47-29

DELAY LINES (DCE SERIES)

TDK DCE series delay lines apply high level winding technology to high performance ferrite inductors and multilayer chip capacitors. Molded with high quality resin, these delay lines are small and highly reliable, and possess stable propagation and temperature characteristics.

Features

- Transfer molded using quality resin for high quality.
- Small design through DUAL-IN-LINE packaging.
- Inductance with good frequency and temperature characteristics for extremely stable propagation and temperature characteristics.
- Operating temperature range from -10°C to $+85^{\circ}\text{C}$.

Common specifications

Operating temperature: -10 to $+85^{\circ}\text{C}$

Storage temperature: -55 to $+100^{\circ}\text{C}$

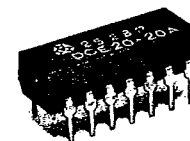
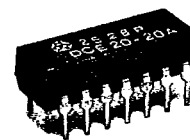
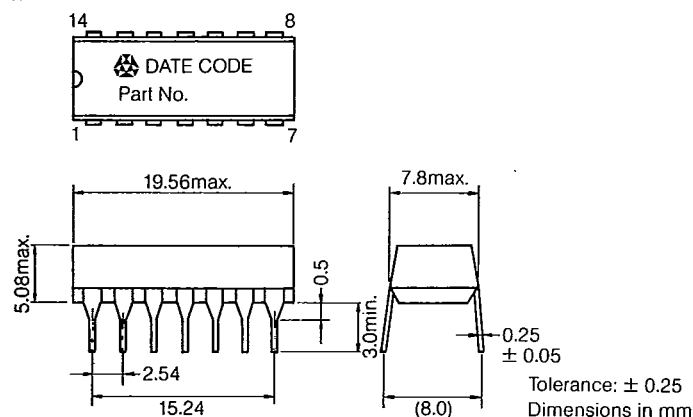
Insulation resistance: 100 M Ω min. (50 Vdc)

Withstand voltage: 50 Vdc

Delay time temperature coefficient: ± 150 ppm/ $^{\circ}\text{C}$

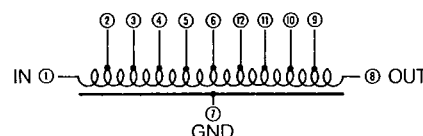
Distortion factor: 15% max.

Maximum rated current: 50 mA

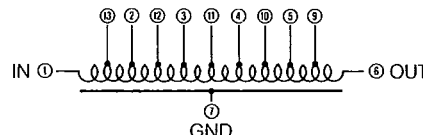


Connections

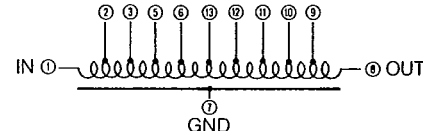
Standard



Style A



Style B



Type	Total delay time (ns) $\pm 5\%$	Characteristics impedance (Ω) $\pm 10\%$	Delay time between taps (ns) $\pm 3\text{ns}$	Rise time (ns) max.	Attenuation (%) max.
DCE01-10-□*	10 $\pm 2\text{ns}$	100	1 $\pm 0.4\text{ns}$	2	10
02-10-□	20 $\pm 2\text{ns}$	100	2 $\pm 0.6\text{ns}$	4	10
03-10-□	30 $\pm 2\text{ns}$	100	3 $\pm 0.8\text{ns}$	6	10
04-10-□	40	100	4 $\pm 1.0\text{ns}$	8	10
05-10-□	50	100	5	10	10
10-10-□	100	100	10	20	10
15-10-□	150	100	15	30	10
20-10-□	200	100	20	40	10
25-10-□	250	100	25	50	10
DCE05-20-□	50	200	5	10	10
07-20-□	70	200	7	15	10
10-20-□	100	200	10	20	10
15-20-□	150	200	15	30	10
20-20-□	200	200	20	40	10
25-20-□	250	200	25	50	10
30-20-□	300	200	30	60	10
35-20-□	350	200	35 $\pm 3.5\text{ns}$	70	10
DCE05-35-□	50	350	5	10	10
10-35-□	100	350	10	20	10
15-35-□	150	350	15	30	10
20-35-□	200	350	20	40	10
25-35-□	250	350	25	50	10
30-35-□	300	350	30	60	10

* Indicate a desired connection in block (□) if not a standard.