BTA06/BTB06 Series 6A TRIACs

DESCRIPTION:

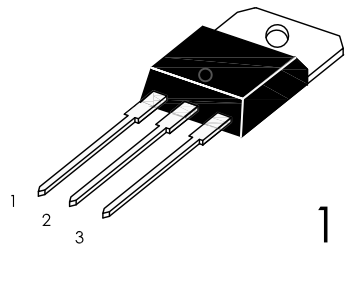
High current density due to double mesa technology; SIPOS and Glass Passivation.

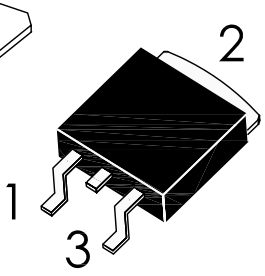
BTA06/BTB06 series triacs is suitable for general purpose AC switching. They can be used as an ON/OFF Function in applications such as static relays, heating regulation, induction motor stating circuits...or for phase control operation light dimmers, motorspeed controllers.

BTA06/BTB06-×××TW、-×××SW、-×××CW、-×××BW are 3 Quadrants triacs, They are specially recommended for use on inductive loads.

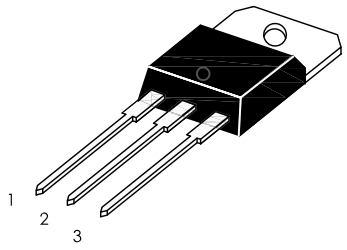
BTA06 are isolated internally, they provides a 2500V RMS isolation voltage from all three terminals to extemal heetsink.

TO-220AB(BTB06)

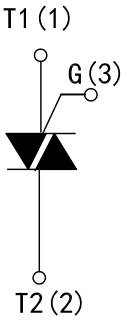




DPAK



TO-220AB insulated
(BTA06)



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	6	A
V_{DRM}/V_{RRM}	600and800	V
$I_{G(Q1)}$	5 to 50	mA

ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range Operrating junction temperature range		Tstg Tj	-40 to +150 -40 to +125	˚C
Repetitive Peak Off-state Voltage Repetitive Peak Reverse Voltage	Tj=25˚C	V_{DRM} V_{RRM}	600and800 600and800	V
Non repetitive Surge Peak Off-state Voltage Non repetitive Peak Reverse Voltage	Tj=25˚C	V_{DSM} V_{RSM}	700and900 700and900	V
RMS on-state current (full sine wave)	DPAK / TO-220AB Tc=110˚C TO-220AB Ins Tc=105˚C	$I_{T(RMS)}$	6	A
Non repetitive surge peak on-state current (full cycle, Tj=25˚C)	f = 50 Hz t=20ms f = 60 Hz t=16.7ms	I_{TSM}	60 63	A
I^2t Value for fusing	tp=10ms	I^2t	21	A^2s
Critical rate of rise of on-state current $I_G=2\times I_{GT}$, tr≤100 ns, f=120Hz, Tj=125˚C		dI /dt	50	A/us
Peak gate current	tp=20us, Tj=125˚C	I_{GM}	4	A
Average gate power dissipation	Tj=125˚C	$P_{G(AV)}$	1	W

ELECTRICAL CHARACTERISTICS(Tj=25℃ unless otherwise specified)

3 Quadrants

Symbol	Test Condition	Quadrant		BTA06/BTB06				Unit
				TW	SW	CW	BW	
I_{GT}	V_D=12V R_L=30Ω	I - II - III	MAX.	5	10	35	50	mA
V_{GT}		I - II - III	MAX.	1.3				V
V_{GD}	$V_D=V_{DRM}$ R_L=3.3KΩ Tj=125℃	I - II - III	MIN..	0.2				V
I_L	I_G=1.2I_{GT}	I - III	MAX.	10	25	50	70	mA
		II		15	30	60	80	
I_H	I_T=100mA		MAX.	10	15	35	50	mA
dV/dt	V_D=67%V_{DRM} gate open Tj=125℃		MIN.	20	40	400	1000	V/μs
(dI/dt)c	(dV/dt) c=0.1V/μs Tj=125℃		MIN.	3.5	6.5	----	----	A/ms
	(dV/dt) c=10V/μs Tj=125℃			1.0	2.9	----	----	
	Without snubber Tj=125℃			----	----	3.5	5.3	

4 Quadrants

Symbol	Test Condition	Quadrant		BTA06/BTB06		Unit
				C	B	
I_{GT}	V_D=12V R_L=30Ω	I - II - III IV	MAX.	25 50	50 100	mA
V_{GT}		ALL	MAX.	1.3		V
V_{GD}	$V_D=V_{DRM}$ R_L=3.3KΩ Tj=125℃	ALL	MIN.	0.2		V
I_L	I_G=1.2I_{GT}	I - III - IV	MAX.	40	50	mA
		II		80	100	
I_H	I_T=100mA		MAX.	25	50	mA
dV/dt	V_D=67%V_{DRM} gate open Tj=125℃		MIN.	200	400	V/μs
(dI/dt)c	(dV/dt) c=0.1V/μs Tj=125℃		MIN.	----	----	
	(dV/dt) c=10V/μs Tj=125℃			----	----	
	Without snubber Tj=125℃			----	----	

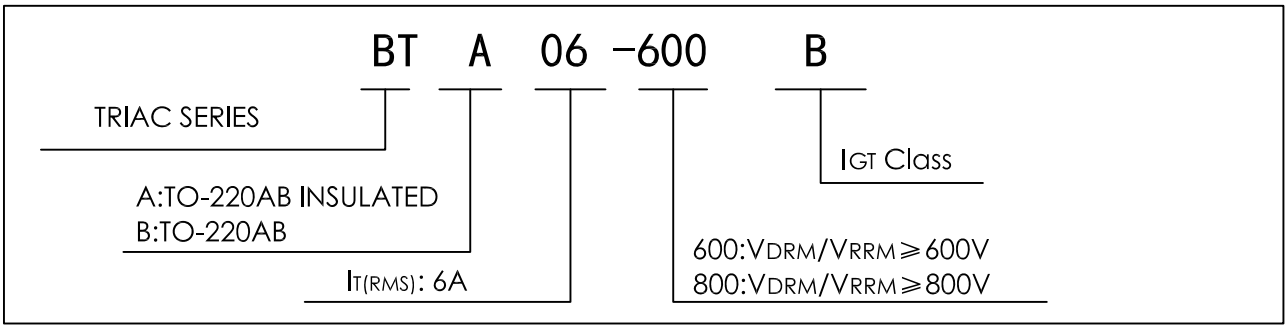
STATIC CHARACTERISTICS

Symbol	Test Conditions		Value (MAX.)	Unit
V_{TM}	I_{TM}=5.5A, tp=380uS	Tj=25℃	1.55	V
I_{DRM}	$V_D=V_{DRM}$	Tj=25℃	5	uA
I_{RRM}	$V_R=V_{RRM}$	Tj=125℃	1	mA

THERMAL RESISTANCES

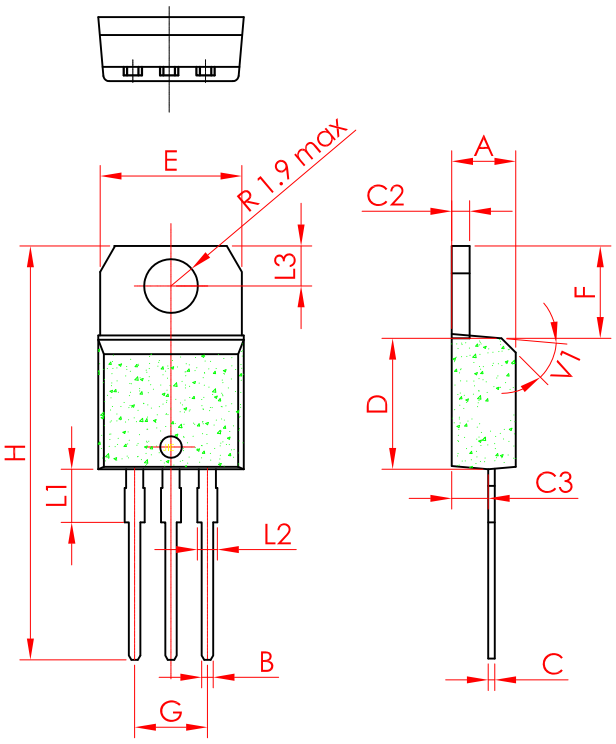
Symbol	Parameter		Value	Unit
$R_{th(j-c)}$	Junction to case (AC)	DPAK/TO-220AB	1.8	℃/W
		TO-220AB Insulated	2.7	

ORDERING INFORMATION



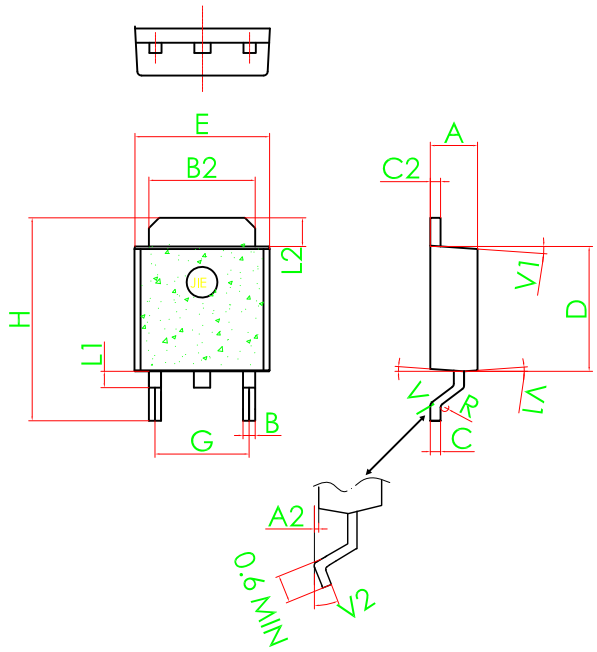
PACKAGE MECHANICAL DATA

TO-220AB



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.4		4.6	0.173		1.181
B	0.61		0.88	0.024		0.034
C	0.49		0.70	0.019		0.027
C2	1.23		1.32	0.048		0.051
C3	2.4		2.72	0.094		0.107
D	8.6		9.7	0.338		0.382
E	10		10.4	0.393		0.409
F	6.2		6.6	0.244		0.259
G	4.8		5.4	0.189		0.213
H	28.0		29.8	11.0		11.7
L1		3.75			0.147	
L2	1.14		1.7	0.044		0.066
L3	2.65		2.95	0.104		0.116
V1		40°			40°	

DPAK



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.095
A2	0.03		0.23	0.001		0.009
B	0.55		0.65	0.021		0.026
B2	5.2		5.4	0.204		0.212
C	0.45		0.62	0.017		0.024
C2	0.48		0.62	0.019		0.024
D	6		6.2	0.236		0.244
E	6.4		6.6	0.251		0.259
G	4.40		4.60	0.173		0.181
H	9.35		10.1	0.368		0.397
L1		0.8			0.031	
L2	1.37		1.5	0.054		0.059
V1		4°			4°	
V2	0°		8°	0°		8°

FIG.1: Maximum power dissipation versus RMS on-state current(full cycle)

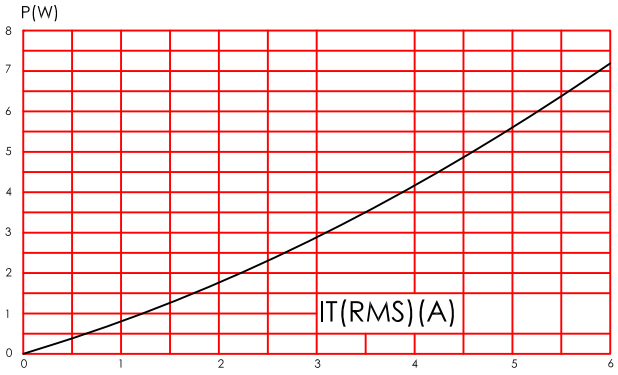


FIG.2: RMS on-state current versus case temperature(full cycle)

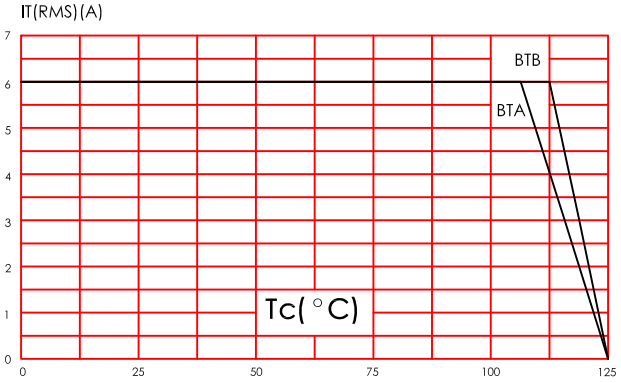


FIG.3: On-state characteristics (maximum values)

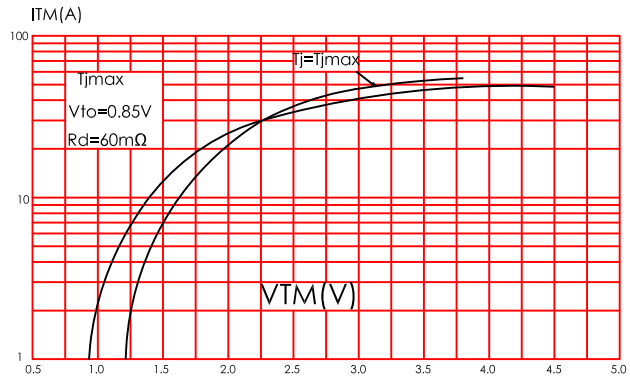


FIG.4: Surge peak on-state current versus number of cycles

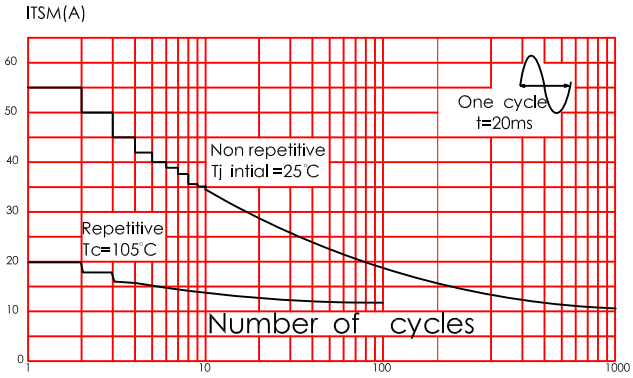


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10ms, and corresponding value of I^2t.

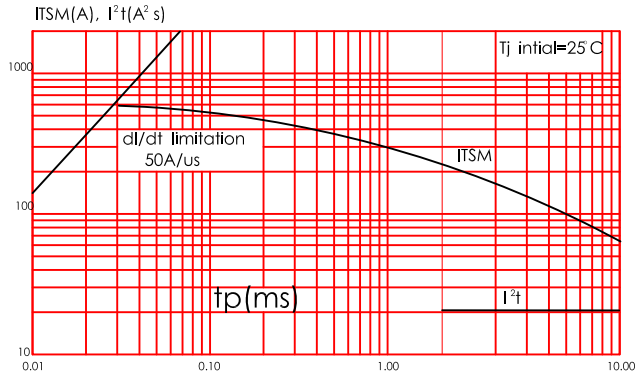


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature(typical values)