



# 30C01SP

## Low-Frequency General-Purpose Amplifier Applications

### Applications

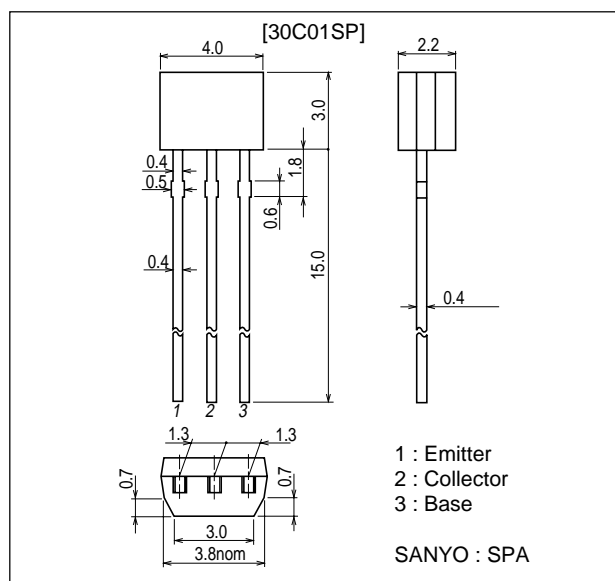
- Low-frequency Amplifier, muting circuit.

### Features

- Large current capacitance.
- Low collector-to-emitter saturation voltage (resistance).  
R<sub>CCE(sat)</sub> typ=0.70Ω [I<sub>C</sub>=0.4A, I<sub>B</sub>=20mA].
- Small ON-resistance (Ron).

### Package Dimensions

unit : mm  
2033A



### Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CB0</sub>		40	V
Collector-to-Emitter Voltage	V <sub>CEO</sub>		30	V
Emitter-to-Base Voltage	V <sub>EBO</sub>		5	V
Collector Current	I <sub>C</sub>		400	mA
Collector Current (Pulse)	I <sub>CP</sub>		800	mA
Collector Dissipation	PC		400	mW
Junction Temperature	T <sub>J</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I <sub>CB0</sub>	V <sub>CB</sub> =30V, I <sub>E</sub> =0			0.1	μA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =4V, I <sub>C</sub> =0			0.1	μA
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =10mA	300		800	
Gain-Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =50mA		380		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		2.4		pF

Marking : YQ

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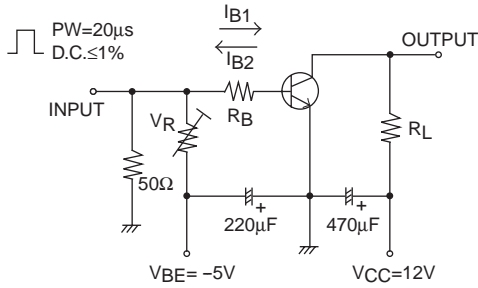
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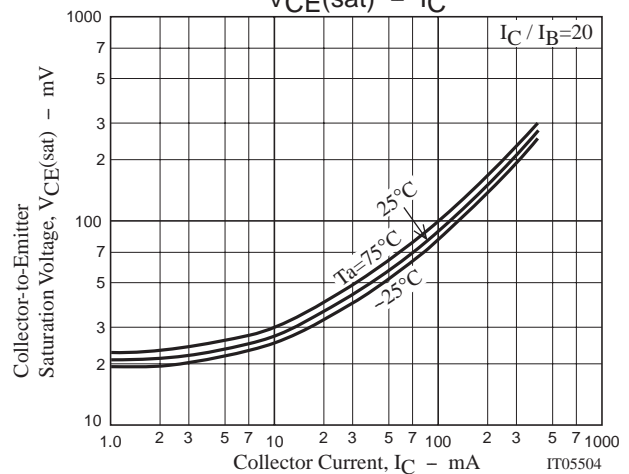
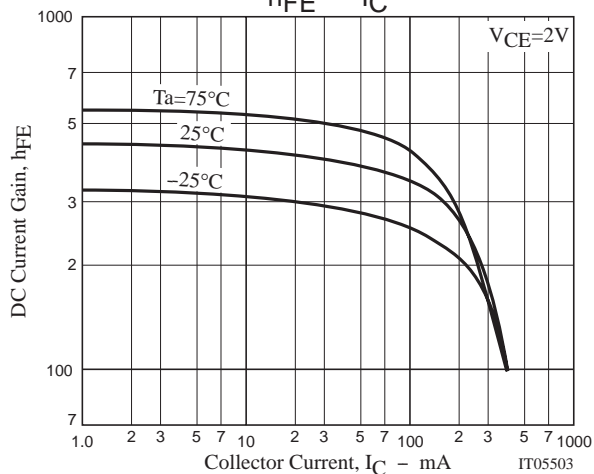
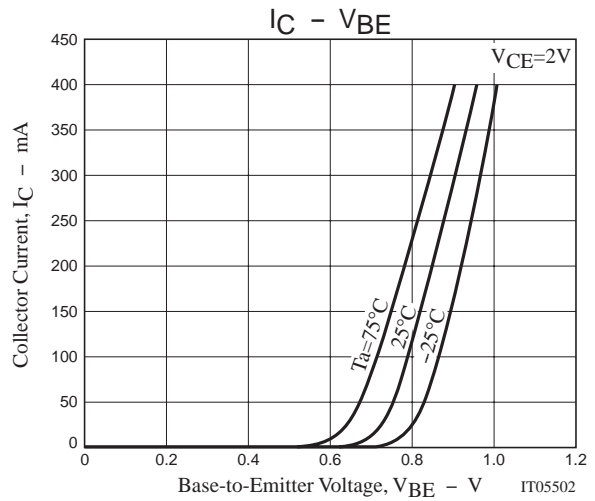
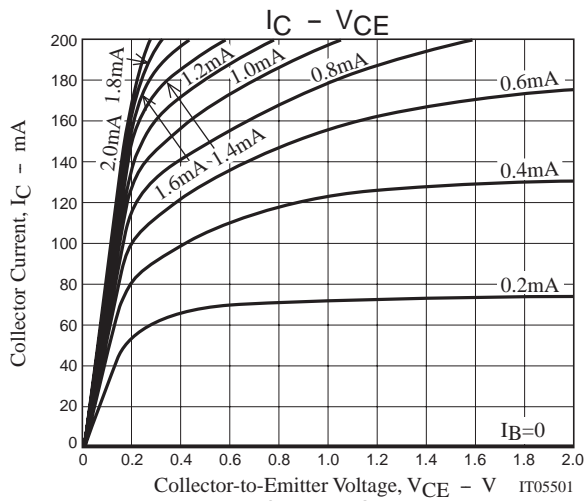
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=100mA, I_B=5mA$		100	200	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=100mA, I_B=5mA$		0.9	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	40			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	30			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	5			V
Turn-ON Time	$t_{on}$	See specified Test Circuit.		42		ns
Storage Time	$t_{stg}$	See specified Test Circuit.		135		ns
Fall Time	$t_f$	See specified Test Circuit.		90		ns

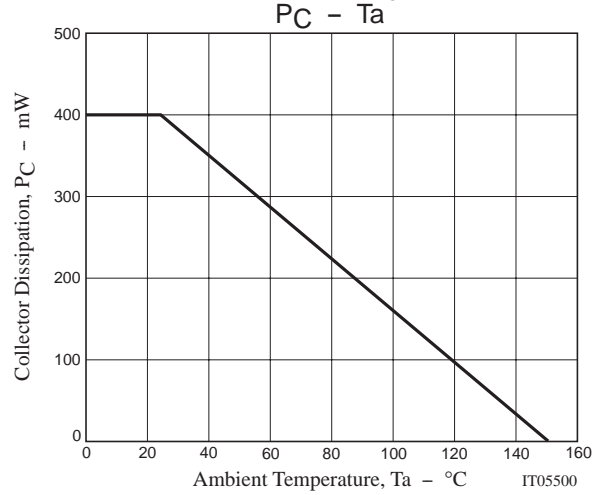
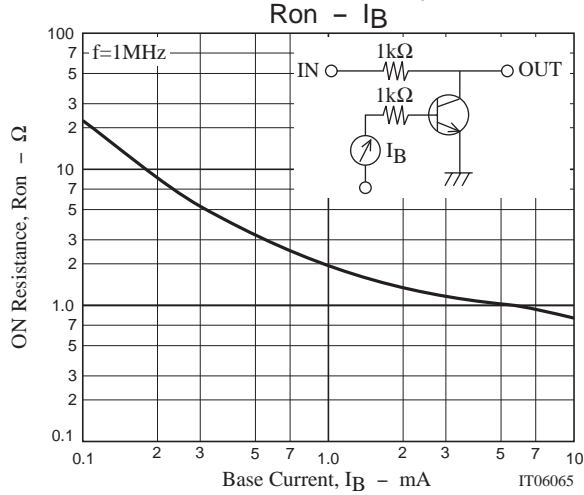
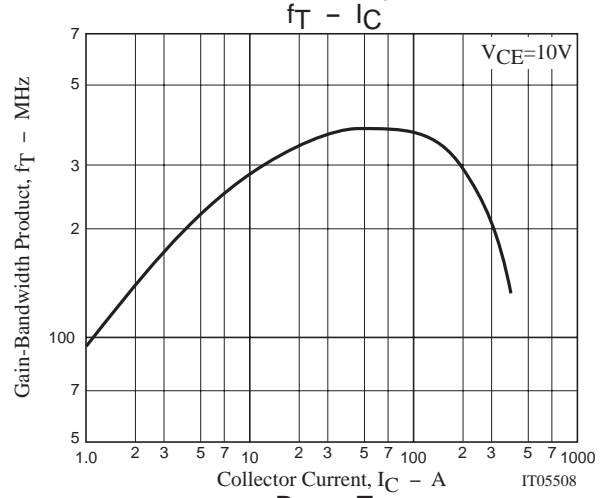
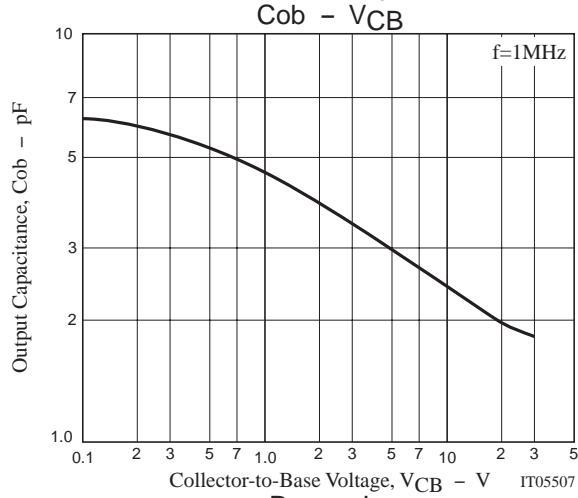
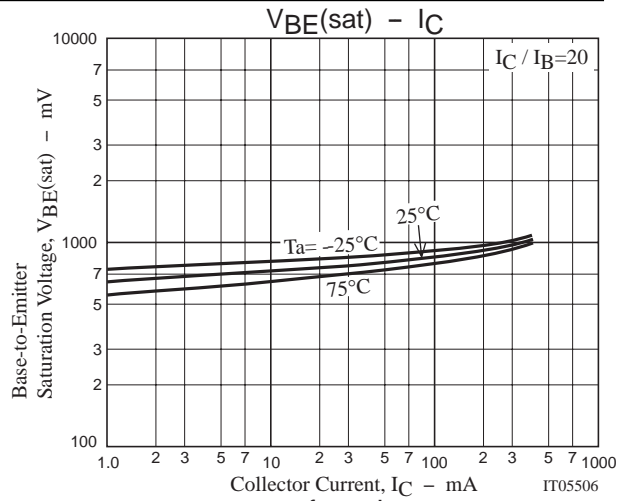
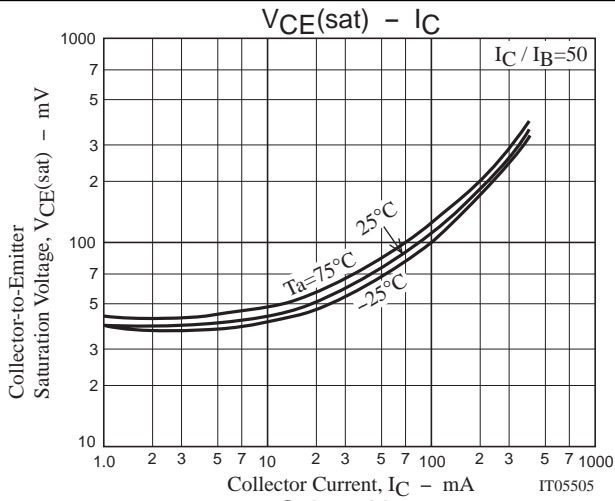
## Switching Time Test Circuit



$$I_C=20I_{B1}=-20I_{B2}=300mA$$



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