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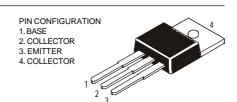
TO-220 Plastic Package

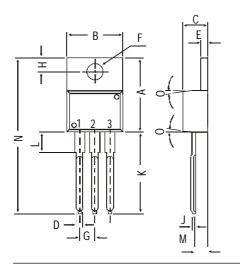
CSC1061

CSC1061 NPN PLASTIC POWER TRANSISTOR

Low frequency Power Amplifier

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	DIM	MIN.	MAX.
diminsions in mm.	Α	14.42	16.51
	В	9.63	10.67
	С	3.56	4.83
	D		0.90
	Е	1.15	1.40
	F	3.75	3.88
	G	2.29	2.79
	Н	2.54	3.43
	J		0.56
	K	12.70	14.73
	L	2.80	4.07
	М	2.03	2.92
	N		31.24
Ŧ	0	DEG 7	

ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	V_{CBO}	max.	50 V
Collector-emitter voltage (open base)	$V_{C\!E\!O}$	max.	50 V
Collector current	I_C	max.	3.0 A
Total power dissipation up to $T_C = 25^{\circ}C$	P_{tot}	max.	25 W
Junction temperature	T_i	max.	150 °C
Collector-emitter saturation voltage	J		
$I_C = 2A$; $I_B = 0.2A$	V_{CEsat}	max.	1.0 V
D.C. current gain			
$I_C = 1A$; $V_{CE} = 4V$	$h_{\!F\!E}$	min.	35
		max.	320

RATINGS (at T_A =25°C unless otherwise specified)

Limiting values			
Collector-base voltage (open emitter)	V_{CBO}	max.	50 V
Collector-emitter voltage (open base)	V_{CEO}	max.	50 V
Emitter-base voltage (open collector)	V_{EBO}	max.	4.0 V
Collector current	I_C	max.	3.0 A

Total power dissipation up to $T_C = 25^{\circ}C$ Junction temperature Storage temperature	P_{tot} T_{j} T_{stg}	max. max. –65 to	25 W 150 ℃ +150 ℃
CHARACTERISTICS			
$T_{amb} = 25$ °C unless otherwise specified			
Collector cutoff current			
$I_E = 0; \ V_{CB} = 20V$	I_{CBO}	max.	$100 \mu A$
Breakdown voltages			
$I_C = 50 \text{ mA}; I_B = 0$	V_{CEO}	min.	50 V
$I_C = 5 \text{ mA}; I_E = 0$	V_{CBO}	min.	50 V
$I_E = 5 \text{ mA}; I_C = 0$	V_{EBO}	min.	4.0 V
Saturation voltages			
$I_C = 2 A$; $I_B = 0.2 A$	V_{CEsat}	max.	1.0 V
Base emitter on voltage			
$I_C = 1 A; V_{CE} = 4 V$	$V_{BE(on)}$	max.	1.5 V
D.C. current gain	(-)		
$I_C = 0.1A$; $V_{CE} = 4V$	$h_{\!F\!E}$	min.	35
$I_C = 1A; V_{CE} = 4V^{**}$	$h_{\!F\!E}$	min.	35
	-	max.	320
Transition frequency			
$I_C = 0.5A; V_{CE} = 4V$	f_T	typ.	8 MHz
	=	<i>J</i> 1	

^{**}hfE classification: A: 35-70 B: 60-120 C: 100-200 D: 160-320

Customer Notes

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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