

GJ1086

1.5A Low Dropout Positive Adjustable or Fixed-Mode Regulator

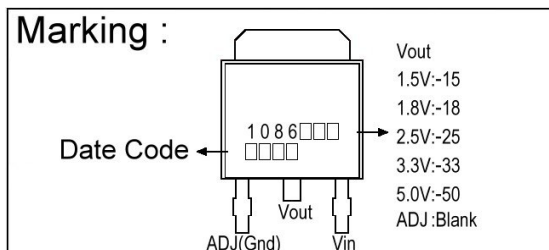
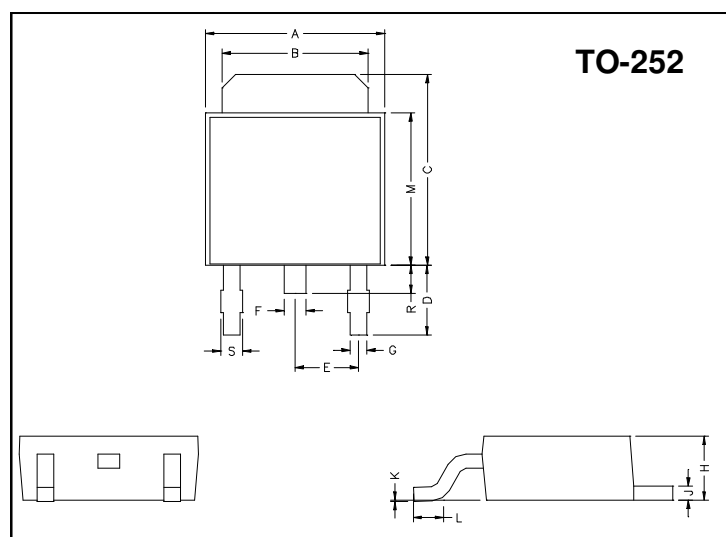
Description

The GJ1086 is a low dropout positive adjustable or fixed-mode regulator with minimum of 1.5A output current capability. The product is specifically designed to provide well-regulated supply for low voltage IC applications such as high-speed bus termination and low current 3.3V logic supply. GJ1086 is also well suited for other applications such as VGA cards. GJ1086 is guaranteed to have <1.4V dropout at full load current making it ideal to provide well-regulated outputs of 1.25 to 5.0V with 4.7 to 12V input supply by different output voltage.

Features

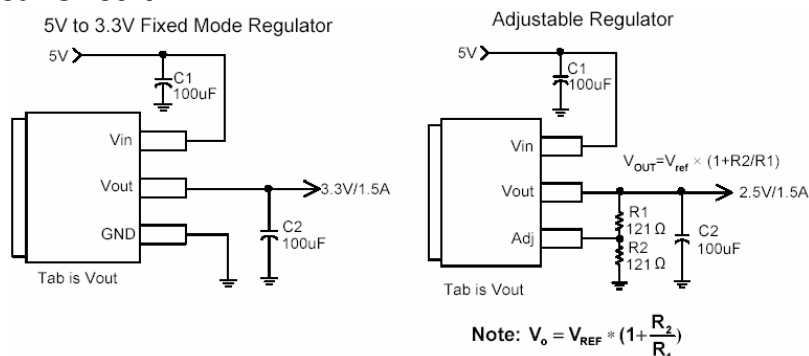
- 3-Terminal Adjustable or fixed 1.5V,1.8V,2.5V,3.3V,5.0V
- 1.4V Maximum Dropout at Full Load current
- Fast Transient response
- Built-in Thermal Shutdown
- Output current limiting

Package Dimensions

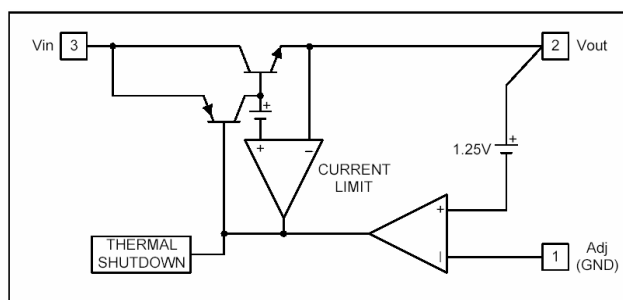


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.40	6.80	G	0.50	0.70
B	5.20	5.50	H	2.20	2.40
C	6.80	7.20	J	0.45	0.55
D	2.40	3.00	K	0	0.15
E	2.30 REF.		L	0.90	1.50
F	0.70	0.90	M	5.40	5.80
S	0.60	0.90	R	0.80	1.20

Typical Circuit



Block Diagram



Pin Descriptions

Name	I/O	Pin#	Function
Adj (GND)		1	Adjustable (Ground only for fixed mode)
Vout	O	2	The output of the regulator. A minimum of 10uF capacitor must be connected from this pin to ground to insure stability.
Vin	I	3	The input pin of regulator. Typically a large storage capacitor is connected from this pin to ground to insure that the input voltage does not sag below the minimum dropout voltage during the load transient response. This pin must always be 1.4V higher than Vout in order for the device to regulate properly.

Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
V _{in}	DC Supply Voltage	-0.3 to 12	V
P _D	Power Dissipation	Internally Limited	
T _{ST}	Storage Temperature	-65 ~ + 150	°C
T _{OP}	Operating Junction Temperature Range	0 ~ + 150	°C

Electrical Characteristics

Parameter	Conditions		MIN	TYP	MAX	UNIT
Reference Voltage	GJ1086-Adj	I _o =10mA, T _J =25°C, (V _{in} -V _{out})=1.5V	1.225	1.250	1.275	V
Output Voltage	GJ1086-1.5	I _o =10mA, T _J =25°C, 3V≤V _{in} ≤12V	1.470	1.500	1.530	V
	GJ1086-1.8	I _o =10mA, T _J =25°C, 3.3V≤V _{in} ≤12V	1.764	1.800	1.836	V
	GJ1086-2.5	I _o =10mA, T _J =25°C, 4V≤V _{in} ≤12V	2.450	2.500	2.550	V
	GJ1086-3.3	I _o =10mA, T _J =25°C, 4.8V≤V _{in} ≤12V	3.235	3.300	3.365	V
	GJ1086-5.0	I _o =10mA, T _J =25°C, 6.5V≤V _{in} ≤12V	4.900	5.000	5.100	V
Line Regulator	GJ1086-XXX	I _o =10mA, V _{out} +1.5<V _{in} <12	-	-	0.2	%
Load Regulation	GJ1086ADJ	V _{in} =3.3V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	-	1	%
	GJ1086-1.5	V _{in} =3.0V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	12	15	mV
	GJ1086-1.8	V _{in} =3.3V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	15	18	mV
	GJ1086-2.5	V _{in} =4.0V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	20	25	mV
	GJ1086-3.3	V _{in} =5.0V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	26	33	mV
	GJ1086-5.0	V _{in} =8.0V, 0mA<I _o <1.5A, T _J =25°C (Note 1,2)	-	40	50	mV
Dropout Voltage (V _{in} -V _{out})	GJ1086-XXX	I _o =1.5A(ΔV _{out} =1% V _{out})	-	1.3	1.4	V
Current Limit	GJ1086-XXX	V _{in} -V _{out} =5V	1.6	-	-	A
Minimum Load Current	Adjustable model	V _{in} =5V	-	5	10	mA
Adjust Pin Current	Adjustable model	V _{in} =12V, I _o =10mA	-	55	100	uA
Quiescent Current	fixed model	V _{in} =12V, I _o =0mA	-	-	12	mA
Thermal Regulation	T _A =25°C, 30ms pulse		-	0.008	0.04	%/W
Ripple Rejection	F=120Hz, C _{out} =25uF, Tantalum, I _o =1.5A					
	GJ1086-XXX	V _{in} =V _{out} +3V	-	60	70	dB
Temperature Stability	I _o =10mA		-	0.5	-	%
Thermal Resistance Junction-to-Ambient (No heat sink ;No air flow)			-	101	-	°C/W
Thermal Resistance Junction-to-Case	Control Circuitry/Power Transistor		-	15	-	°C/W

Note 1: See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are measured at a constant junction Temperature by low duty cycle pulse testing. Load regulation is measured at the output lead =1/18" from the package.

Note 2: Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the difference in input and output and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

Functional Description

Introduction

The GJ1086 adjustable or fixed-mode Low Dropout (LDO) regulator is a 3 terminal device which can easily be programmed by internal mask change to any voltage within the range of 1.25 to $V_{in}-1.4V$. The GJ1086 only needs 1.4V differential between V_{in} and V_{out} to maintain output regulation, in addition, the output voltage tolerances are also extremely tight and they include the transient response as part of the specification. For example, Intel VRE specification calls for a total of $\pm 100mV$ including initial tolerance, load regulation and 0 to 1.5A load step. The GJ1086 is specifically designed to meet the fast current transient needs as well as providing an accurate initial voltage, reducing the overall system cost with the need for fewer output capacitors.

Load Regulation

Since the GJ1086 is only a 3 terminal device, it is not possible to provide true remote sensing of the output voltage at the load. But it can supply good load regulation by internal feedback bypass the external loss such as adjustable mode.

Stability

The GJ1086 requires the use of an output capacitor as part of the frequency compensation in order to make the regulator stable. For most applications a minimum of 10uF aluminum electrolytic capacitor insures both stability and good transient response.

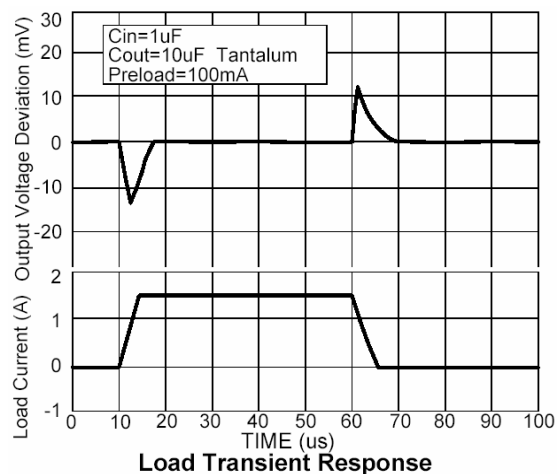
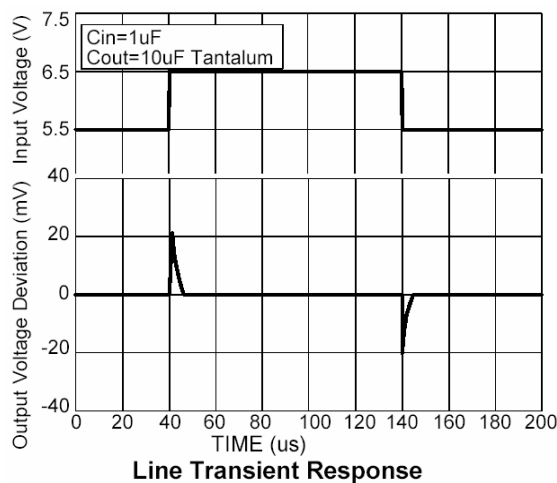
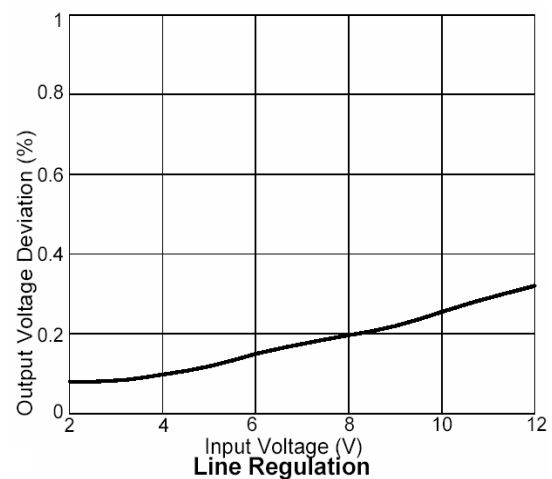
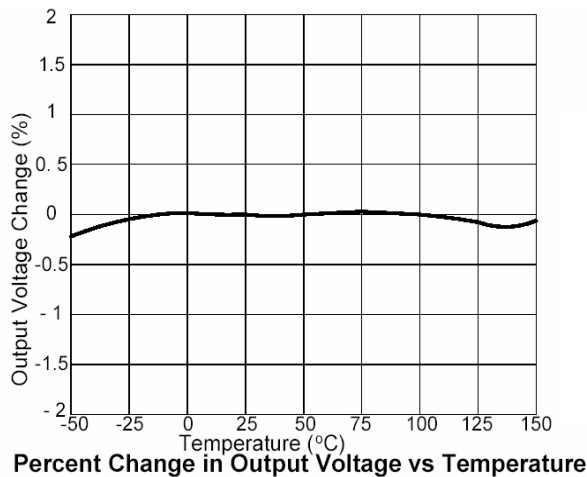
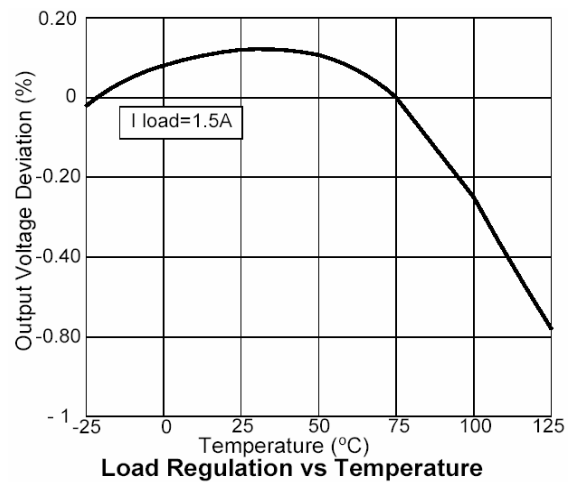
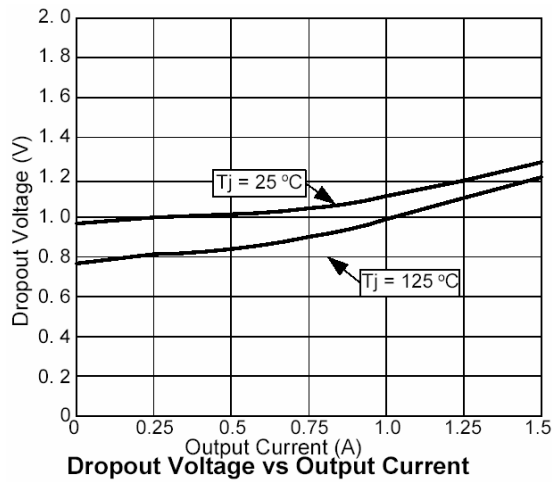
Thermal Design

The GJ1086 incorporates an internal shutdown that protects the device when the junction temperature exceeds the maximum allowable junction temperatures. Although this device can operate with junction temperatures in the range of 150°C, it is recommended that the selected heat sink be chosen such that during maximum continuous load operation the junction temperature is kept below the temperature.

Layout Consideration

The output capacitors must be located as close to the V_{out} terminal of the device as possible. It is recommended to use a section of a layer of the PC board as a plane to connect the V_{out} pin to the output capacitors to prevent any high frequency oscillation that may result due to excessive trace inductance.

Typical Performance Characteristics



Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of GTM.
- GTM reserves the right to make changes to its products without notice.
- GTM semiconductor products are not warranted to be suitable for use in life-support Applications, or systems.
- GTM assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.

Head Office And Factory:

- Taiwan:** No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
- TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- China:** (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
- TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165