TOSHIBA HIGH EFFICIENCY DIODE STACK (HED) SILICON EPITAXIAL TYPE

# 20JL2C41

# SWITCHING MODE POWER SUPPLY APPLICATIONS **CONVERTER & CHOPPER APPLICATION**

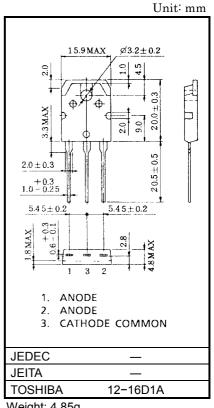
• Repetitive Peak Reverse Voltage  $: V_{RRM} = 600V$ • Average Output Rectified Current  $: I_O = 20A$ 

• Ultra Fast Reverse-Recovery Time : trr = 50ns (Max)

• Low Switching Losses and Output Noise.

# MAXIMUM RATINGS (Ta=25°C)

| CHARACTERISTIC                   | SYMBOL           | RATING     | UNIT |  |
|----------------------------------|------------------|------------|------|--|
| Repetitive Peak Reverse Voltage  | $V_{RRM}$        | 600        | V    |  |
| Average Output Rectified Current | Io               | 20         | Α    |  |
| Peak One Cycle Surge Forward     | leou             | 100 (50Hz) | Α    |  |
| Current (Sine Wave)              | IFSM             | 110 (60Hz) |      |  |
| Junction Temparature             | Tj               | -40~150    | °C   |  |
| Storage Temparature Range        | T <sub>stg</sub> | -40~150    | °C   |  |
| Screw Torque                     | _                | 0.8        | N·m  |  |



Weight: 4.85g

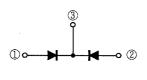
## **ELECTRICAL CHARACTERISTICS (Ta=25°C)**

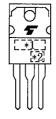
| CHARACTERIST              | IC                 | SYMBOL                | TEST CONDITION                            | TYP. | MAX | UNIT   |
|---------------------------|--------------------|-----------------------|---|------|-----|--------|
| Peak Forward Voltage      | (Note 1)           | $V_{FM}$              | I <sub>FM</sub> = 10A                     | _    | 2.0 | V      |
| Repetitive Peak Reverse C | urrent<br>(Note 1) | I <sub>RRM</sub>      | V <sub>RRM</sub> = 600V                   | _    | 50  | μA     |
| Reverse Recovery time     | (Note 1)           | t <sub>rr</sub>       | I <sub>F</sub> = 2A, di / dt = - 50A / μs | _    | 50  | ns     |
| Forward Recovery time     | (Note 1)           | t <sub>fr</sub>       | I <sub>F</sub> = 1A                       | _    | 150 | ns     |
| Thermal Resistance        |                    | R <sub>th (j-c)</sub> | DC Total, Junction to Case                | _    | 1.5 | °C / W |

Note 1: A value of one cell.

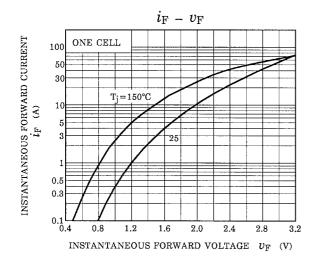
### **POLARITY**

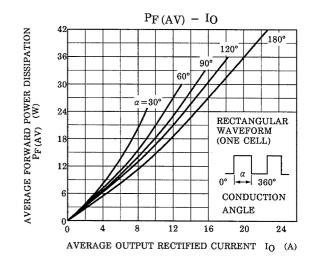
#### **MARKING**

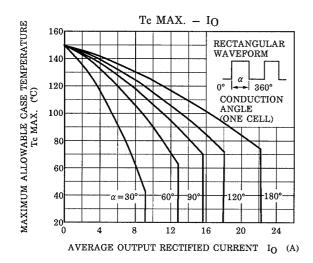


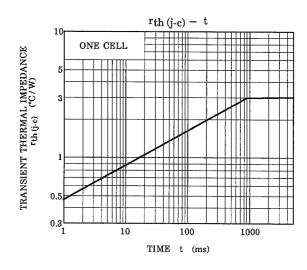


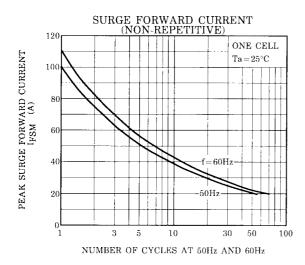
| * 1 | MARK       | 20JL2C                               |
|-----|------------|--------------------------------------|
| * 2 | Lot Number | om Alphabet A)<br>the Christian Era) |

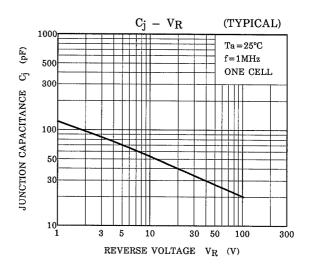












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