



Parameter	Rating	Units
Blocking Voltage	400	V_p
Load Current	500	mA_{rms}
On State Voltage Drop	1.4	V_{rms} (at $I_L = 500mA_{rms}$)

Features

- Load Current up to $0.5A_{rms}$
- $400V_p$ Blocking Voltage
- 5mA Sensitivity
- Zero-Crossing Detection
- DC Control, AC Output
- Optically Isolated
- TTL and CMOS Compatible
- Low EMI and RFI Generation
- High Noise Immunity
- Machine Insertable, Wave Solderable
- Flammability classification rating of V-0

Applications

- Programmable Control
- Process Control
- Power Control Panels
- Remote Switching
- Gas Pump Electronics
- Contactors
- Large Relays
- Solenoids
- Motors
- Heaters

Description

The CPC1943 is an AC Solid State Switch using optical coupling with dual power SCR outputs to produce an alternative to optocoupler and Triac circuits. The CPC1943 switches are robust enough to provide a blocking voltage of up to $400V_p$. In addition, tightly controlled zero cross circuitry ensures switching of AC loads without the generation of transients. The input and output circuits are optically coupled to provide $3750V_{rms}$ of isolation and noise immunity between control and load circuits. As a result the CPC1943 is well suited for industrial environments where electromagnetic interference could disrupt the operation of electromechanical relays.

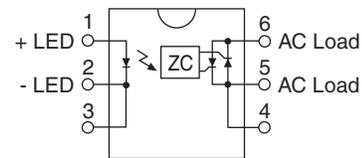
Approvals

- UL Recognized Component: File # E69938
- CSA Certified Component: Certificate # 1172007

Ordering Information

Part #	Description
CPC1943G	6 Pin Dip (50/Tube)
CPC1943GS	6 Pin Surface Mount (50/Tube)
CPC1943GSTR	6 Pin Surface Mount (1000/Reel)

Pin Configuration



Absolute Maximum Ratings (@ 25° C)

Parameter	Ratings	Units
Blocking Voltage	400	V _P
Reverse Input Voltage	5	V
Input Control Current	100	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
P _{MAX} , Total Package Dissipation ²	800	mW
Isolation Voltage Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate Linearly 1.33 mW/°C

² Derate Linearly 6.67 mW/°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics

Parameters	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Operating Voltage Range	V _L	-	20	-	120	V _{rms}
Load Current, Continuous	V _L =120-240V _{rms}	I _L	0.005	-	0.5	A _{rms}
Off State Leakage Current	V _L =400V _P	I _{LEAK}	-	-	1	mA
On-State Voltage Drop	I _L =500mA _{rms}	V _{ON}	-	-	1.4	V _{rms}
Critical Rate of Rise ³	-	dV/dt	1000	-	-	V/μs
Switching Speeds						
Turn-on	I _F =5 mA	t _{ON}	-	-	0.5	cycles
Turn-off		t _{OFF}	-	-	0.5	
Zero-Cross Turn-On Voltage	1st half cycle	-	-	2	10	V
	Subsequent half cycle	-	-	1	-	V
Operating Frequency ¹	-		20	-	500	Hz
Load Power Factor for Guaranteed Turn-On ²	-	PF	0.25	-	-	-
Input Characteristics @ 25°C						
Input Control Current ⁴	-	I _F	-	0.8	5	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Input Drop-out Voltage	-		0.8	-	-	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics @ 25°C						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

¹ Zero Cross 1st half cycle @ <100Hz

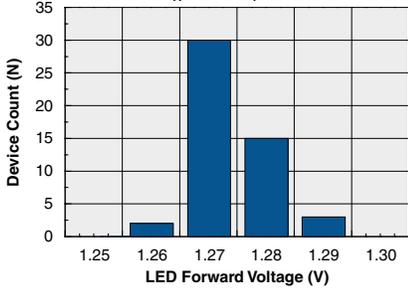
² Snubber circuits may be required at low power factors.

³ Tested in accordance with EIA/NARM standard RS-443.

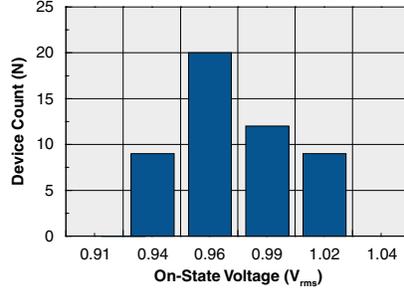
⁴ For high noise environments, use I_F=10mA.

PERFORMANCE DATA*

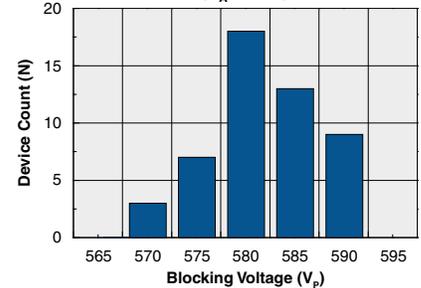
Typical LED Forward Voltage Drop
($T_A=25^\circ\text{C}$, $I_F=5\text{mA}$)



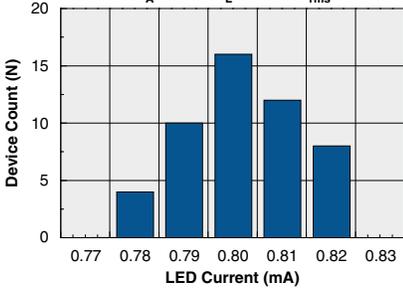
Typical On-State Voltage Distribution
($T_A=25^\circ\text{C}$, $I_L=500\text{mA}_{\text{rms}}$)



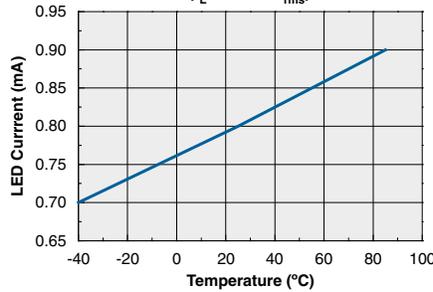
Typical Blocking Voltage Distribution
($T_A=25^\circ\text{C}$)



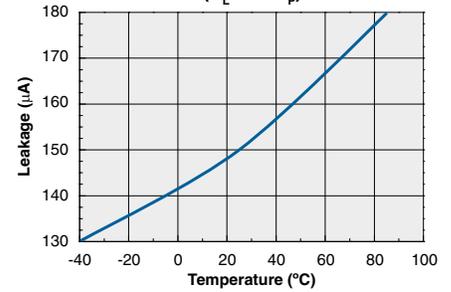
Typical I_F for Switch Operation
($T_A=25^\circ\text{C}$, $I_L=500\text{mA}_{\text{rms}}$)



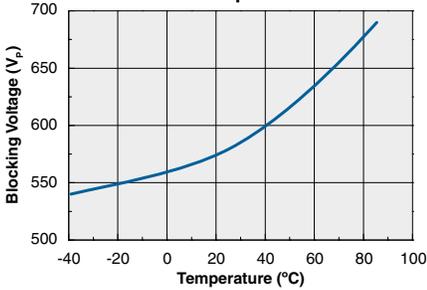
Typical I_F for Switch Operation vs. Temperature
($I_L=250\text{mA}_{\text{rms}}$)



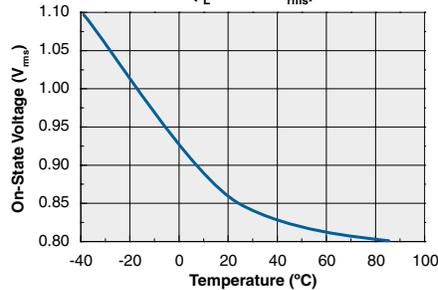
Typical Leakage Current vs. Temperature
($V_L=400\text{V}_p$)



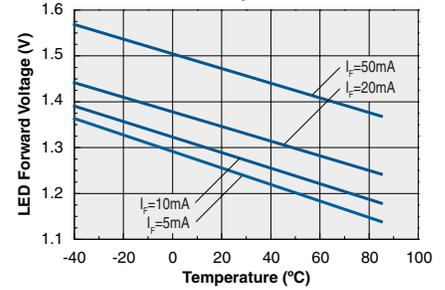
Typical Blocking Voltage vs. Temperature



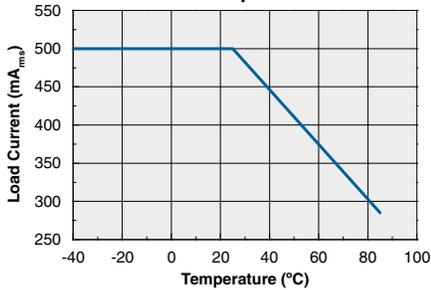
Typical On-State Voltage vs. Temperature
($I_L=250\text{mA}_{\text{rms}}$)



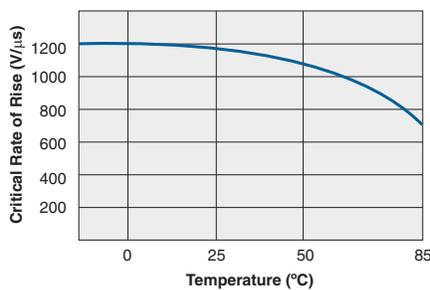
Typical LED Forward Voltage Drop vs. Temperature



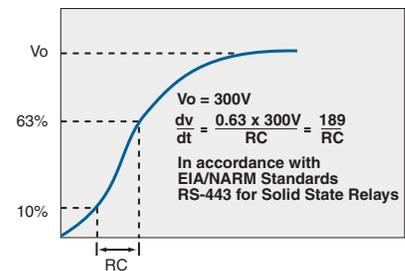
Load Current, Free Air vs. Temperature



Typical dV/dt vs. Temperature



dV/dt Method



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

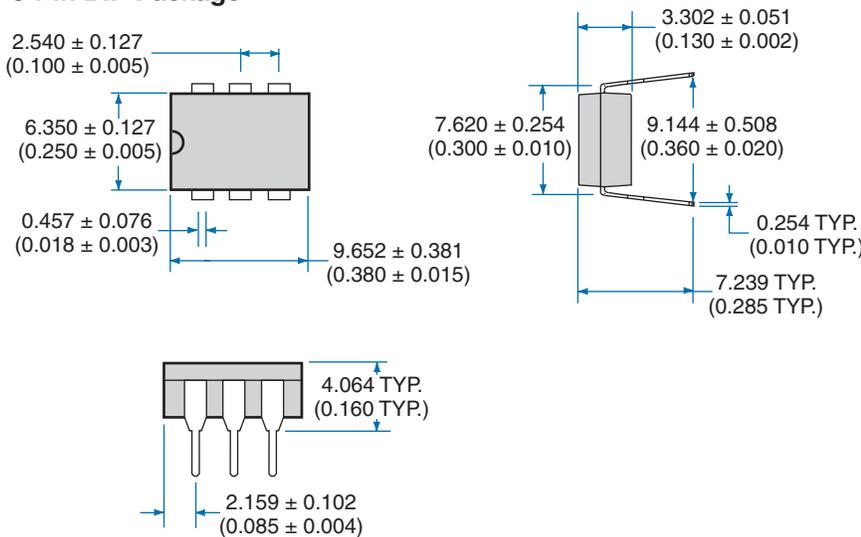
Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

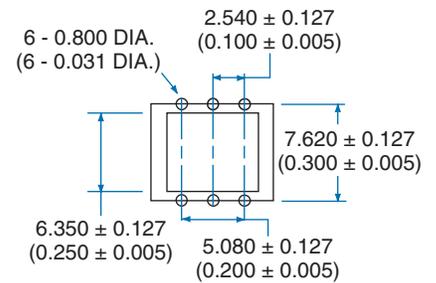


MECHANICAL DIMENSIONS

6-Pin DIP Package

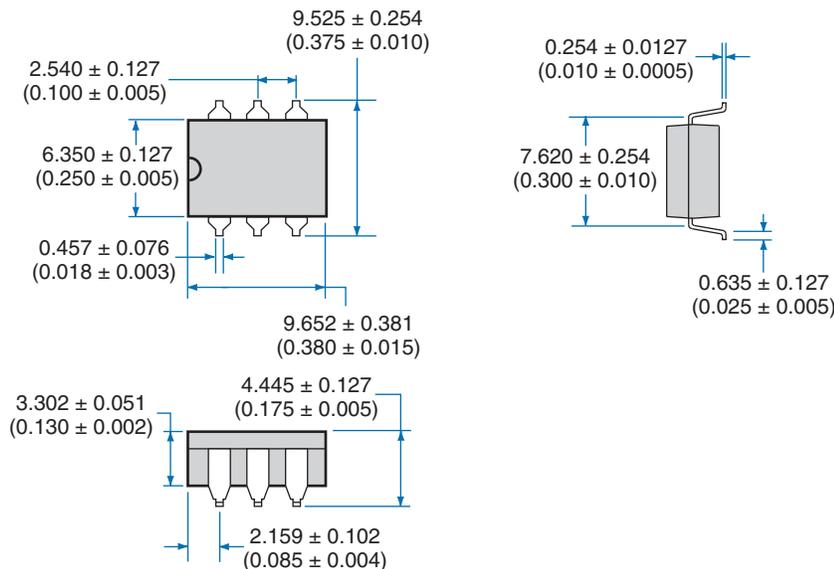


PC Board Pattern (Top View)

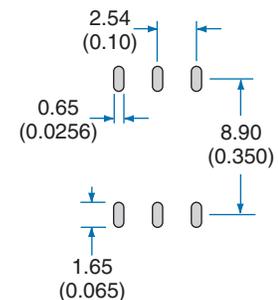


Dimensions
mm
(inches)

6-Pin Surface Mount Package

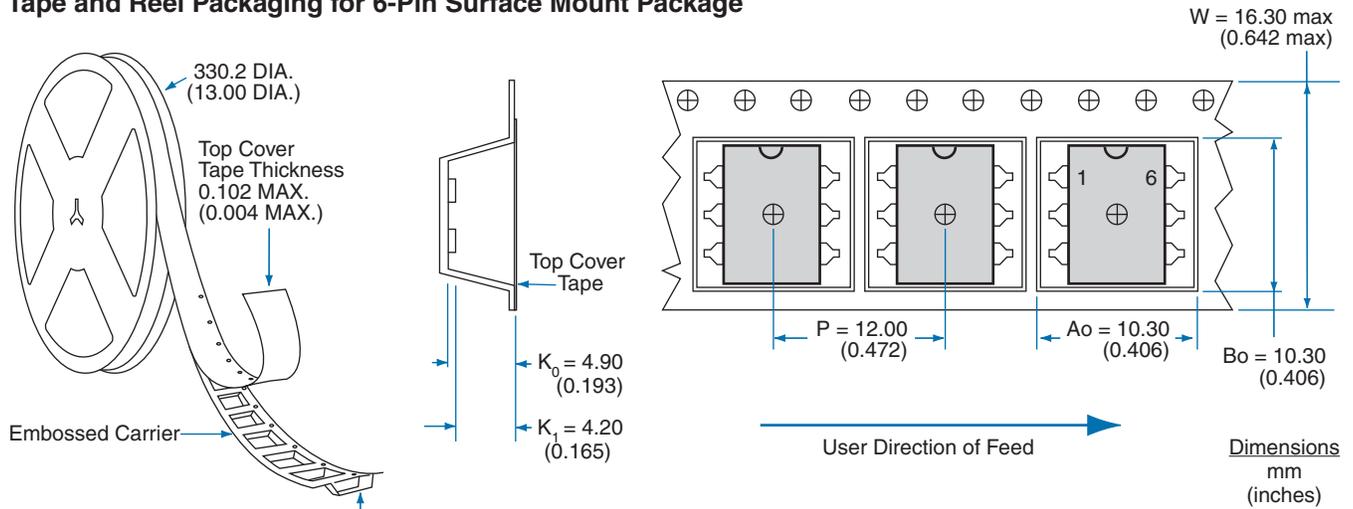


Recommended PCB Land Pattern



Dimensions
mm
(inches)

Tape and Reel Packaging for 6-Pin Surface Mount Package



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

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