RJL6032DPP-M0

Silicon N Channel MOS FET
High Speed Power Switching

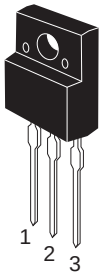
R07DS0250EJ0100
Rev.1.00
Jan 27, 2010

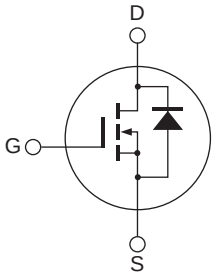
Features

- Low on-state resistance
  $R_{DS(on)}$ = 3.3 Ω typ. (at I_D = 1 A, V_{GS} = 10 V, Ta = 25°C)
- High speed switching
- Built in fast recovery diode

Outline

RENESAS Package code: PRSS0003AF-A
(Package name: TO-220FL)





1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	600	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	2	A
Drain peak current	$I_{D\ (pulse)}$ Note1	8	A
Avalanche current	I_{AP} Note3	2	A
Channel dissipation	Pch Note 2	30.6	W
Channel to case thermal Impedance	θch-c	4.08	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. Pulse width limited by safe operating area.
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

Electrical Characteristics

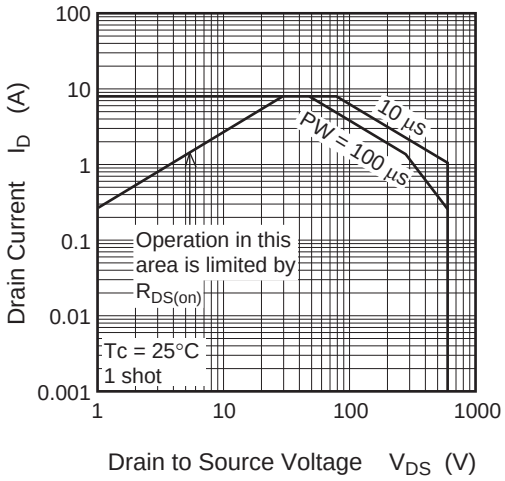
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	V_{DS} = 600 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	μA	V_{GS} = ±30 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS (off)}$	2	—	4	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS (on)}$	—	3.3	3.7	Ω	I_D = 1 A, V_{GS} = 10 V Note 4
Input capacitance	Ciss	—	265	—	pF	V_{DS} = 25 V V_{GS} = 0 f = 1 MHz
Output capacitance	Coss	—	35	—	pF	
Reverse transfer capacitance	Crss	—	4.5	—	pF	
Turn-on delay time	$t_{d (on)}$	—	12	—	ns	V_{DD} = 300 V I_D = 1 A V_{GS} = 10 V Rg = 10 Ω
Rise time	t_r	—	13	—	ns	
Turn-off delay time	$t_{d (off)}$	—	30	—	ns	
Fall time	t_f	—	40	—	ns	
Total gage charge	Qg	—	10.6	—	nC	V_{DD} = 480 V V_{GS} = 10 V I_D = 2 A
Gate to source charge	Qgs	—	1.2	—	nC	
Gate to drain charge	Qgd	—	5.6	—	nC	
Body-drain diode forward voltage	V_{DF}	—	—	1.5	V	I_F = 2 A, V_{GS} = 0 Note 4
Body-drain diode reverse recovery time	t_{rr}	—	—	160	ns	I_F = 2 A, V_{GS} = 0 V_{DD} = 300 V di_F/dt = 100 A/μs

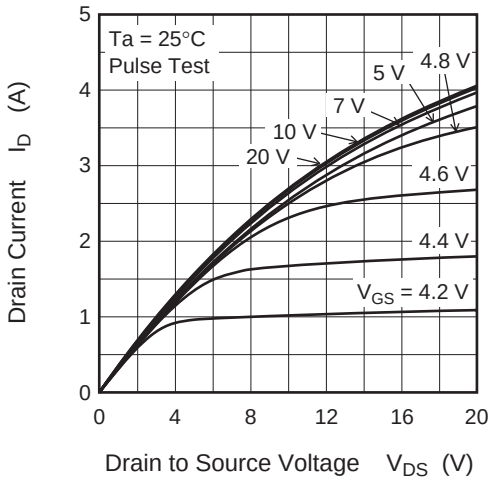
Note: 4. Pulse test

Main Characteristics

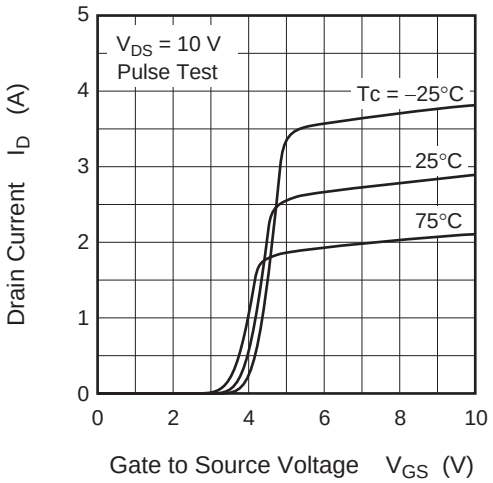
Maximum Safe Operation Area



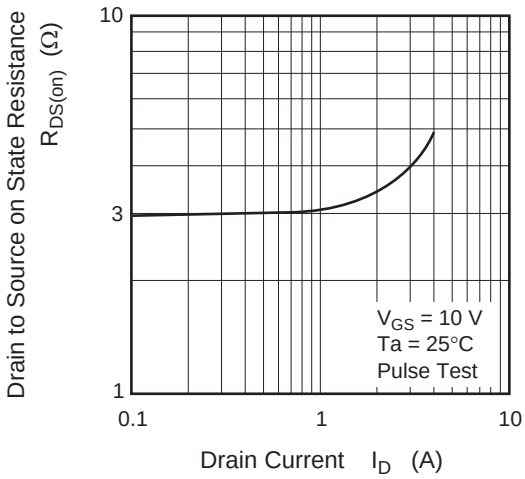
Typical Output Characteristics



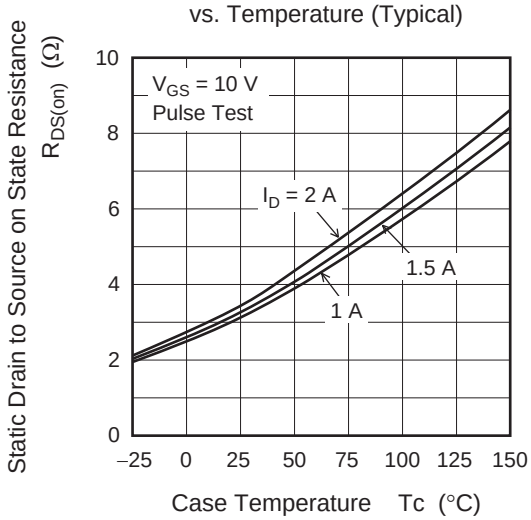
Typical Transfer Characteristics



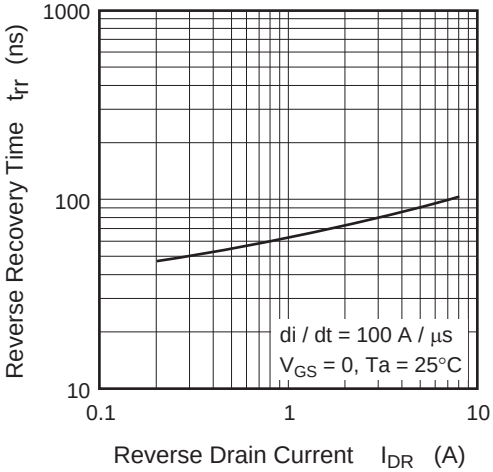
Static Drain to Source on State Resistance vs. Drain Current (Typical)



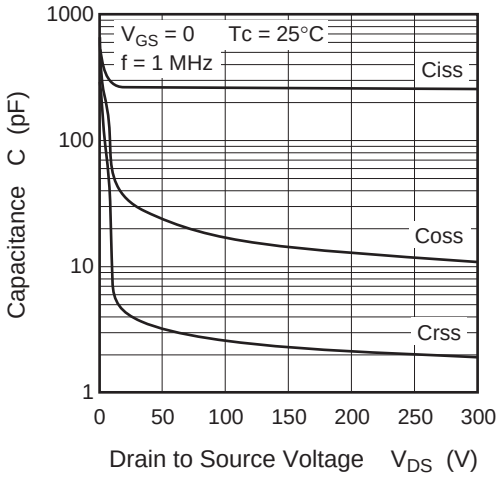
Static Drain to Source on State Resistance vs. Temperature (Typical)



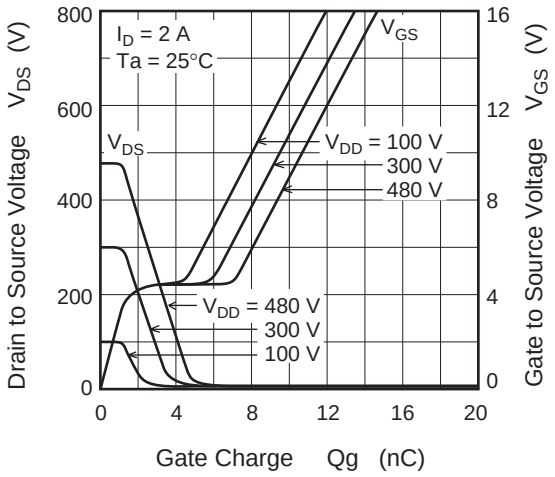
Body-Drain Diode Reverse Recovery Time (Typical)



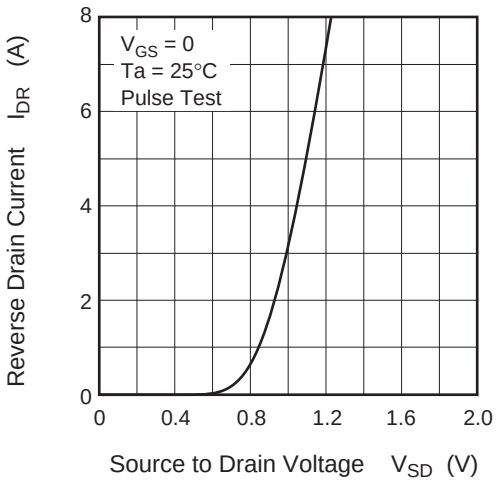
Typical Capacitance vs.
Drain to Source Voltage



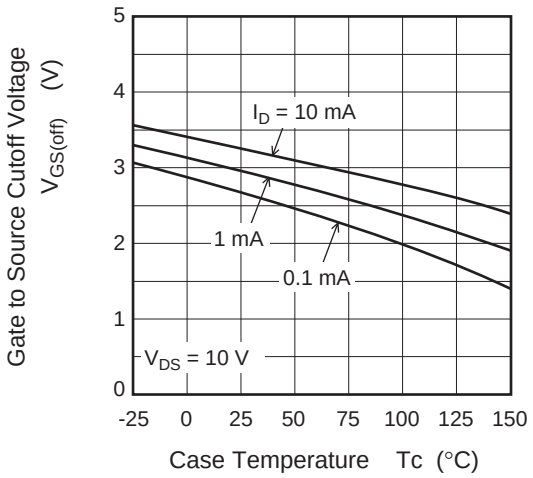
Dynamic Input Characteristics (Typical)



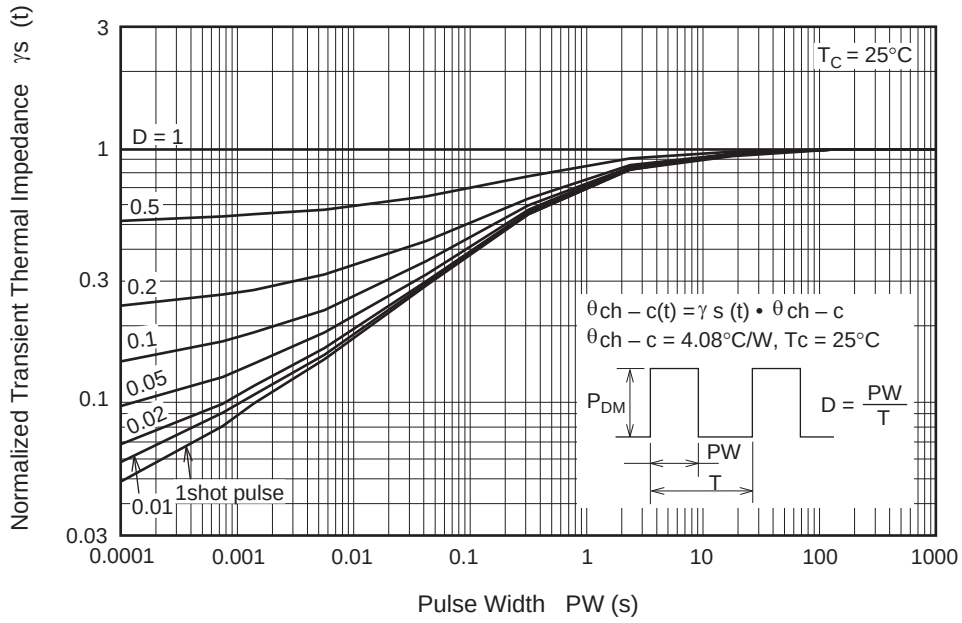
Reverse Drain Current vs.
Source to Drain Voltage (Typical)



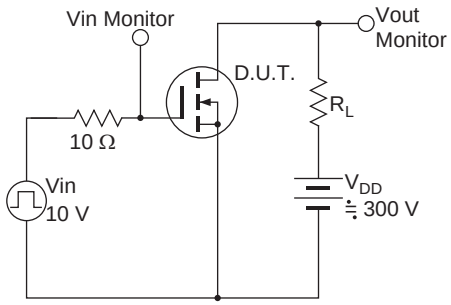
Gate to Source Cutoff Voltage
vs. Case Temperature (Typical)



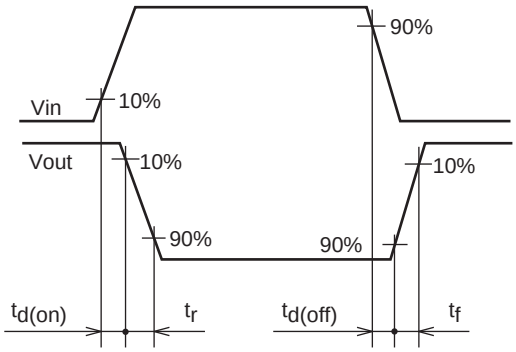
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



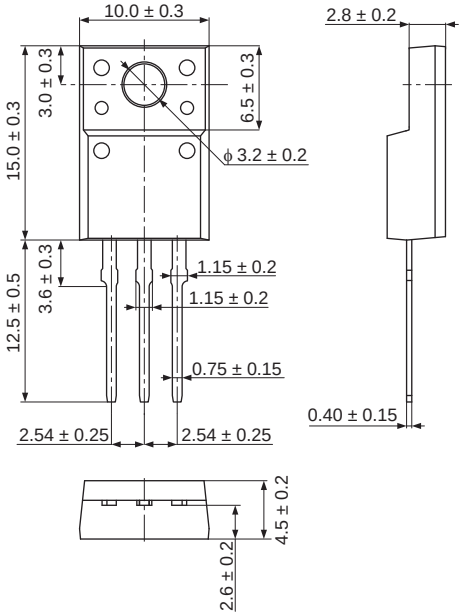
Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-220FL	—	PRSS0003AF-A	TO-220FL	1.5g

Unit: mm



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJL6032DPP-M0-T2	1050 pcs	Box (Tube)

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