

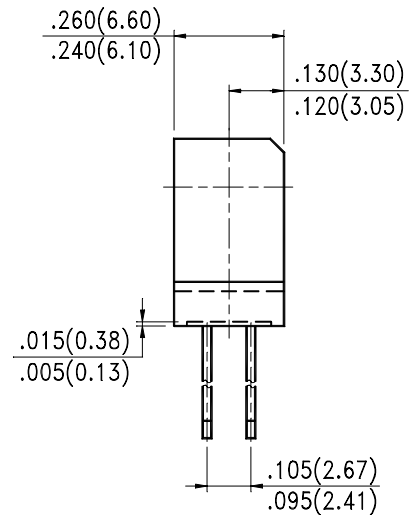
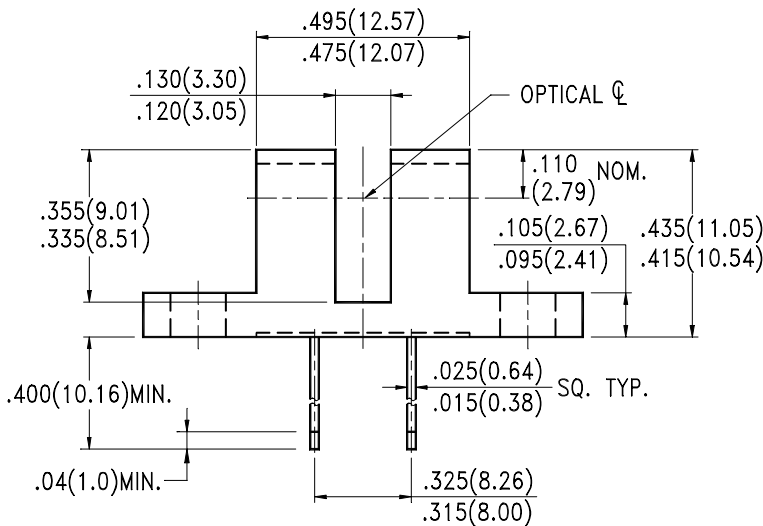
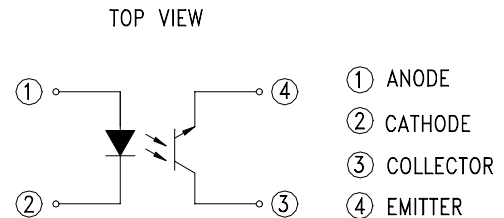
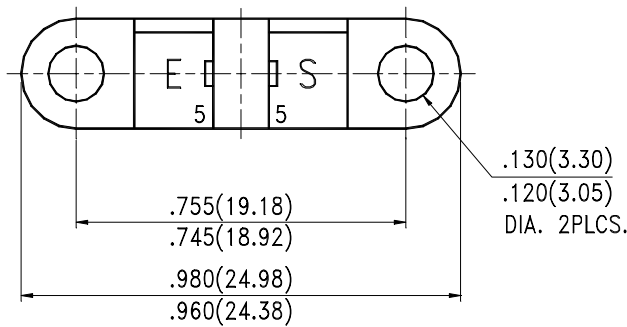
FEATURES

- * NON-CONTACT SWITCHING.
- * FAST SWITCHING SPEED.

APPLICATION

- * SCANNER, PRINTER.

PACKAGE DIMENSIONS



NOTES:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25\text{mm}(.010\text{'})$ unless otherwise noted.



ABSOLUTE MAXIMUM RATINGS AT T_A=25

PARAMETER	SYMBOL	MAXIMUM RATING	UNIT
INPUT LED			
Power Dissipation	P _D	75	mW
Continuous Forward Current	I _F	50	mA
Reverse Voltage	V _R	5	V
OUTPUT PHOTOTRANSISTOR			
Power Dissipation	P _C	100	mW
Collector-Emitter Voltage	V _{CEO}	30	V
Emitter-Collector Voltage	V _{ECO}	5	V
Collector Current	I _C	20	mA
Operating Temperature Range	T _{opr}	-25 to + 85	
Storage Temperature Range	T _{stg}	-55 to + 100	
Lead Soldering Temperature [1.6mm (.063") Form Case]	T _{sol}	260 for 5 Seconds	



ELECTRICAL OPTICAL CHARACTERISTICS AT TA=25

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
INPUT LED						
Forward Voltage	V_F		1.2	1.6	V	$I_F = 20\text{mA}$
Reverse Current	I_R			100	μA	$V_R = 5\text{V}$
OUTPUT PHOTOTRANSISTOR						
Collector-Emitter Dark Current	I_{CEO}			100	nA	$V_{CE} = 10\text{V}$
COUPLER						
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$			0.4	V	$I_C = 0.25\text{mA}$ $I_F = 20\text{mA}$
On State Collector Current	$I_{C(ON)}$	0.5			mA	$V_{CE} = 5\text{V}$ $I_F = 20\text{mA}$
Response Time	Rise Time	T_R	3	15	μS	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$ $R_L = 100$
	Fall Time	T_F	4	20		

TYPICAL ELECTRICAL / OPTICAL CHARACTERISTICS CURVES

(25 Ambient Temperature Unless Otherwise Noted)

Fig.1 Power Dissipation vs. Ambient Temperature

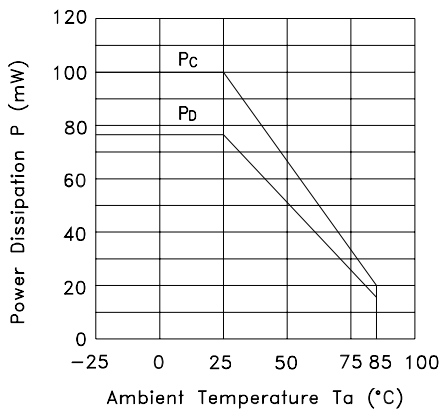


Fig.2 Forward Current vs. Forward Voltage

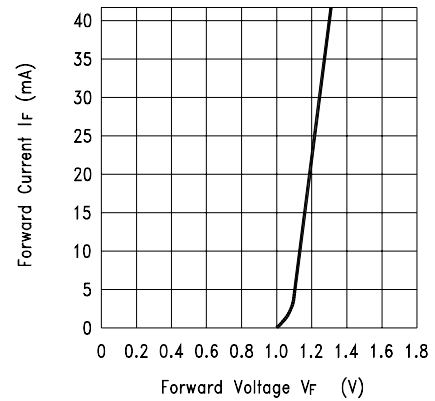


Fig.3 Collector Current vs. Collector-emitter Voltage

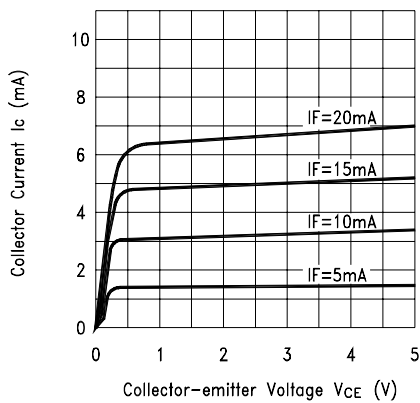
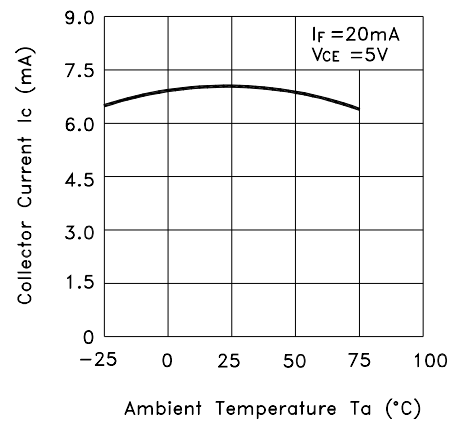


Fig.4 Collector Current vs. Ambient Temperature



TYPICAL ELECTRICAL / OPTICAL CHARACTERISTICS CURVES

(25 Ambient Temperature Unless Otherwise Noted)

Fig.5 Collector-emitter Saturation Voltage vs. Ambient Temperature

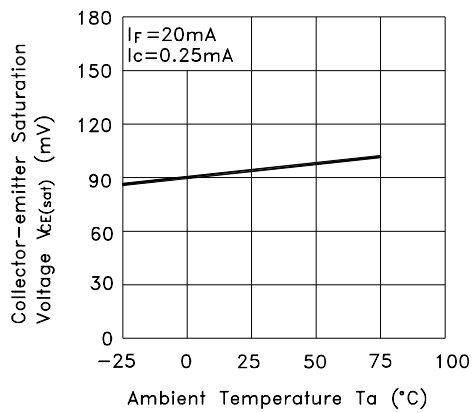
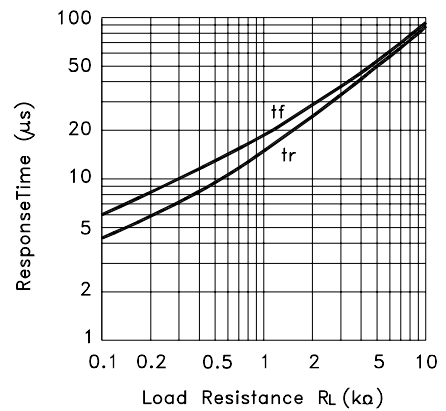


Fig.6 Response Time vs. Load Resistance



Test Circuit for Response Time

