

DESCRIPTION

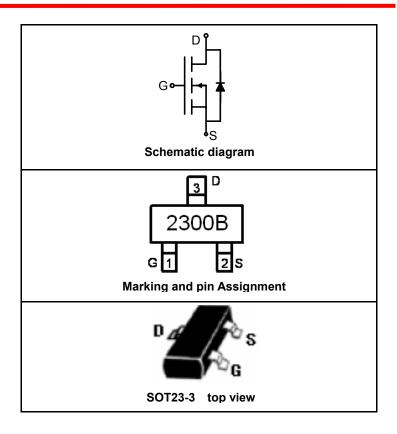
The SSF2300B uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

GENERAL FEATURES

- $V_{DS} = 20V, I_D = 4.5A$ $R_{DS(ON)} < 50m\Omega$ @ $V_{GS} = 2.5V$ $R_{DS(ON)} < 40m\Omega$ @ $V_{GS} = 4.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- Battery protection
- Load switch
- Power management



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2300B	SSF2300B	SOT23-3	Ø180mm	8 mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25 ℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	20	V
Gate-Source Voltage	V _G S	±10	V
Durin Courset Continuous & Courset Duland (Nata 1)	I _D	4.5	Α
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	16	Α
Maximum Power Dissipation	P _D	1.2	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

THERMAL CHARACTERISTICS

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	140	°C/W	I
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20			V



Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V			1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V,V _{DS} =0V			±100	nA
ON CHARACTERISTICS (Note 3)	·					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	0.5	0.7	1.5	V
Drain-Source On-State Resistance	В	V _{GS} =2.5V, I _D =4A		35	50	mΩ
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.5A		28	40	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =4.5A		8		S
DYNAMIC CHARACTERISTICS (Note4)	·					
Input Capacitance	C _{lss}			500		PF
Output Capacitance	C _{oss}	V_{DS} =10V, V_{GS} =0V, F=1.0MHz		250		PF
Reverse Transfer Capacitance	C _{rss}			90		PF
SWITCHING CHARACTERISTICS (Note 4))					
Turn-on Delay Time	t _{d(on)}			7		nS
Turn-on Rise Time	t _r	V_{DD} =10V, R_L = 2.8 Ω		55		nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5V, R_{GEN} =6 Ω , I_D =3.6A,		16		nS
Turn-Off Fall Time	t _f			10		nS
Total Gate Charge	Qg			10		nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =4.2A,V _{GS} =4.5V		2.3		nC
Gate-Drain Charge	Q _{gd}			2.9		nC
DRAIN-SOURCE DIODE CHARACTERIST	ics	1			l	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1.3A			1.2	V

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
 Guaranteed by design, not subject to production testing.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

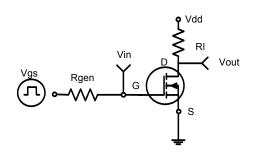
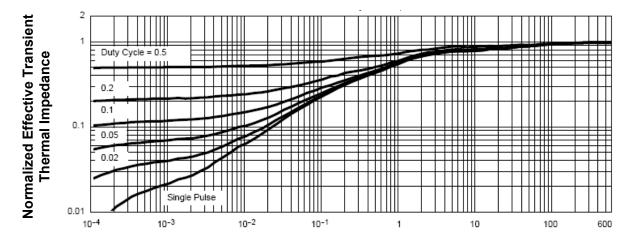


Figure 1: Switching Test Circuit

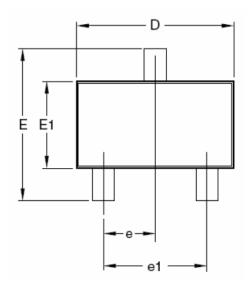
Figure 2:Switching Waveforms

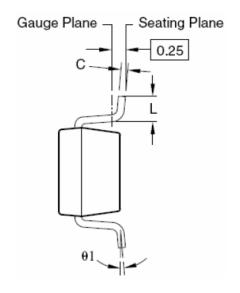


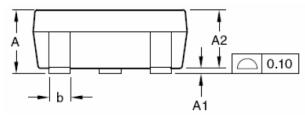
Square Wave Pluse Duration(sec)
Figure 3: Normalized Maximum Transient Thermal Impedance



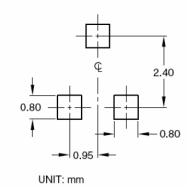
SOT23-3 PACKAGE INFORMATION







RECOMMENDED LAND PATTERN



Dimensions in millimeters

Symbols	Min.	Nom.	Max.		
Α	0.90	_	1.25		
A1	0.00	_	0.13		
A2	0.70	1.00	1.15		
b	0.30	0.40	0.50		
С	0.08	0.13	0.20		
D	2.80	2.90	3.10		
E	2.60	2.80	3.00		
E1	1.40	1.60	1.80		
е	0.95 BSC				
e1	1.90 BSC				
L	0.30	_	0.60		
θ1	O°	5°	8°		

Dimensions in inches

Symbols	Min.	Nom.	Max.		
Α	0.035	_	0.049		
A1	0.000	_	0.005		
A2	0.028	0.039	0.045		
b	0.012	0.016	0.020		
С	0.003	0.005	0.008		
D	0.110	0.114	0.122		
E	0.102	0.110	0.118		
E1	0.055	0.063	0.071		
е	0.037 BSC				
e1	0.075 BSC				
L	0.012	_	0.024		
θ1	0°	5°	8°		

NOTES:

- 1. Tolerance ± 0.10 mm (4 mil) unless otherwise specified
- 2. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- Dimension L is measured in gauge plane.
 Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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